SPEC® CFP2006 Result

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Dell Inc.

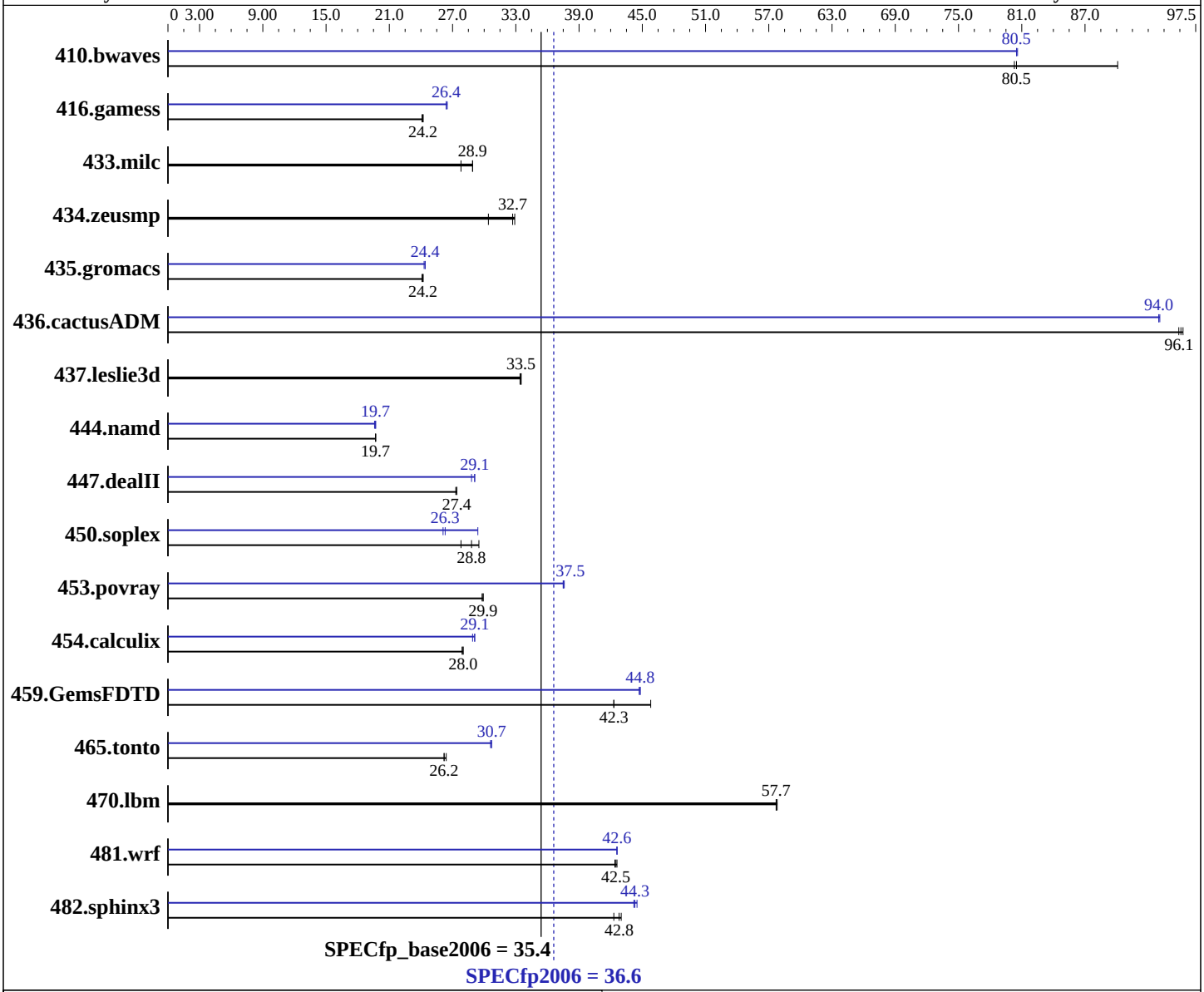
Dell Precision T7500 (Intel Xeon X5570, 2.93 GHz)

SPECfp®2006 = 36.6

SPECfp_base2006 = 35.4

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Mar-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon X5570
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Windows Vista Business SP1 (64-bit)
Compiler:	Intel C++ Compiler for Intel 64, Version 11.0 Build 20090131 Package ID: w_cproc_p_11.0.072 Intel Visual Fortran Compiler for Intel 64, Version 11.0 Build 20090131 Package ID: w_cprof_p_11.0.072 Microsoft Visual Studio 2008 SP1
Auto Parallel:	Yes
File System:	NTFS

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L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6x4 GB DDR3-1333R, ECC, CL9)
Disk Subsystem:	1 x 146 GB SAS 15000 RPM
Other Hardware:	None

System State:	Default
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1 for x64

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	169	80.3	**169**	**80.5**	151	90.1	169	80.5	**169**	**80.5**	169	80.6
416.gamess	**810**	**24.2**	809	24.2	811	24.1	741	26.4	**741**	**26.4**	740	26.5
433.milc	**318**	**28.9**	318	28.9	330	27.8	**318**	**28.9**	318	28.9	330	27.8
434.zeusmp	**278**	**32.7**	277	32.9	299	30.4	**278**	**32.7**	277	32.9	299	30.4
435.gromacs	296	24.1	295	24.2	**295**	**24.2**	293	24.3	293	24.4	**293**	**24.4**
436.cactusADM	124	96.3	125	95.9	**124**	**96.1**	127	94.1	**127**	**94.0**	127	94.0
437.leslie3d	281	33.5	**281**	**33.5**	281	33.4	281	33.5	**281**	**33.5**	281	33.4
444.namd	406	19.7	**407**	**19.7**	407	19.7	408	19.6	407	19.7	**408**	**19.7**
447.dealII	**418**	**27.4**	417	27.4	419	27.3	**394**	**29.1**	398	28.8	394	29.1
450.soplex	283	29.5	**290**	**28.8**	301	27.8	**318**	**26.3**	320	26.1	283	29.4
453.povray	178	29.8	178	29.9	**178**	**29.9**	**142**	**37.5**	142	37.5	142	37.6
454.calculix	295	27.9	295	28.0	**295**	**28.0**	286	28.9	283	29.1	**284**	**29.1**
459.GemsFDTD	**251**	**42.3**	232	45.8	251	42.3	237	44.7	237	44.8	**237**	**44.8**
465.tonto	376	26.2	**375**	**26.2**	373	26.4	320	30.7	321	30.6	**321**	**30.7**
470.lbm	**238**	**57.7**	238	57.7	238	57.8	**238**	**57.7**	238	57.7	238	57.8
481.wrf	262	42.6	264	42.4	**263**	**42.5**	262	42.6	**262**	**42.6**	263	42.6
482.sphinx3	453	43.0	**455**	**42.8**	461	42.3	441	44.2	438	44.5	**440**	**44.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
BIOS Settings
  Memory Node Interleaving set to NUMA.
```

General Notes

```
Binaries were built on Windows Vista Business (64-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 /assume:underscore
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qparallel
-Qopt-prefetch /F512000000

C++ benchmarks:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qparallel
-Qopt-prefetch -Qcxx_features /F512000000 shlw64mt.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qparallel
-Qopt-prefetch /F1000000000

Benchmarks using both Fortran and C:
-QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qparallel
-Qopt-prefetch /F1000000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qunroll2 /F512000000

C++ benchmarks:

444.namd: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Oa /F512000000 shlw64mt.lib -link /FORCE:MULTIPLE

447.dealII: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qansi-alias -Qscalar-rep- /F512000000 shlw64mt.lib -link /FORCE:MULTIPLE

450.soplex: -Qprof_gen(pass 1) -QxSSE4.2 -Qauto-ilp32 -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F512000000 shlw64mt.lib -link /FORCE:MULTIPLE

453.povray: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias /F512000000 shlw64mt.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: -QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel /F1000000000

416.gamess: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qopt-prefetch -Qparallel /F1000000000

465.tonto: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

436.cactusADM: -Qprof_gen(pass 1) -QxSSE4.2(pass 2) -Qauto-ilp32(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel /F1000000000

454.calculix: -QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- /F1000000000

481.wrf: -QxSSE4.2 -Qauto-ilp32 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel /F1000000000

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell.ic11.0.windows.flags.20090710.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell.ic11.0.windows.flags.20090710.xml

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