



SPEC® CINT2006 Result

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NEC Corporation

Express5800/R140a-4
(Intel Xeon E7430)

SPECint_rate2006 = 99.7

SPECint_rate_base2006 = 93.9

CPU2006 license: 9006

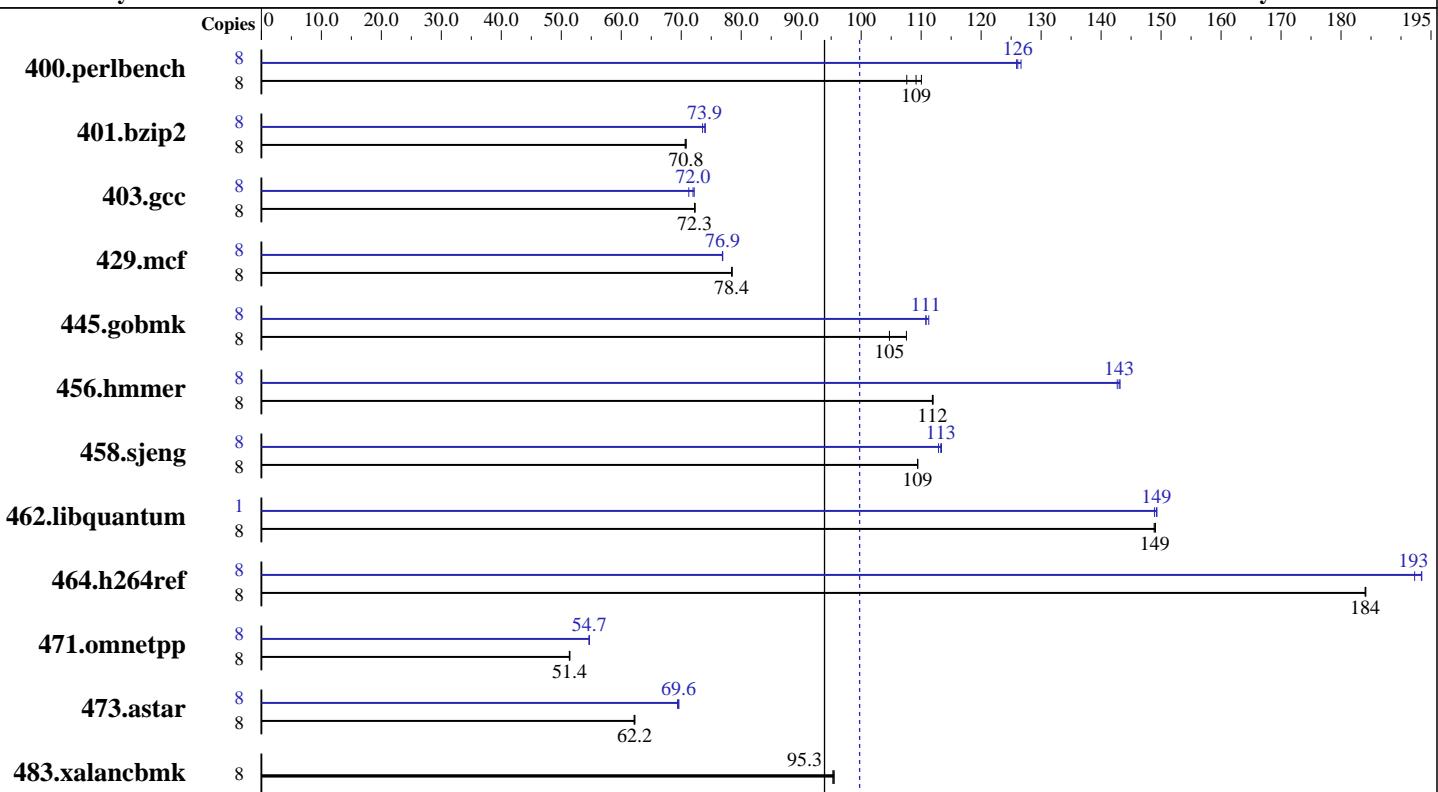
Test sponsor: NEC Corporation

Tested by: Bull SAS

Test date: Feb-2009

Hardware Availability: Nov-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E7430
CPU Characteristics: 1066 MHz system bus
CPU MHz: 2133
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2,3,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (16 x 2GB DDR2-667 FBDIMM)
Disk Subsystem: 1x146 GB SAS, 10000 RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel: Yes
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	710	110	716	109	726	108	8	617	127	621	126	620	126
401.bzip2	8	1093	70.7	1091	70.8	1090	70.8	8	1043	74.0	1044	73.9	1050	73.6
403.gcc	8	892	72.2	891	72.3	891	72.3	8	892	72.2	895	72.0	904	71.3
429.mcf	8	929	78.5	931	78.4	930	78.4	8	949	76.9	949	76.9	949	76.9
445.gobmk	8	801	105	801	105	780	108	8	757	111	757	111	754	111
456.hammer	8	667	112	666	112	667	112	8	522	143	521	143	523	143
458.sjeng	8	885	109	885	109	885	109	8	854	113	855	113	857	113
462.libquantum	8	1114	149	1113	149	1112	149	1	139	149	139	149	139	149
464.h264ref	8	961	184	962	184	962	184	8	915	193	915	193	921	192
471.omnetpp	8	973	51.4	972	51.4	974	51.3	8	914	54.7	915	54.6	914	54.7
473.astar	8	902	62.2	902	62.3	904	62.1	8	807	69.6	809	69.4	807	69.6
483.xalancbmk	8	578	95.5	579	95.3	579	95.3	8	578	95.5	579	95.3	579	95.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except
for 462.libquantum peak

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Platform Notes

BIOS Settings:
Adjacent Cache Line Prefetch = Disabled
Hardware Prefetcher = Disabled

General Notes

The NEC Express5800/R140a-4(Intel Xeon E7430) and
the Bull NovaScale R480 E1(Intel Xeon E7430, 2.13 GHz) models are electronically equivalent.
The results have been measured on a Bull NovaScale R480 E1(Intel Xeon E7430, 2.13 GHz) model.



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmr: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll12
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll12 -ansi-alias

C++ benchmarks:

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Peak Optimization Flags (Continued)

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.00.html>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.00.xml>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.xml>

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For other inquiries, please contact webmaster@spec.org.

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