SPEC® CFP2006 Result

NEC Corporation

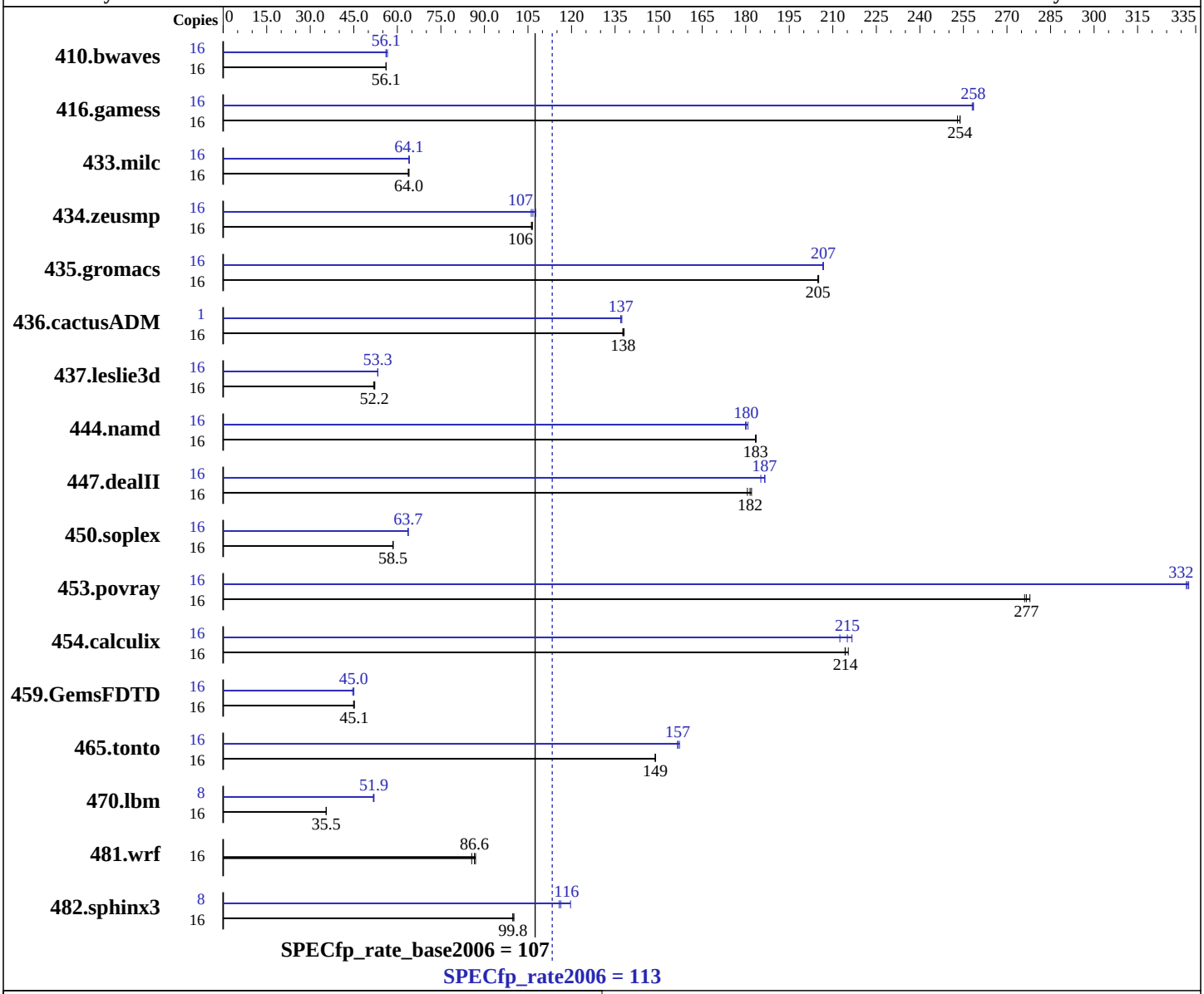
Express5800/R140a-4
(Intel Xeon E7430)

SPECfp®_rate2006 = 113

SPECfp_rate_base2006 = 107

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: Bull SAS

Test date: Feb-2009
Hardware Availability: Nov-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon E7430
CPU Characteristics:	1066 MHz system bus
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip
CPU(s) orderable:	1,2,3,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip, 3 MB shared / 2 cores

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (16 x 2GB DDR2-667 FBDIMM)
Disk Subsystem: 1x146 GB SAS, 10000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	3875	56.1	**3874**	**56.1**	3873	56.1	16	3874	56.1	3841	56.6	**3874**	**56.1**
416.gamess	16	**1234**	**254**	1234	254	1238	253	16	1214	258	1212	258	**1212**	**258**
433.milc	16	2304	63.7	2295	64.0	**2296**	**64.0**	16	2295	64.0	2291	64.1	**2292**	**64.1**
434.zeusmp	16	1371	106	1367	107	**1369**	**106**	16	**1364**	**107**	1372	106	1353	108
435.gromacs	16	557	205	**557**	**205**	558	205	16	553	207	553	207	**553**	**207**
436.cactusADM	16	1389	138	**1387**	**138**	1384	138	1	87.0	137	**87.2**	**137**	87.2	137
437.leslie3d	16	2902	51.8	**2883**	**52.2**	2878	52.3	16	**2823**	**53.3**	2821	53.3	2825	53.2
444.namd	16	**699**	**183**	699	184	700	183	16	713	180	710	181	**712**	**180**
447.dealII	16	1006	182	1014	181	**1008**	**182**	16	981	187	**981**	**187**	988	185
450.soplex	16	2279	58.5	**2280**	**58.5**	2281	58.5	16	2095	63.7	**2094**	**63.7**	2093	63.7
453.povray	16	306	278	308	276	**308**	**277**	16	257	332	256	333	**256**	**332**
454.calculix	16	616	214	**616**	**214**	613	215	16	609	217	**614**	**215**	621	212
459.GemsFDTD	16	3778	44.9	3758	45.2	**3762**	**45.1**	16	3804	44.6	**3776**	**45.0**	3770	45.0
465.tonto	16	1058	149	1057	149	**1058**	**149**	16	1006	157	1002	157	**1005**	**157**
470.lbm	16	6199	35.5	6187	35.5	**6189**	**35.5**	8	2125	51.7	**2117**	**51.9**	2116	51.9
481.wrf	16	**2065**	**86.6**	2087	85.6	2055	87.0	16	**2065**	**86.6**	2087	85.6	2055	87.0
482.sphinx3	16	**3124**	**99.8**	3113	100	3125	99.8	8	1347	116	**1341**	**116**	1303	120

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores except
 for 436.cactusADM peak
 For peak modules using 1/2 the number of available cores, copies
 were each assigned to a single L2 cache using mysubmit.pl script.
 See the flags description file for mysubmit.pl details.
```

Operating System Notes

```
 'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to physical,0
 KMP_STACKSIZE set to 64M
```

Platform Notes

```
BIOS Settings:
Adjacent Cache Line Prefetch = Disabled
Hardware Prefetcher = Disabled
High Bandwidth option = Enabled
```

General Notes

```
The NEC Express5800/R140a-4(Intel Xeon E7430) and
the Bull NovaScale R480 E1(Intel Xeon E7430, 2.13 GHz) models are electronically equivalent.
The results have been measured on a Bull NovaScale R480 E1(Intel Xeon E7430, 2.13 GHz) model.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

C++ benchmarks (except as noted below):
icpc

450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Fortran benchmarks (except as noted below):
ifort

437.leslie3d: /opt/intel/Compiler/11.0/042/bin/ia32/ifort
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main

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Peak Portability Flags (Continued)

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static

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Peak Optimization Flags (Continued)

437.leslie3d: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch`

459.GemsFDTD: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch`

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32`

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32`

454.calculix: `-xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.00.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.00.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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