



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 189

PRIMERGY RX200 S5, Intel Xeon X5550, 2.66 GHz

SPECfp_rate_base2006 = 181

CPU2006 license: 19

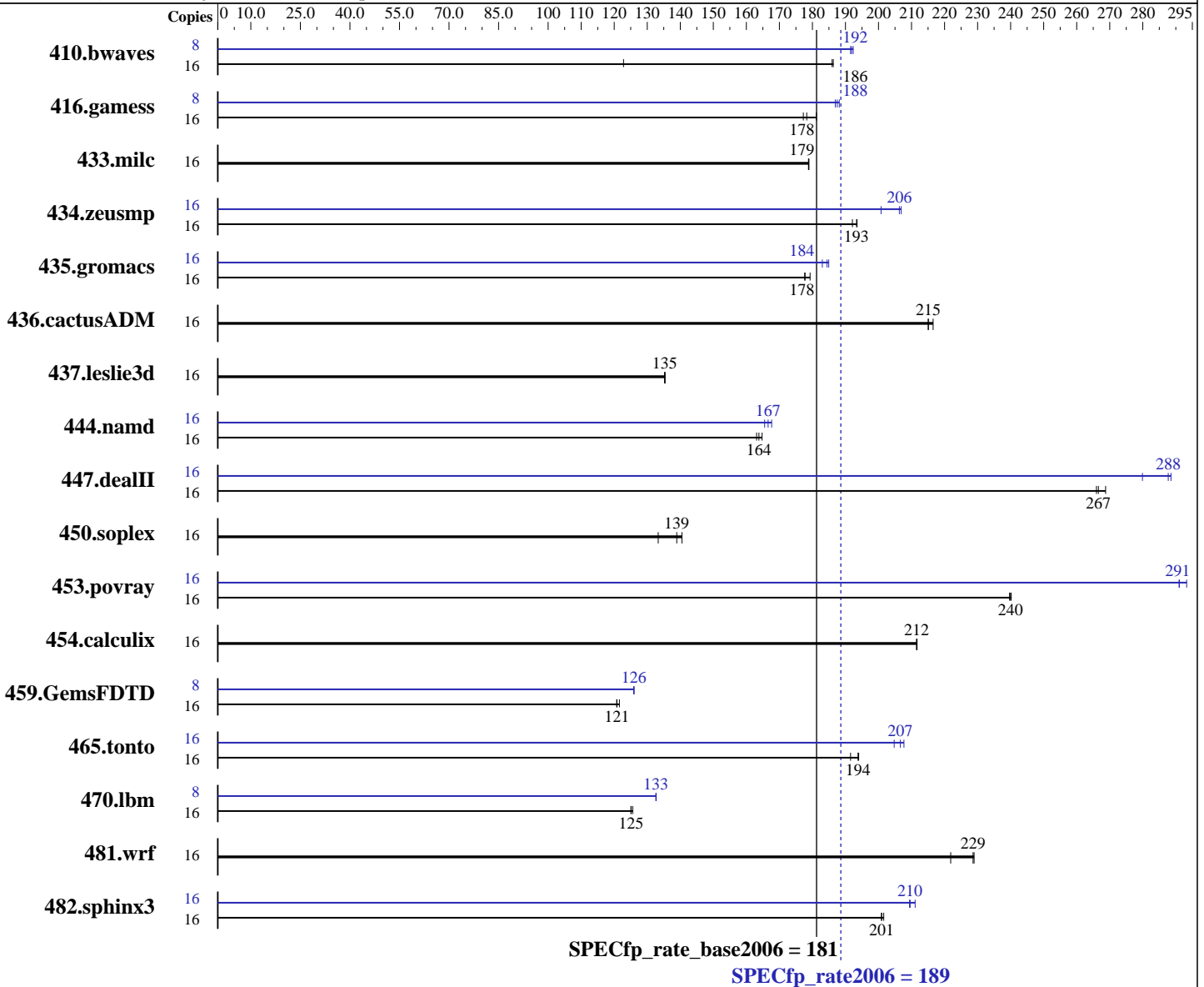
Test sponsor: Fujitsu

Tested by: Fujitsu Siemens Computers

Test date: Mar-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5550
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smpp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
 Auto Parallel: No
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 64-bit

Continued on next page



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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem: 1 x SAS, 73 GB, 10000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1770	123	1167	186	<u>1169</u>	<u>186</u>	8	568	192	<u>567</u>	<u>192</u>	565	192
416.gamess	16	1729	181	<u>1757</u>	<u>178</u>	1767	177	8	833	188	<u>835</u>	<u>188</u>	838	187
433.milc	16	<u>821</u>	<u>179</u>	821	179	821	179	16	<u>821</u>	<u>179</u>	821	179	821	179
434.zeusmp	16	<u>753</u>	<u>193</u>	752	194	758	192	16	<u>706</u>	<u>206</u>	704	207	725	201
435.gromacs	16	637	179	643	178	<u>643</u>	<u>178</u>	16	618	185	<u>619</u>	<u>184</u>	624	183
436.cactusADM	16	<u>889</u>	<u>215</u>	883	217	889	215	16	<u>889</u>	<u>215</u>	883	217	889	215
437.leslie3d	16	1112	135	<u>1112</u>	<u>135</u>	1111	135	16	1112	135	<u>1112</u>	<u>135</u>	1111	135
444.namd	16	779	165	787	163	<u>783</u>	<u>164</u>	16	<u>770</u>	<u>167</u>	765	168	775	166
447.dealII	16	688	266	681	269	<u>687</u>	<u>267</u>	16	634	289	<u>636</u>	<u>288</u>	654	280
450.soplex	16	1001	133	<u>960</u>	<u>139</u>	950	141	16	1001	133	<u>960</u>	<u>139</u>	950	141
453.povray	16	354	240	<u>355</u>	<u>240</u>	355	240	16	293	291	<u>292</u>	<u>291</u>	290	293
454.calculix	16	624	212	<u>624</u>	<u>212</u>	624	212	16	624	212	<u>624</u>	<u>212</u>	624	212
459.GemsFDTD	16	1396	122	<u>1405</u>	<u>121</u>	1406	121	8	<u>674</u>	<u>126</u>	674	126	674	126
465.tonto	16	<u>812</u>	<u>194</u>	812	194	822	192	16	758	208	<u>762</u>	<u>207</u>	769	205
470.lbm	16	<u>1757</u>	<u>125</u>	1758	125	1750	126	8	829	133	<u>829</u>	<u>133</u>	828	133
481.wrf	16	781	229	806	222	<u>782</u>	<u>229</u>	16	781	229	806	222	<u>782</u>	<u>229</u>
482.sphinx3	16	<u>1552</u>	<u>201</u>	1547	202	1552	201	16	<u>1488</u>	<u>210</u>	1489	209	1477	211

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>



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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.xml>

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