



# SPEC® CINT2006 Result

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## Acer Incorporated

SPECint®\_rate2006 = 47.8

## Acer Altos R520 (Intel Xeon X5270)

SPECint\_rate\_base2006 = 44.8

CPU2006 license: 97

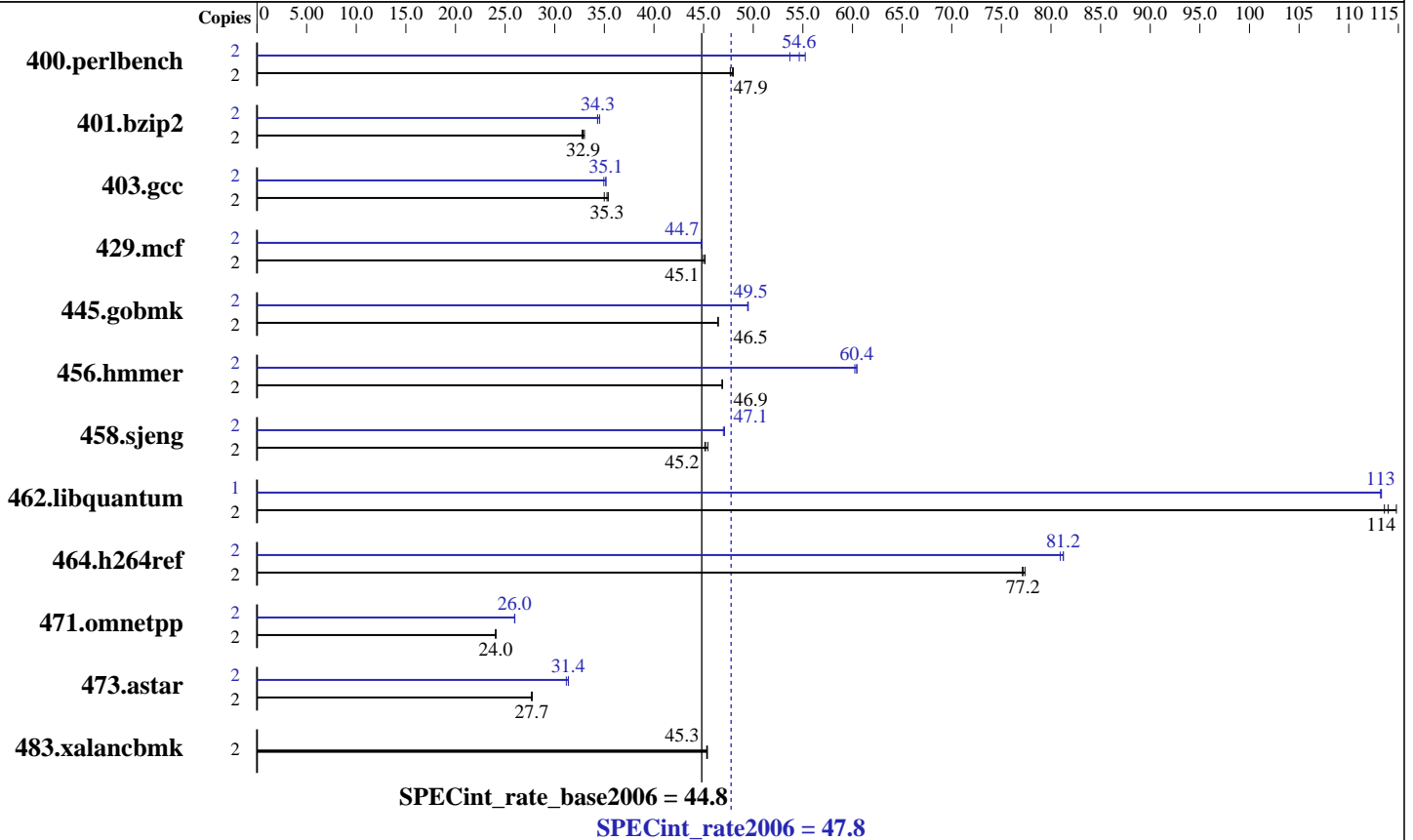
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Dec-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon X5270  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 3500  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)  
 Disk Subsystem: 1 x 73GB 10K RPM SAS HDD  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	410	47.7	<b>408</b>	<b>47.9</b>	407	48.0	2	364	53.7	<b>358</b>	<b>54.6</b>	354	55.2
401.bzip2	2	589	32.8	<b>587</b>	<b>32.9</b>	585	33.0	2	563	34.3	559	34.5	<b>562</b>	<b>34.3</b>
403.gcc	2	460	35.0	<b>456</b>	<b>35.3</b>	455	35.4	2	<b>458</b>	<b>35.1</b>	458	35.2	461	34.9
429.mcf	2	<b>404</b>	<b>45.1</b>	404	45.1	405	45.0	2	408	44.7	407	44.8	<b>408</b>	<b>44.7</b>
445.gobmk	2	452	46.5	<b>452</b>	<b>46.5</b>	452	46.4	2	424	49.5	<b>424</b>	<b>49.5</b>	424	49.4
456.hammer	2	398	46.9	<b>398</b>	<b>46.9</b>	398	46.9	2	<b>309</b>	<b>60.4</b>	310	60.2	309	60.4
458.sjeng	2	536	45.1	<b>535</b>	<b>45.2</b>	533	45.4	2	514	47.1	515	47.0	<b>514</b>	<b>47.1</b>
462.libquantum	2	365	114	<b>364</b>	<b>114</b>	361	115	1	183	113	183	113	<b>183</b>	<b>113</b>
464.h264ref	2	574	77.1	572	77.4	<b>573</b>	<b>77.2</b>	2	545	81.3	<b>545</b>	<b>81.2</b>	547	80.9
471.omnetpp	2	<b>520</b>	<b>24.0</b>	520	24.1	520	24.0	2	481	26.0	482	26.0	<b>481</b>	<b>26.0</b>
473.astar	2	<b>507</b>	<b>27.7</b>	508	27.7	506	27.7	2	448	31.4	<b>448</b>	<b>31.4</b>	451	31.2
483.xalancbmk	2	304	45.4	<b>304</b>	<b>45.3</b>	305	45.3	2	304	45.4	<b>304</b>	<b>45.3</b>	305	45.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset was used to bind processes to cores except  
for 462.libquantum peak

## Platform Notes

BIOS Settings  
High Bandwidth Optimization set to ON

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to "physical,0"  
KMP\_STACKSIZE set to 64M  
'ulimit -s unlimited' was set for stacksize unlimited  
This result was measured on the Acer Altos R520.  
The Altos R720 and Altos R520 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -parallel -par-runtime-control  
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.04.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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