SPEC® CINT2006 Result
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ACTION S.A.

ACTINA SOLAR 212 X2 (Intel Xeon E5420, 2.5 GHz)

$SPECint®\_rate2006 = \quad 118$

$SPECint\_rate\_base2006 = \quad 108$

CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Dec-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

Benchmark	Copies	Result
400.perlbench	8	159
400.perlbench	8	138
401.bzip2	8	99.1
401.bzip2	8	95.1
403.gcc	8	79.0
403.gcc	8	79.3
429.mcf	8	88.0
429.mcf	8	88.2
445.gobmk	8	139
445.gobmk	8	131
456.hmmer	8	171
456.hmmer	8	133
458.sjeng	8	138
458.sjeng	8	132
462.libquantum	1	228
462.libquantum	8	167
464.h264ref	8	232
464.h264ref	8	221
471.omnetpp	8	45.2
471.omnetpp	8	44.4
473.astar	8	73.9
473.astar	8	66.8
483.xalancbmk	8	110

SPECint_rate_base2006 = 108

SPECint_rate2006 = 118

Hardware

CPU Name:	Intel Xeon E5420
CPU Characteristics:	1333 MHz System Bus
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x 2 GB, PC2-5300, CL 5-5-5, FB ECC)
Disk Subsystem:	500 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux Enterprise Server 10 (x86_64) with SP2, kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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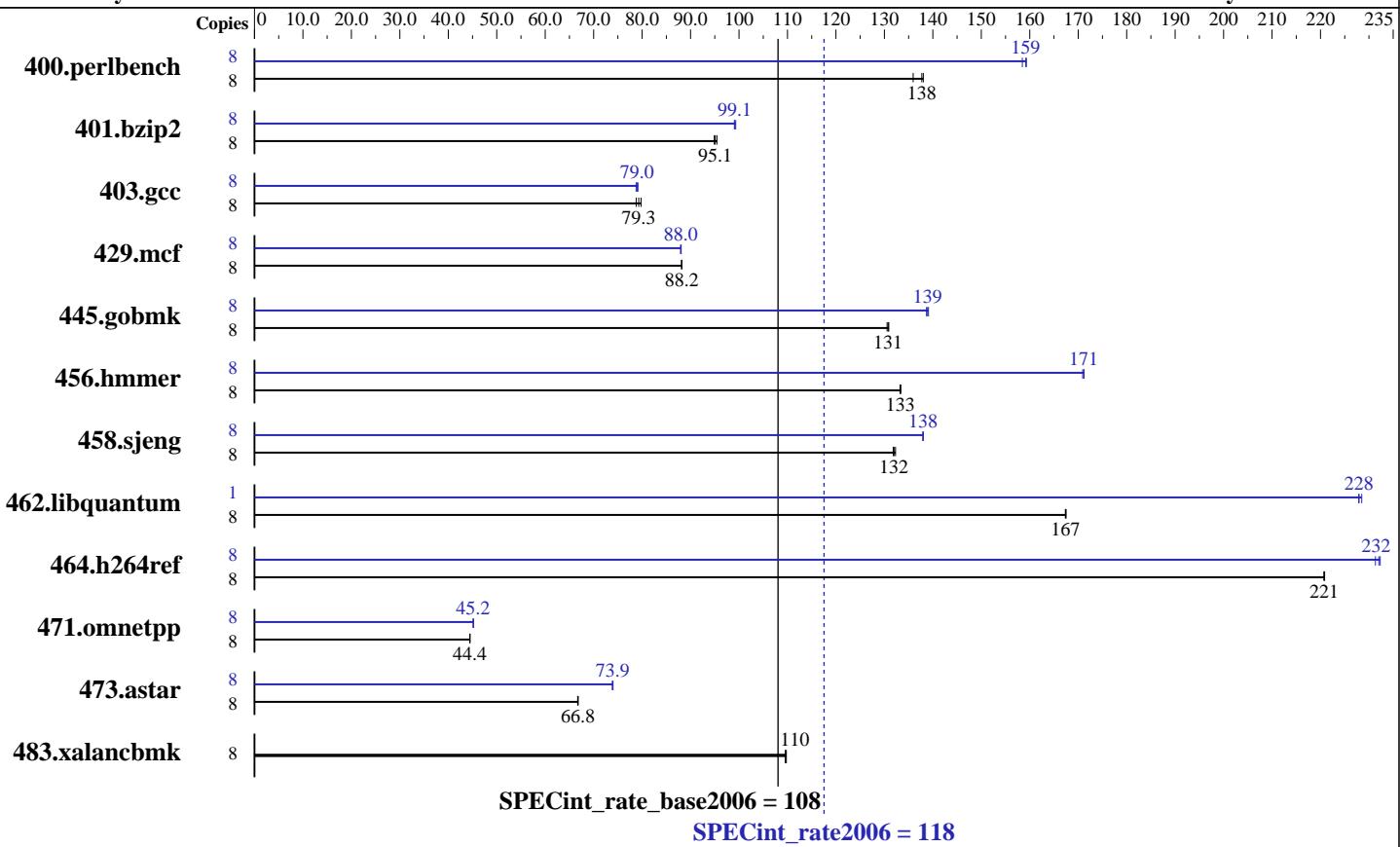
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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	575	136	566	138	**568**	**138**	8	493	158	491	159	**491**	**159**
401.bzip2	8	809	95.4	**812**	**95.1**	814	94.9	8	**779**	**99.1**	777	99.3	779	99.0
403.gcc	8	807	79.8	**812**	**79.3**	818	78.8	8	814	79.1	818	78.8	**815**	**79.0**
429.mcf	8	828	88.2	827	88.2	**827**	**88.2**	8	**829**	**88.0**	828	88.1	829	88.0
445.gobmk	8	643	131	641	131	**642**	**131**	8	603	139	**604**	**139**	605	139
456.hmmer	8	**560**	**133**	560	133	560	133	8	436	171	**436**	**171**	437	171
458.sjeng	8	**733**	**132**	734	132	732	132	8	702	138	**702**	**138**	701	138
462.libquantum	8	**990**	**167**	990	168	990	167	1	**90.9**	**228**	90.9	228	90.7	229
464.h264ref	8	802	221	802	221	**802**	**221**	8	762	232	**763**	**232**	765	231
471.omnetpp	8	**1125**	**44.4**	1124	44.5	1125	44.4	8	**1107**	**45.2**	1107	45.2	1107	45.1
473.astar	8	**841**	**66.8**	842	66.7	841	66.8	8	**760**	**73.9**	760	73.9	759	74.0
483.xalancbmk	8	503	110	**504**	**110**	504	110	8	503	110	**504**	**110**	504	110

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Platform Notes

This result is measured on ACTINA SOLAR 222 X2.
Note that the ACTINA SOLAR 222 X2 and ACTINA SOLAR 212 X2
are electrically equivalent.

General Notes

'taskset' was used to bind processes to cores except
for 462.libquantum peak
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

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Tested by: ACTION S.A.	**Software Availability:** Nov-2008

Peak Optimization Flags

C benchmarks:

```
   400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                  -no-prec-div -static -ansi-alias -opt-prefetch

        401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                  -no-prec-div -static -opt-prefetch -ansi-alias

          403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
                  -opt-malloc-options=3

          429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                  -no-prec-div -static -opt-prefetch

        445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
                  -no-prec-div -ansi-alias

        456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
                  -ansi-alias

        458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                  -no-prec-div -static -unroll4

    462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
                  -opt-malloc-options=3 -parallel -par-runtime-control
                  -opt-prefetch

       464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                  -no-prec-div -static -unroll2 -ansi-alias
```

C++ benchmarks:

```
      471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                  -no-prec-div -ansi-alias -opt-ra-region-strategy=block
                  -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

        473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                  -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
                  -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

     483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
          403.gcc: -Dalloca=_alloca
```

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The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.00.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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