



SPEC® CFP2006 Result

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NTT System S. A.
NTT Business W 986G

SPECfp®2006 = 22.3
SPECfp_base2006 = 21.5

CPU2006 license: 9013

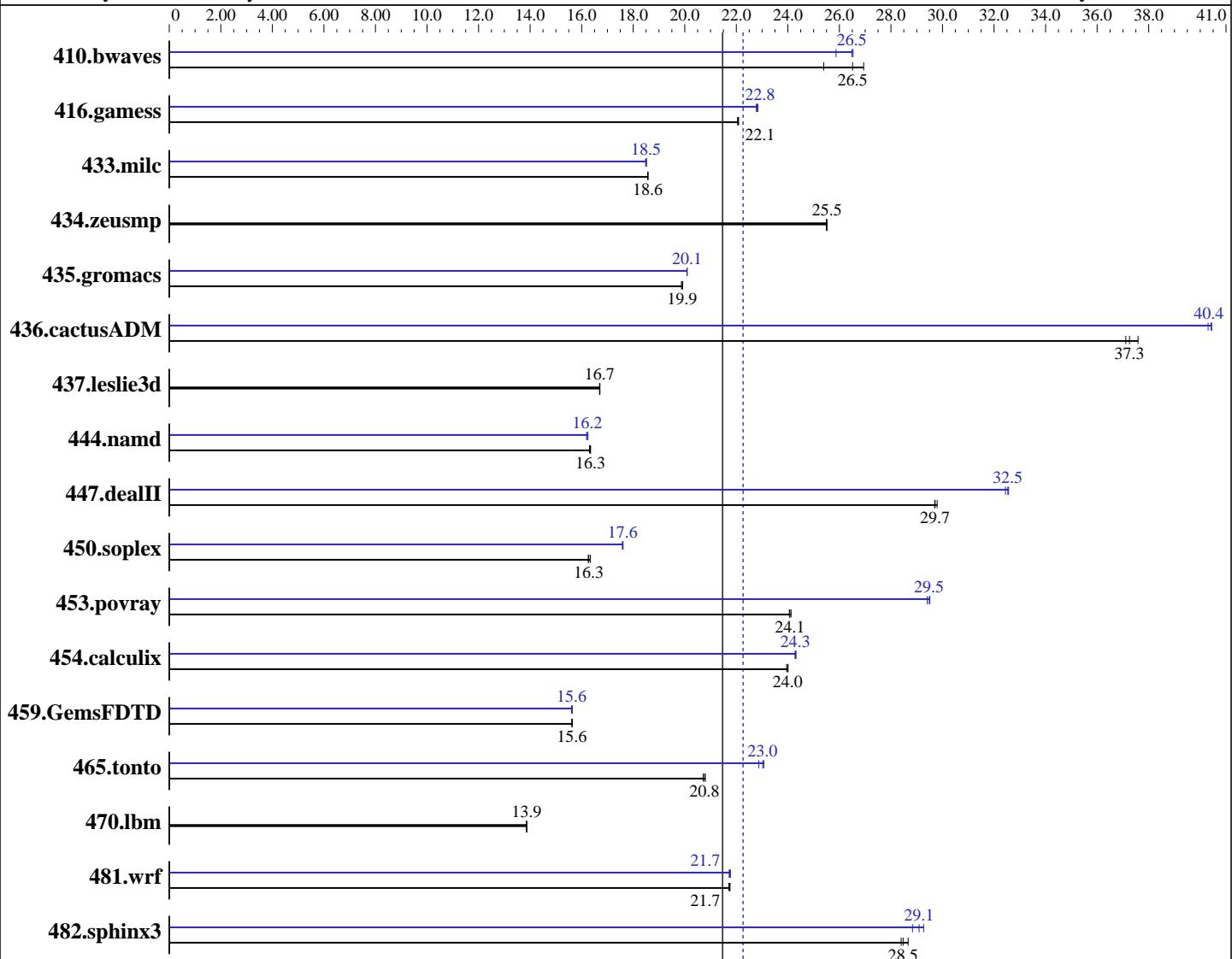
Test sponsor: NTT System S. A.

Tested by: NTT System S. A.

Test date: Nov-2008

Hardware Availability: Nov-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core 2 Duo E8400
CPU Characteristics:
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 6 MB I+D on chip per chip

Software

Operating System: SuSe Linux SLES10 SP1, Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066, l_cprof_p_11.0.066
Auto Parallel: Yes
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit

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L3 Cache: None
Other Cache: None
Memory: 2 GB (2x1GB)
Disk Subsystem: 250 GB SATA, 7200RPM
Other Hardware: None

Other Software: Microquill SmartHeap V8.1
Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	535	25.4	504	26.9	513	26.5	512	26.5	513	26.5	525	25.9
416.gamess	887	22.1	887	22.1	888	22.0	857	22.8	858	22.8	860	22.8
433.milc	495	18.6	494	18.6	494	18.6	496	18.5	497	18.5	496	18.5
434.zeusmp	357	25.5	357	25.5	357	25.5	357	25.5	357	25.5	357	25.5
435.gromacs	359	19.9	358	19.9	359	19.9	355	20.1	355	20.1	355	20.1
436.cactusADM	321	37.3	318	37.6	322	37.1	296	40.4	296	40.3	295	40.4
437.leslie3d	563	16.7	563	16.7	563	16.7	563	16.7	563	16.7	563	16.7
444.namd	491	16.3	491	16.3	492	16.3	494	16.2	494	16.2	495	16.2
447.dealII	385	29.7	384	29.8	385	29.7	351	32.6	352	32.5	353	32.4
450.soplex	512	16.3	510	16.3	513	16.3	474	17.6	474	17.6	474	17.6
453.povray	221	24.1	221	24.1	221	24.1	180	29.5	181	29.4	180	29.5
454.calculix	344	24.0	344	24.0	344	24.0	340	24.3	339	24.3	340	24.3
459.GemsFDTD	679	15.6	679	15.6	679	15.6	680	15.6	679	15.6	680	15.6
465.tonto	474	20.8	475	20.7	473	20.8	427	23.0	430	22.9	427	23.1
470.lbm	991	13.9	991	13.9	991	13.9	991	13.9	991	13.9	991	13.9
481.wrf	514	21.7	513	21.8	515	21.7	514	21.7	513	21.8	514	21.7
482.sphinx3	680	28.7	686	28.4	685	28.5	676	28.8	666	29.3	670	29.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort



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Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Fortran benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Peak Compiler Invocation

C benchmarks:

```
icc
```

C++ benchmarks:

```
icpc
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icc ifort
```



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Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
    -no-prec-div -static -fno-alias

470.lbm: basepeak = yes

482.sphinx3: -m32 -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll12

```

C++ benchmarks:

```

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
    -no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
    -no-prec-div -static -unroll12 -ansi-alias -scalar-rep-
    -opt-prefetch

450.soplex: -m32 -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo
    -O3 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
    -no-prec-div -static -unroll14 -ansi-alias

```

Fortran benchmarks:

```
410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
    -parallel
```

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Peak Optimization Flags (Continued)

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch
-parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.html>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.06.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.06.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Tue Jul 22 22:47:49 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 12 January 2009.