



SPEC® CFP2006 Result

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Bull SAS

NovaScale R480 E1
(Intel Xeon E7420, 2.13 GHz)

SPECfp®_rate2006 = 55.7

SPECfp_rate_base2006 = 53.4

CPU2006 license: 20

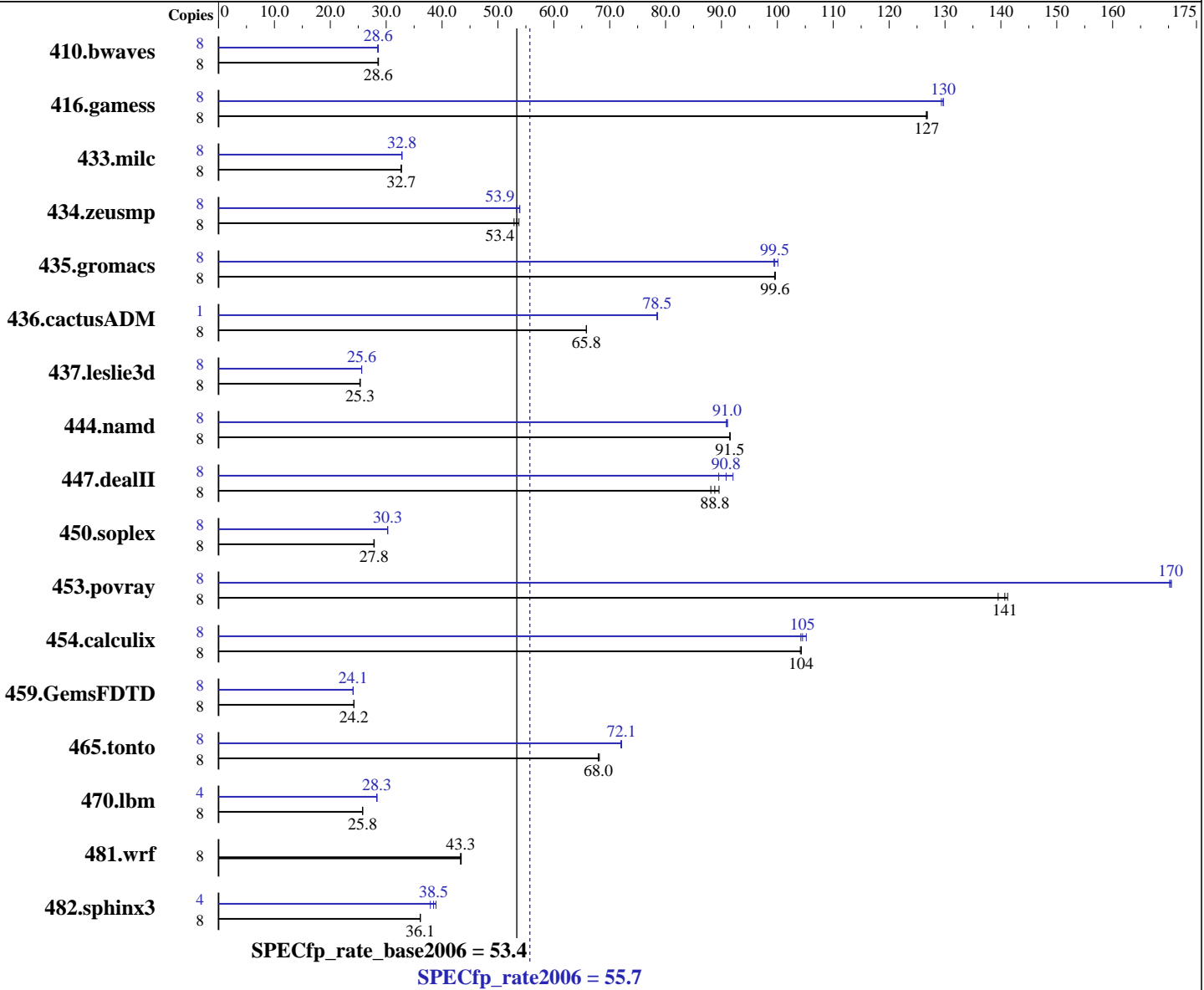
Test sponsor: Bull SAS

Tested by: NEC Corporation

Test date: Nov-2008

Hardware Availability: Nov-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E7420
 CPU Characteristics: 1066 MHz system bus
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2,3,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-sm-p
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.044, l_cprof_b_11.0.044
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (16x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x73.2 GB SAS, 15000RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3810	28.5	3807	28.6	3798	28.6	8	3804	28.6	3801	28.6	3819	28.5
416.gamess	8	1236	127	1235	127	1237	127	8	1208	130	1208	130	1211	129
433.milc	8	2244	32.7	2245	32.7	2248	32.7	8	2239	32.8	2237	32.8	2238	32.8
434.zeusmp	8	1377	52.9	1354	53.8	1364	53.4	8	1352	53.9	1350	53.9	1365	53.3
435.gromacs	8	574	99.5	573	99.6	573	99.6	8	571	100	574	99.5	575	99.4
436.cactusADM	8	1453	65.8	1452	65.8	1452	65.8	1	152	78.5	152	78.5	152	78.6
437.leslie3d	8	2964	25.4	2974	25.3	2969	25.3	8	2937	25.6	2936	25.6	2935	25.6
444.namd	8	701	91.5	701	91.5	701	91.6	8	705	91.1	706	90.9	705	91.0
447.dealII	8	1031	88.8	1022	89.6	1039	88.1	8	994	92.1	1008	90.8	1022	89.5
450.soplex	8	2394	27.9	2398	27.8	2400	27.8	8	2205	30.3	2204	30.3	2205	30.3
453.povray	8	302	141	305	140	301	141	8	250	170	250	170	250	171
454.calculix	8	633	104	633	104	634	104	8	628	105	633	104	632	105
459.GemsFDTD	8	3505	24.2	3500	24.3	3504	24.2	8	3522	24.1	3526	24.1	3526	24.1
465.tonto	8	1158	68.0	1158	68.0	1155	68.2	8	1092	72.1	1091	72.1	1093	72.0
470.lbm	8	4263	25.8	4263	25.8	4263	25.8	4	1941	28.3	1940	28.3	1939	28.3
481.wrf	8	2062	43.3	2057	43.4	2063	43.3	8	2062	43.3	2057	43.4	2063	43.3
482.sphinx3	8	4318	36.1	4320	36.1	4318	36.1	4	2026	38.5	2057	37.9	2004	38.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except for 436.cactusADM peak
For peak modules using 1/2 the number of available cores, copies were each assigned to a single L2 cache using mysubmit.pl script.
See the flags description file for mysubmit.pl details.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M



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Platform Notes

Bios settings:
Hardware Prefetcher: Disabled
Adjacent Cache Line Prefetch: Disabled
FSB High Bandwidth Optimization: Enabled

General Notes

The NEC Express5800/R140a-4(Intel Xeon E7420) and the Bull NovaScale R480 E1(Intel Xeon E7420, 2.13 GHz) models are electronically equivalent. The results have been measured on a NEC Express5800/R140a-4(Intel Xeon E7420) model.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

482.sphinx3: /opt/intel/Compiler/11.0/044/bin/ia32/icc
-L/opt/intel/Compiler/11.0/044/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/044/ipp/ia32/include

C++ benchmarks (except as noted below):
icpc

450.soplex: /opt/intel/Compiler/11.0/044/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/044/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/044/ipp/ia32/include

Fortran benchmarks (except as noted below):
ifort

437.leslie3d: /opt/intel/Compiler/11.0/044/bin/ia32/ifort
-L/opt/intel/Compiler/11.0/044/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/044/ipp/ia32/include

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main

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Peak Portability Flags (Continued)

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias
470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32
482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32
447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-
450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3
453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-
434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static

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Peak Optimization Flags (Continued)

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml>

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For other inquiries, please contact webmaster@spec.org.

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