SPEC® CFP2006 Result

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YOYOtech
(Test Sponsor: Future Publishing Ltd.)

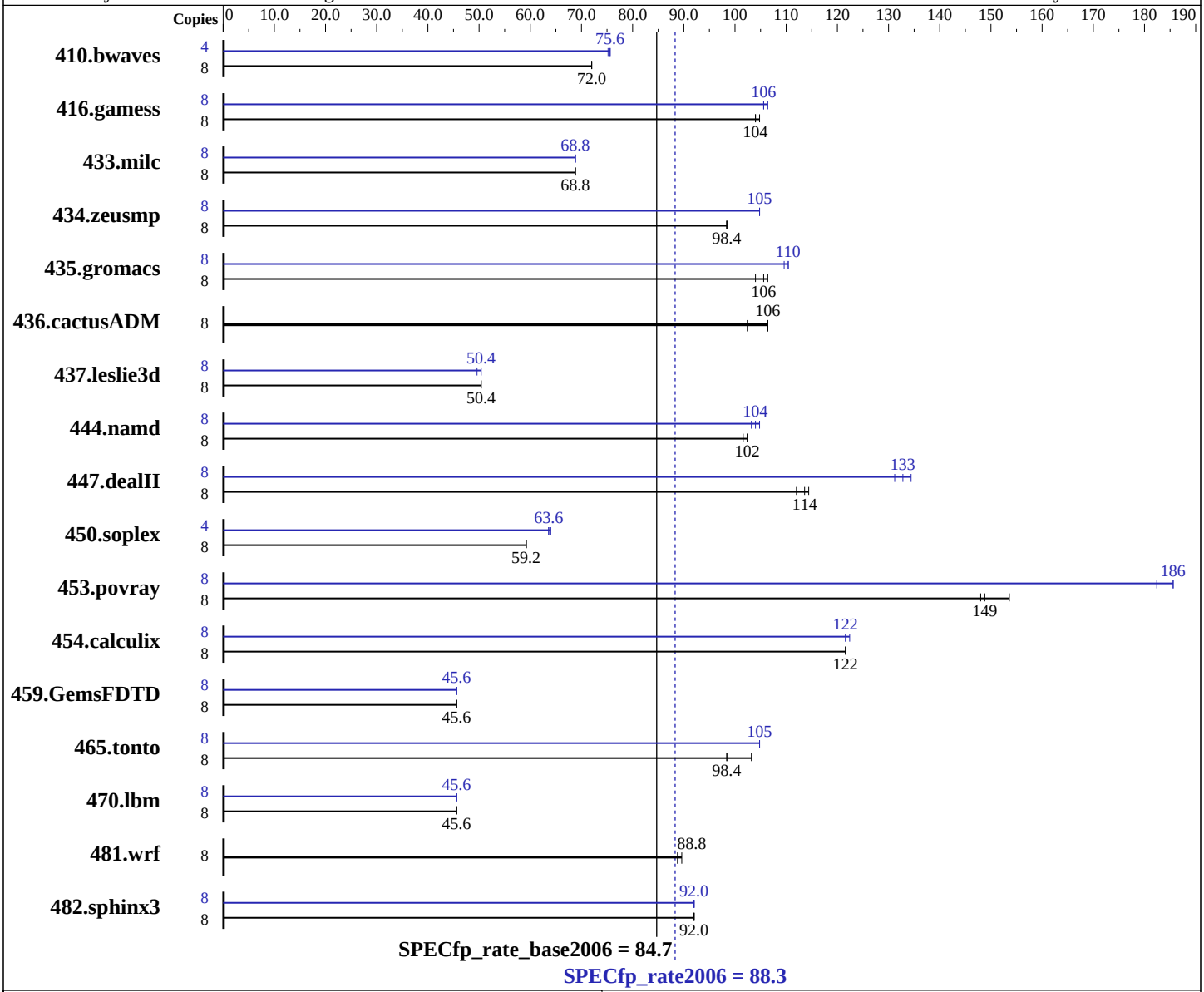
Fi7EPOWER MLK1610 (Intel Core i7-965)

SPECfp®_rate2006 = 88.3

SPECfp_rate_base2006 = 84.7

CPU2006 license: 3772
Test sponsor: Future Publishing Ltd.
Tested by: Future Publishing Ltd.

Test date: Oct-2008
Hardware Availability: Nov-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Core i7-965 Extreme Edition
CPU Characteristics:	Intel Turbo Boost Technology disabled, clocked at 3.73 GHz
CPU MHz:	3733
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Intel Visual Fortran Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cprof_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 9 GB (3x 2GB and 3x 1GB Corsair DDR3-1066, 9-9-9-24)
Disk Subsystem: 80 GB SATA, SSD
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.1 from http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**1508**	**72.0**	1508	72.0	1508	72.0	4	720	75.6	**721**	**75.6**	721	75.2
416.gamess	8	1498	105	1504	104	**1502**	**104**	8	1477	106	**1478**	**106**	1480	106
433.milc	8	**1063**	**68.8**	1063	68.8	1063	68.8	8	1063	68.8	**1063**	**68.8**	1064	68.8
434.zeusmp	8	742	98.4	**740**	**98.4**	738	98.4	8	**694**	**105**	692	105	697	105
435.gromacs	8	538	106	550	104	**541**	**106**	8	**518**	**110**	517	110	520	110
436.cactusADM	8	**898**	**106**	896	106	934	102	8	**898**	**106**	896	106	934	102
437.leslie3d	8	1499	50.4	1497	50.4	**1497**	**50.4**	8	1503	50.4	1505	49.6	**1504**	**50.4**
444.namd	8	**629**	**102**	630	102	629	102	8	614	105	620	103	**616**	**104**
447.dealII	8	815	112	**807**	**114**	801	114	8	697	131	681	134	**691**	**133**
450.soplex	8	**1130**	**59.2**	1129	59.2	1130	59.2	4	**523**	**63.6**	523	64.0	523	63.6
453.povray	8	**286**	**149**	287	148	277	154	8	234	182	229	186	**230**	**186**
454.calculix	8	543	122	**543**	**122**	543	122	8	**542**	**122**	539	122	543	122
459.GemsFDTD	8	1852	45.6	**1850**	**45.6**	1850	45.6	8	1858	45.6	**1861**	**45.6**	1863	45.6
465.tonto	8	**798**	**98.4**	765	103	799	98.4	8	751	105	753	105	**753**	**105**
470.lbm	8	2420	45.6	**2421**	**45.6**	2421	45.6	8	**2421**	**45.6**	2420	45.6	2421	45.6
481.wrf	8	**1004**	**88.8**	1005	88.8	1000	89.6	8	**1004**	**88.8**	1005	88.8	1000	89.6
482.sphinx3	8	1693	92.0	1695	92.0	**1694**	**92.0**	8	1692	92.0	1690	92.0	**1691**	**92.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
System was configured with HIS Radeon HD 4870 X2 discrete graphics card
Binaries were built on Windows Vista Ultimate (32-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

C++ benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Benchmarks using both Fortran and C:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qc99

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc9 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Oa /F1000000000

470.lbm: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

482.sphinx3: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 /F1000000000

C++ benchmarks:

444.namd: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Oa /F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

447.dealII: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qscalar-rep- /F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

450.soplex: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

453.povray: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias /F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: `-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000`

416.gamess: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000`

434.zeusmp: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- /F1000000000`

437.leslie3d: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000`

459.GemsFDTD: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qopt-prefetch /F1000000000`

465.tonto: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000`

Benchmarks using both Fortran and C:

435.gromacs: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000`

436.cactusADM: `basepeak = yes`

454.calculix: `-QxSSE4.2 -Qipo -O3 -Qprec-div- /F1000000000`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090713.html
http://www.spec.org/cpu2006/flags/Intel-Win32-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090713.xml
http://www.spec.org/cpu2006/flags/Intel-Win32-Platform.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 22:27:13 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 January 2009.