



SPEC® CFP2006 Result

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ACTION S.A.

SPECfp®_rate2006 = 69.3

ACTINA SOLAR 220 X2 (Intel Xeon E5410, 2.0 GHz)

SPECfp_rate_base2006 = 63.2

CPU2006 license: 9008

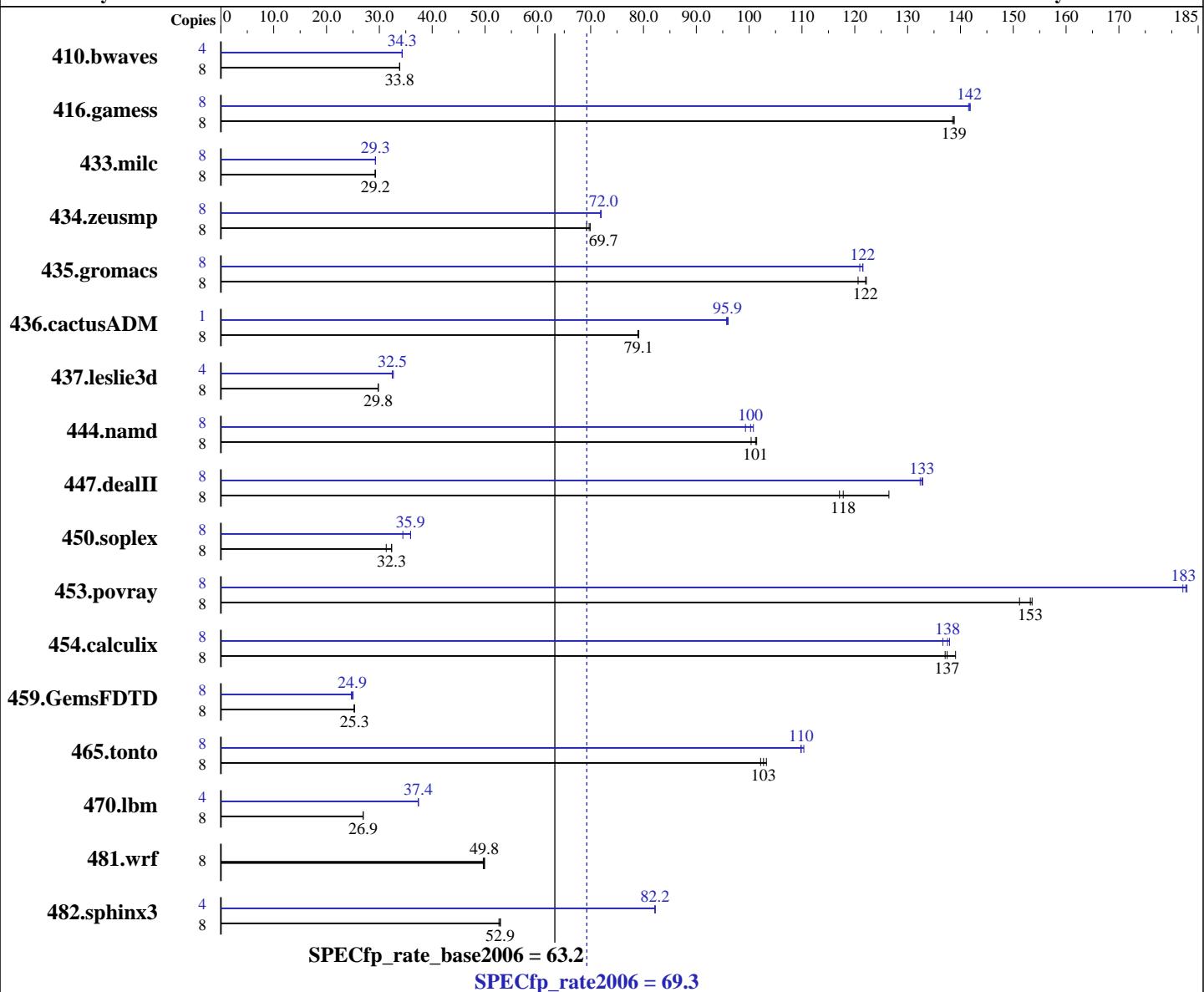
Test date: Dec-2008

Test sponsor: ACTION S.A.

Hardware Availability: Sep-2008

Tested by: ACTION S.A.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5410
CPU Characteristics: 1333 MHz System Bus
CPU MHz: 2333
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) with SP2, kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
Auto Parallel: Yes
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: None	Peak Pointers: 32/64-bit
Other Cache: None	Other Software: Binutils 2.18.50.0.7.20080502
Memory: 16 GB (8x2 GB, PC2-5300, CL 5-5-5, FB ECC)	
Disk Subsystem: 500 GB SATA, 7200 RPM	
Other Hardware: None	

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	<u>3213</u>	<u>33.8</u>	3214	33.8	3210	33.9	4	1584	34.3	<u>1584</u>	<u>34.3</u>	1583	34.3
416.gamess	8	1131	139	1128	139	<u>1129</u>	<u>139</u>	8	<u>1105</u>	<u>142</u>	1104	142	1107	142
433.milc	8	2511	29.2	2513	29.2	<u>2512</u>	<u>29.2</u>	8	<u>2510</u>	<u>29.3</u>	2510	29.3	2510	29.3
434.zeusmp	8	<u>1044</u>	<u>69.7</u>	1041	69.9	1052	69.2	8	1013	71.9	1011	72.0	<u>1011</u>	<u>72.0</u>
435.gromacs	8	474	121	467	122	<u>468</u>	<u>122</u>	8	472	121	470	122	<u>470</u>	<u>122</u>
436.cactusADM	8	1211	78.9	<u>1209</u>	<u>79.1</u>	1208	79.1	1	124	96.0	<u>125</u>	<u>95.9</u>	125	95.7
437.leslie3d	8	2528	29.8	<u>2525</u>	<u>29.8</u>	2523	29.8	4	1151	32.7	1159	32.4	<u>1158</u>	<u>32.5</u>
444.namd	8	<u>634</u>	<u>101</u>	639	100	633	101	8	<u>640</u>	<u>100</u>	646	99.3	636	101
447.dealII	8	<u>777</u>	<u>118</u>	724	126	782	117	8	<u>690</u>	<u>133</u>	689	133	691	132
450.soplex	8	2132	31.3	2064	32.3	<u>2065</u>	<u>32.3</u>	8	1937	34.4	1857	35.9	<u>1858</u>	<u>35.9</u>
453.povray	8	282	151	<u>278</u>	<u>153</u>	277	154	8	234	182	233	183	<u>233</u>	<u>183</u>
454.calculix	8	<u>480</u>	<u>137</u>	475	139	481	137	8	483	137	478	138	<u>480</u>	<u>138</u>
459.GemsFDTD	8	<u>3358</u>	<u>25.3</u>	3358	25.3	3364	25.2	8	3438	24.7	<u>3413</u>	<u>24.9</u>	3391	25.0
465.tonto	8	<u>766</u>	<u>103</u>	762	103	770	102	8	717	110	<u>717</u>	<u>110</u>	713	110
470.lbm	8	4083	26.9	<u>4082</u>	<u>26.9</u>	4080	26.9	4	1470	37.4	<u>1470</u>	<u>37.4</u>	1471	37.4
481.wrf	8	1790	49.9	<u>1793</u>	<u>49.8</u>	1799	49.7	8	1790	49.9	<u>1793</u>	<u>49.8</u>	1799	49.7
482.sphinx3	8	2947	52.9	2962	52.6	<u>2948</u>	<u>52.9</u>	4	949	82.2	948	82.2	<u>949</u>	<u>82.2</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Taskset was used to bind processes to cores except for 436.cactusADM peak
 OMP_NUM_THREADS set to number of processors
 KMP_AFFINITY set to "physical,0"
 KMP_STACKSIZE set to 64M



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Hardware Availability: Sep-2008
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Base Compiler Invocation

C benchmarks:
 icc

C++ benchmarks:
 icpc

Fortran benchmarks:
 ifort

Benchmarks using both Fortran and C:
 icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
 -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
 -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:
 -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:
 -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: /opt/intel/Compiler/11.0/042/bin/ia32/ifort
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

```
433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
           -no-prec-div -static -fno-alias

470.lbm: -xsse4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
          -auto-ilp32

482.sphinx3: -xsse4.1 -ipo -O3 -no-prec-div -static -unroll2
```

C++ benchmarks:

```
444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
           -no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
            -no-prec-div -static -unroll2 -ansi-alias -scalar-rep

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
             -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
              -no-prec-div -static -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: -xsse4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
             -no-prec-div -static -unroll2 -Ob0 -ansi-alias
             -scalar-rep

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
             -no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
               -no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
                -no-prec-div -static -unroll2 -Ob0 -opt-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
              -no-prec-div -static -unroll4 -auto
```

Benchmarks using both Fortran and C:

```
435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
              -no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3
                 -no-prec-div -static -unroll2 -opt-prefetch -parallel
                 -auto-ilp32
```

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Peak Optimization Flags (Continued)

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.02.xml>

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For other inquiries, please contact webmaster@spec.org.

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