



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Rj-2
(Intel Xeon X5470)

SPECfp®2006 = 26.2

SPECfp_base2006 = 25.0

CPU2006 license: 9006

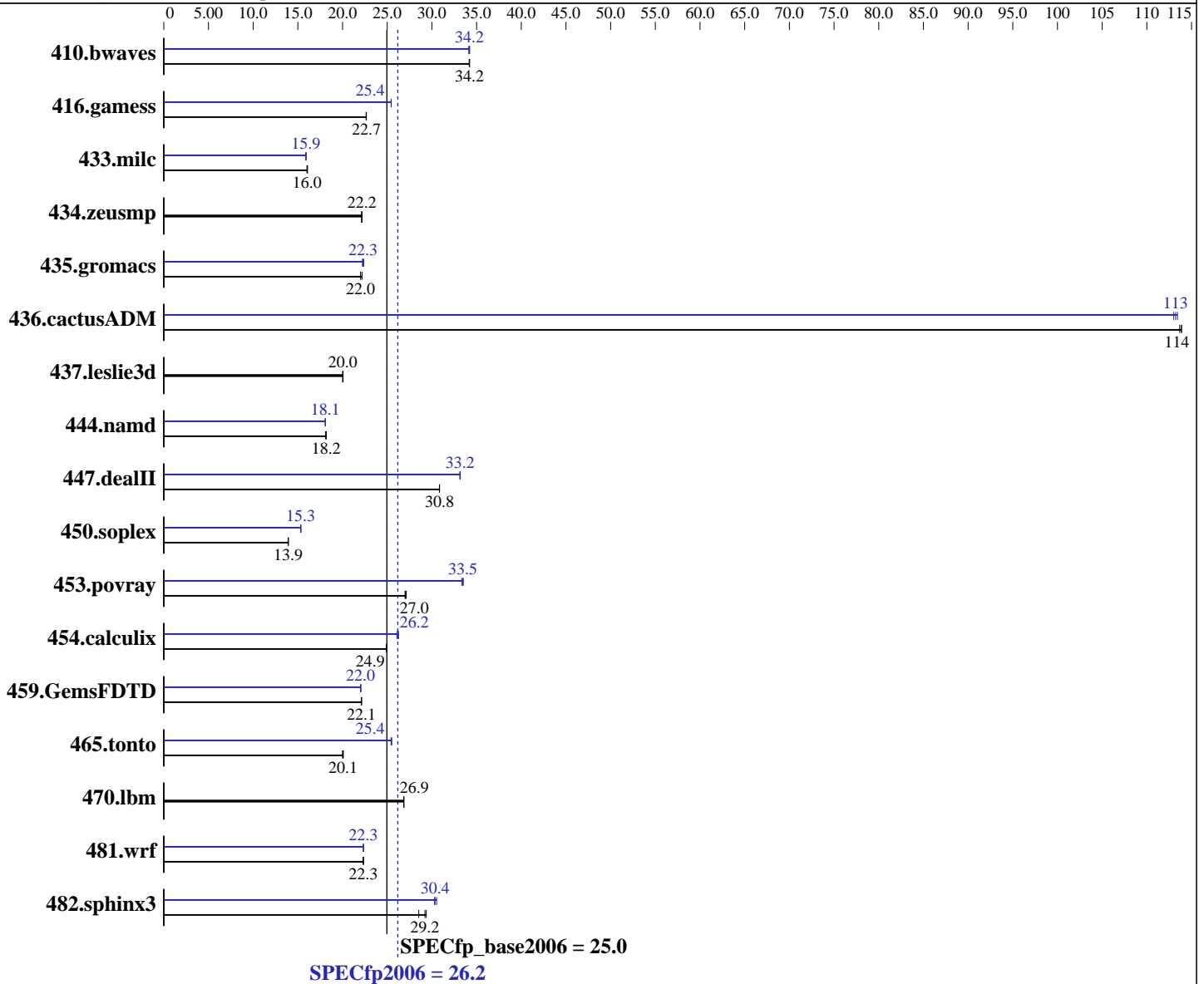
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Oct-2008

Hardware Availability: Oct-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5470
 CPU Characteristics: 3.33 GHz, 2x6 MB L2 shared, 1333 MHz system bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smpp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.044, l_fproc_b_11.0.044
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

Continued on next page



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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x146.5 GB SAS, 15000RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|------------|-------------|------------|-------------|------------|-------------|------------|-------------|------------|-------------|------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 397 | 34.2 | <u>397</u> | <u>34.2</u> | 398 | 34.2 | 397 | 34.2 | <u>397</u> | <u>34.2</u> | 398 | 34.1 |
| 416.gamess | 864 | 22.7 | <u>864</u> | <u>22.7</u> | 865 | 22.6 | <u>770</u> | <u>25.4</u> | 769 | 25.5 | 770 | 25.4 |
| 433.milc | <u>572</u> | <u>16.0</u> | 573 | 16.0 | 571 | 16.1 | <u>577</u> | <u>15.9</u> | 578 | 15.9 | 576 | 15.9 |
| 434.zeusmp | 411 | 22.1 | 410 | 22.2 | <u>411</u> | <u>22.2</u> | 411 | 22.1 | 410 | 22.2 | <u>411</u> | <u>22.2</u> |
| 435.gromacs | 324 | 22.0 | <u>324</u> | <u>22.0</u> | 322 | 22.2 | <u>320</u> | <u>22.3</u> | 321 | 22.2 | 319 | 22.4 |
| 436.cactusADM | <u>105</u> | <u>114</u> | 105 | 114 | 105 | 114 | 106 | 113 | <u>106</u> | <u>113</u> | 105 | 113 |
| 437.leslie3d | <u>469</u> | <u>20.0</u> | 469 | 20.1 | 470 | 20.0 | <u>469</u> | <u>20.0</u> | 469 | 20.1 | 470 | 20.0 |
| 444.namd | 443 | 18.1 | <u>441</u> | <u>18.2</u> | 441 | 18.2 | 444 | 18.1 | 444 | 18.1 | <u>444</u> | <u>18.1</u> |
| 447.dealII | <u>371</u> | <u>30.8</u> | 371 | 30.9 | 371 | 30.8 | 345 | 33.1 | <u>345</u> | <u>33.2</u> | 345 | 33.2 |
| 450.soplex | 598 | 13.9 | <u>599</u> | <u>13.9</u> | 600 | 13.9 | 544 | 15.3 | <u>544</u> | <u>15.3</u> | 544 | 15.3 |
| 453.povray | 197 | 27.0 | <u>197</u> | <u>27.0</u> | 196 | 27.1 | 160 | 33.3 | <u>159</u> | <u>33.5</u> | 159 | 33.5 |
| 454.calculix | <u>331</u> | <u>24.9</u> | 331 | 24.9 | 331 | 24.9 | 314 | 26.3 | 316 | 26.1 | <u>315</u> | <u>26.2</u> |
| 459.GemsFDTD | 480 | 22.1 | 480 | 22.1 | <u>480</u> | <u>22.1</u> | 481 | 22.0 | <u>482</u> | <u>22.0</u> | 482 | 22.0 |
| 465.tonto | 490 | 20.1 | <u>490</u> | <u>20.1</u> | 492 | 20.0 | 386 | 25.5 | <u>387</u> | <u>25.4</u> | 387 | 25.4 |
| 470.lbm | <u>512</u> | <u>26.9</u> | 512 | 26.9 | 511 | 26.9 | <u>512</u> | <u>26.9</u> | 512 | 26.9 | 511 | 26.9 |
| 481.wrf | 500 | 22.3 | <u>500</u> | <u>22.3</u> | 501 | 22.3 | <u>500</u> | <u>22.3</u> | 500 | 22.3 | 500 | 22.3 |
| 482.sphinx3 | 664 | 29.4 | <u>667</u> | <u>29.2</u> | 683 | 28.5 | 638 | 30.5 | 643 | 30.3 | <u>642</u> | <u>30.4</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 200M

Platform Notes

Bios settings:
Hardware Prefetcher: Enabled
Adjacent Cache Line Prefetch: Enabled



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General Notes

The NEC Express5800/120Rh-1(Intel Xeon X5470), the NEC Express5800/120Rj-2(Intel Xeon X5470), the Bull NovaScale R440 E1(Intel Xeon X5470, 3.33 GHz) and the Bull NovaScale R460 E1(Intel Xeon X5470, 3.33 GHz) models are electronically equivalent. The results have been measured on a NEC Express5800/120Rj-2(Intel Xeon X5470) model.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Continued on next page



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Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: /opt/intel/Compiler/11.0/044/bin/ia32/icc
-L/opt/intel/Compiler/11.0/044/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/044/ipp/ia32/include

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/Compiler/11.0/044/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/044/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/044/ipp/ia32/include

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64

Continued on next page



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Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-
-opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch
-parallel

Continued on next page



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Peak Optimization Flags (Continued)

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml>

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For other inquiries, please contact webmaster@spec.org.

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