SPEC® CINT2006 Result

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ACTION S.A.

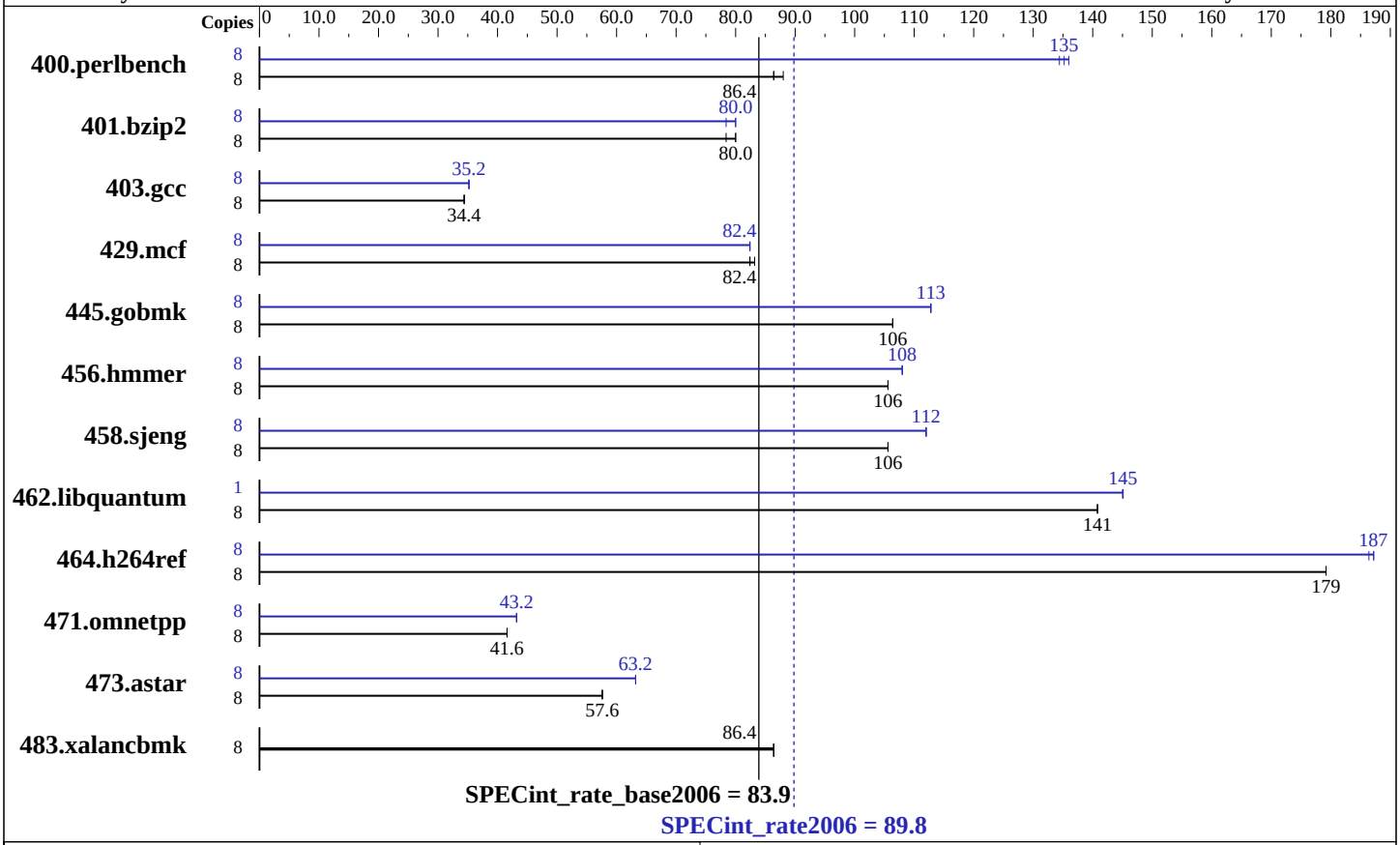
ACTINA SOLAR 202 X2 (Intel Xeon E5405, 2.0 GHz)

SPECint®_rate2006 = 89.8

SPECint_rate_base2006 = 83.9

CPU2006 license: 9008
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Nov-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon E5405
CPU Characteristics:	1333 MHz System Bus
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300, CL 5-5-5, ECC)
Disk Subsystem:	RAID 10 (6x300 GB SAS, 15K RPM
Other Hardware:	None

Software

Operating System:	Windows 2003 Server Enterprise Edition (32-bit) Service Pack 2
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	None SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**902**	**86.4**	905	86.4	888	88.0	8	583	134	575	136	**578**	**135**
401.bzip2	8	983	78.4	965	80.0	**967**	**80.0**	8	984	78.4	**968**	**80.0**	965	80.0
403.gcc	8	**1870**	**34.4**	1870	34.4	1866	34.4	8	1850	35.2	1828	35.2	**1833**	**35.2**
429.mcf	8	881	83.2	**883**	**82.4**	884	82.4	8	**885**	**82.4**	882	82.4	886	82.4
445.gobmk	8	789	106	789	106	**789**	**106**	8	**742**	**113**	742	113	744	113
456.hmmer	8	709	106	709	106	**709**	**106**	8	691	108	691	108	**691**	**108**
458.sjeng	8	915	106	915	106	**915**	**106**	8	866	112	866	112	**866**	**112**
462.libquantum	8	1178	141	1178	141	**1178**	**141**	1	143	145	143	145	**143**	**145**
464.h264ref	8	990	179	**988**	**179**	988	179	8	950	186	947	187	**947**	**187**
471.omnetpp	8	1191	41.6	**1191**	**41.6**	1192	41.6	8	1162	43.2	1163	43.2	**1163**	**43.2**
473.astar	8	973	57.6	970	57.6	**972**	**57.6**	8	886	63.2	**884**	**63.2**	884	63.2
483.xalancbmk	8	**638**	**86.4**	637	86.4	639	86.4	8	**638**	**86.4**	637	86.4	639	86.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
Product description located as of 10/2008:
http://www.actina.pl
Binaries were built on Windows Vista Ultimate (32-bit)
OMP_NUM_THREADS set to number of logical processors as seen by the OS
KMP_AFFINITY set to physical,0
submit disabled for 462.libquantum peak
Start command was used to bind processes to CPUs
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-QxSSSE3 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

C++ benchmarks:
-QxSSSE3 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qc99

C++ benchmarks:
icl -Qvc9

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Continued on next page

Peak Optimization Flags (Continued)

401.bzip2: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias /F512000000

403.gcc: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

429.mcf: -QxSSSE3 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: -QxSSSE3 -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -QxSSSE3 -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

458.sjeng: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: -QxSSSE3 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -QxSSSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090713.html

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090713.xml`

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For other inquiries, please contact webmaster@spec.org.

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