



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

CELSIUS H270, Intel Core 2 Extreme QX9300
processor

SPECint®2006 = 21.9

SPECint_base2006 = 19.3

CPU2006 license: 22

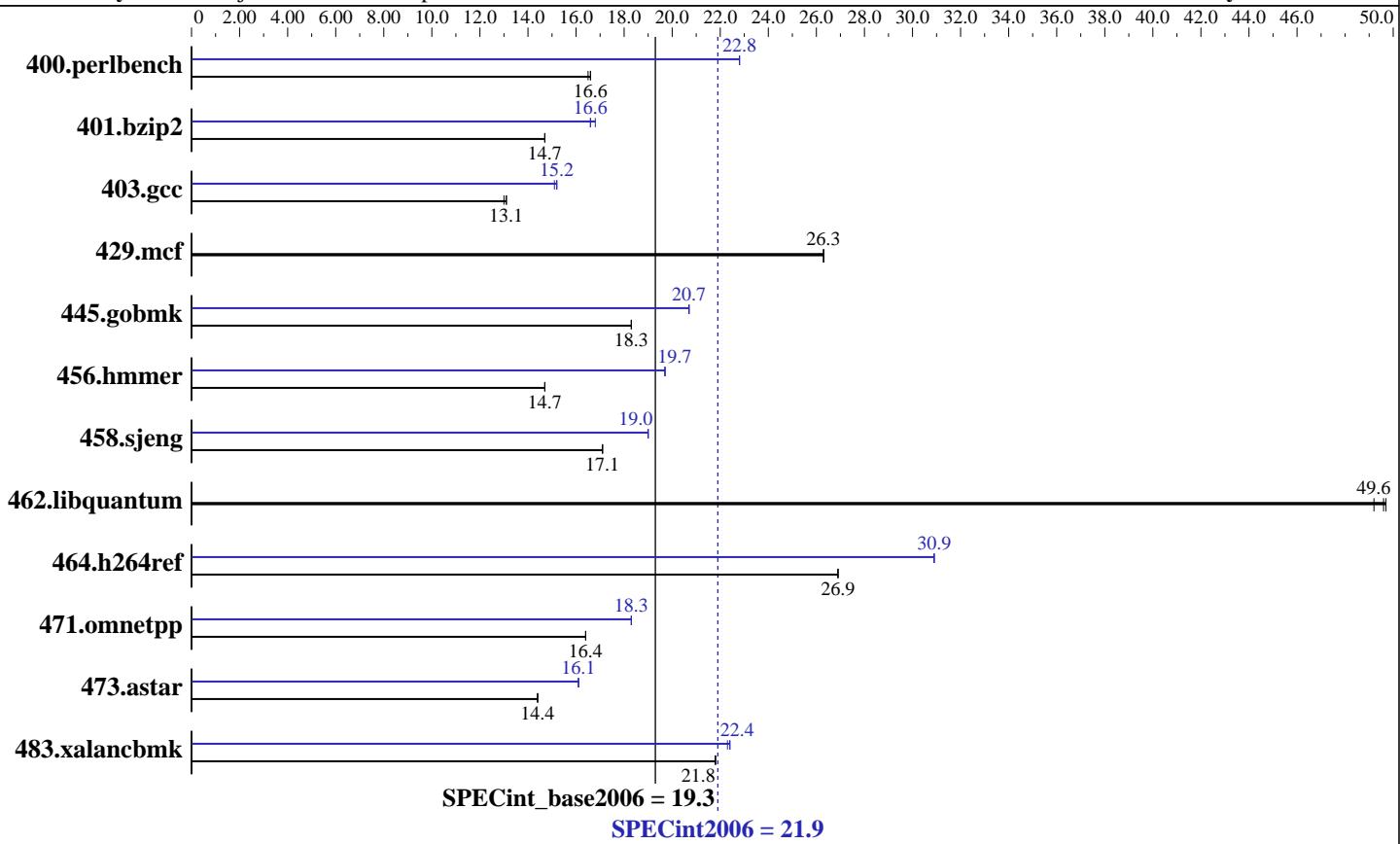
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2008

Hardware Availability: Dec-2008

Software Availability: Feb-2008



Hardware

CPU Name: Intel Core 2 Extreme QX9300
CPU Characteristics:
CPU MHz: 2530
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 4 GB (2x2 GB DDR3 1066 MHz SDRAM)
Disk Subsystem: 1 x 80 GB SATA 7200 RPM
Other Hardware: None

Software

Operating System: Windows Vista Ultimate, 64 bit Version
Compiler: Intel C++ Compiler
for applications running on IA-32, Version 10.1, Build 20080312
Intel C++ Compiler
for applications running on Intel 64, Version 10.1, Build 20080312
Microsoft Visual Studio 2005 with SP1 (for libraries)
Auto Parallel: Yes
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library 9



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	591	16.5	587	16.6	587	16.6	428	22.8	428	22.8	428	22.8
401.bzip2	658	14.7	658	14.7	658	14.7	576	16.8	580	16.6	581	16.6
403.gcc	617	13.0	615	13.1	616	13.1	534	15.1	531	15.2	530	15.2
429.mcf	346	26.3	346	26.3	346	26.3	346	26.3	346	26.3	346	26.3
445.gobmk	574	18.3	574	18.3	575	18.3	508	20.7	507	20.7	507	20.7
456.hmmer	634	14.7	634	14.7	634	14.7	473	19.7	473	19.7	473	19.7
458.sjeng	710	17.1	710	17.1	710	17.1	638	19.0	638	19.0	638	19.0
462.libquantum	418	49.6	417	49.7	421	49.2	418	49.6	417	49.7	421	49.2
464.h264ref	821	26.9	821	26.9	821	26.9	716	30.9	717	30.9	717	30.9
471.omnetpp	382	16.4	382	16.4	382	16.4	342	18.3	342	18.3	342	18.3
473.astar	488	14.4	489	14.4	489	14.4	436	16.1	436	16.1	436	16.1
483.xalancbmk	316	21.8	316	21.8	316	21.8	308	22.4	309	22.4	309	22.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Screen Saver was disabled

Power scheme was set to "Always On"

"Turn off monitor" option within this scheme was set to "Never".

Platform Notes

BIOS default settings have been used.

General Notes

For information about Fujitsu Siemens Computers please see:

<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:

 icl -Qvc8 -Qc99

C++ benchmarks:

 icl -Qvc8



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Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp, --no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

-fast -Qparallel -Qvec-guard-write -Qpar-runtime-control -F512000000
libguide40.lib

C++ benchmarks:

-fast -Qcxx-features -F512000000 libguide40.lib shlw32M.lib
-link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icl -Qvc8 -Qc99

401.bzip2: C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Bin\icl.exe
-IC:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Include
-link -LIBPATH:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Lib
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\PlatformSDK\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib

456.hmmer: C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Bin\icl.exe
-IC:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Include
-link -LIBPATH:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Lib
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\PlatformSDK\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib

C++ benchmarks:

icl -Qvc8



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Peak Portability Flags

```
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_WIN32
456.hmmer: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption_cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qparallel
               -Qpar-runtime-control -Qansi-alias -Qprefetch -F512000000
               libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
               -F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
               libguide40.lib

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -O2 -Qipo -QxT
               -Qprec-div- -Qansi-alias -F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
               -Qansi-alias -Qopt-multi-version-aggressive -F512000000
               libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
               -F512000000 libguide40.lib

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
               -Qansi-alias -F512000000 libguide40.lib
```

C++ benchmarks:

```
471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
               -Qansi-alias -Qopt-ra-region-strategy=block -F512000000
               libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
               -Qansi-alias -Qopt-ra-region-strategy=routine -F512000000
               libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
-Qansi-alias -F512000000 libguide40.lib shlw32M.lib
-link -FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.xml

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For other inquiries, please contact webmaster@spec.org.

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