



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = Not Run

PRIMERGY TX300 S4, Intel Xeon E5405, 2.0 GHz

SPECint_rate_base2006 = 94.9

CPU2006 license: 22

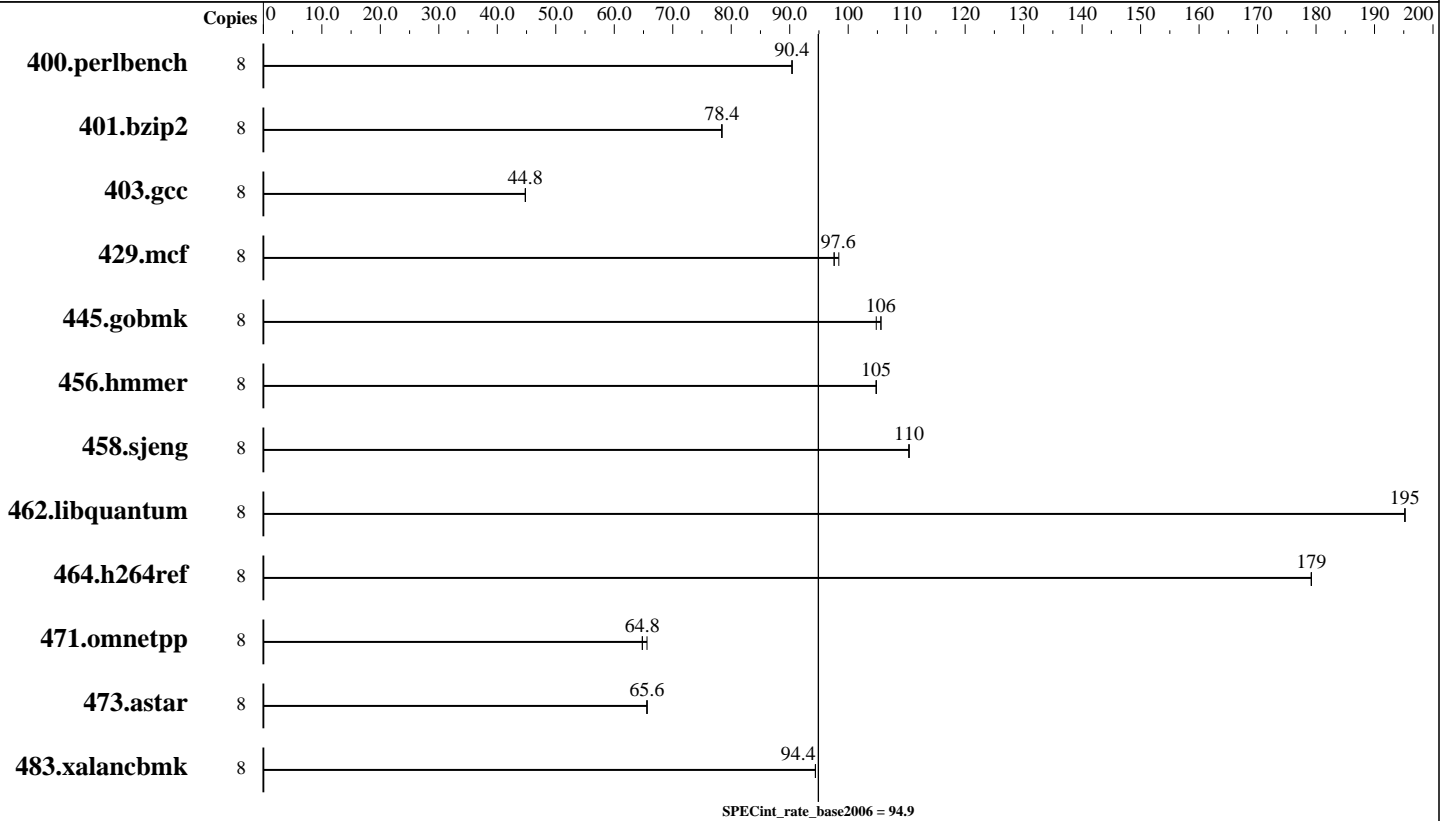
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2008

Hardware Availability: Dec-2007

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5405
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x SAS, 73 GB, 15000 rpm
 Other Hardware: None

Software

Operating System: Windows Server 2003 R2 Enterprise X64 SP2
 Compiler: Intel C++ Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cproc_p_11.0.054
 Microsoft Visual Studio 2008 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	861	90.4	<u>861</u>	<u>90.4</u>	864	90.4							
401.bzip2	8	983	78.4	<u>983</u>	<u>78.4</u>	985	78.4							
403.gcc	8	<u>1439</u>	<u>44.8</u>	1439	44.8	1449	44.8							
429.mcf	8	751	97.6	743	98.4	<u>745</u>	<u>97.6</u>							
445.gobmk	8	796	106	<u>797</u>	<u>106</u>	801	105							
456.hammer	8	711	105	<u>711</u>	<u>105</u>	711	105							
458.sjeng	8	880	110	<u>880</u>	<u>110</u>	880	110							
462.libquantum	8	850	195	850	195	<u>850</u>	<u>195</u>							
464.h264ref	8	987	179	987	179	<u>987</u>	<u>179</u>							
471.omnetpp	8	766	65.6	768	64.8	<u>767</u>	<u>64.8</u>							
473.astar	8	<u>854</u>	<u>65.6</u>	855	65.6	854	65.6							
483.xalancbmk	8	<u>586</u>	<u>94.4</u>	586	94.4	586	94.4							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Platform Notes

BIOS settings:
Hardware Prefetch = Disabled, Adjacent Sector Prefetch = Disabled

General Notes

Binaries were built on Windows Vista Ultimate (32-bit)

For information about Fujitsu Siemens Computers in your country please see:
<http://www.fujitsu-siemens.com/countries>

Base Compiler Invocation

C benchmarks:
icl -Qvc9 -Qc99

C++ benchmarks:
icl -Qvc9



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Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090713.html>

<http://www.spec.org/cpu2006/flags/Intel-Win32-Platform.html>

<http://www.spec.org/cpu2006/flags/Intel-Win32-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090713.xml>

<http://www.spec.org/cpu2006/flags/Intel-Win32-Platform.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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