SPEC® CINT2006 Result

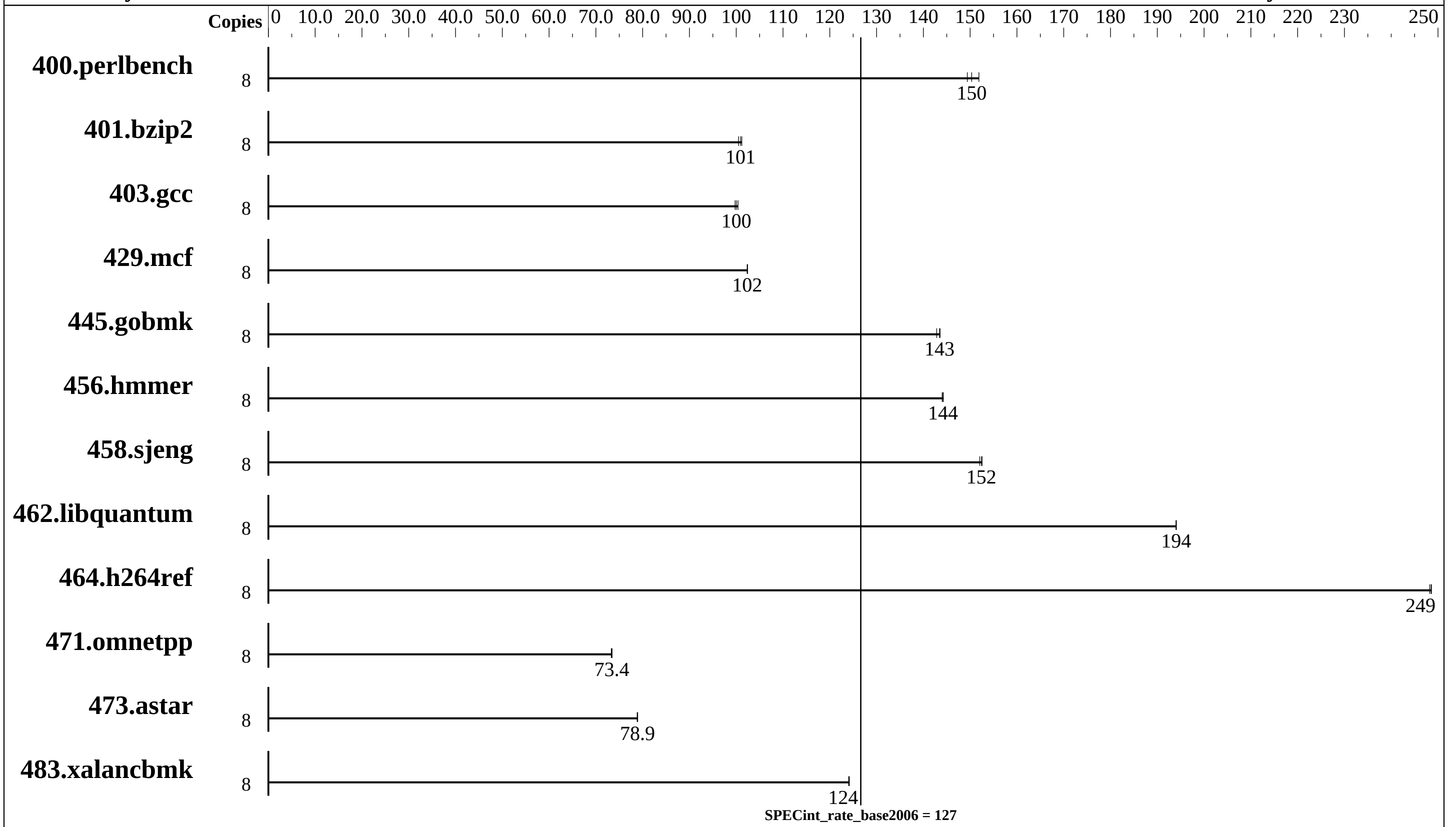
Dell Inc.

SPECint®_rate2006 = Not Run

PowerEdge 2900 III (Intel Xeon E5440, 2.83 GHz)

SPECint_rate_base2006 = 127

CPU2006 license:	55	**Test date:**	Nov-2008
Test sponsor:	Dell Inc.	**Hardware Availability:**	Sep-2008
Tested by:	Dell Inc.	**Software Availability:**	Nov-2008



Hardware

CPU Name:	Intel Xeon E5440
CPU Characteristics:	
CPU MHz:	2833
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB DDR2-667 FBDIMM)
Disk Subsystem:	2 x 73 GB 15000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cc_b_11.0.042
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	515	152	523	149	**520**	**150**							
401.bzip2	8	**765**	**101**	763	101	768	100							
403.gcc	8	646	99.8	642	100	**644**	**100**							
429.mcf	8	712	102	**713**	**102**	713	102							
445.gobmk	8	584	144	**585**	**143**	587	143							
456.hmmer	8	518	144	**518**	**144**	517	144							
458.sjeng	8	**635**	**152**	637	152	635	153							
462.libquantum	8	**854**	**194**	854	194	854	194							
464.h264ref	8	712	249	713	248	**712**	**249**							
471.omnetpp	8	681	73.5	**681**	**73.4**	682	73.3							
473.astar	8	712	78.8	**712**	**78.9**	711	78.9							
483.xalancbmk	8	445	124	445	124	**445**	**124**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

Platform Notes

```
BIOS Settings:
Hardware Prefetcher = Disabled (Default = Enabled)
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)
```

General Notes

```
taskset was used to bind processes to cores except
for 462.libquantum peak
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.xml

Tested with SPEC CPU2006 v1.1.
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