



SPEC® CFP2006 Result

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Fujitsu Siemens Computers

PRIMERGY BX620 S4, Intel Xeon X5470, 3.33 GHz

SPECfp®_rate2006 = 81.5

SPECfp_rate_base2006 = 74.4

CPU2006 license: 22

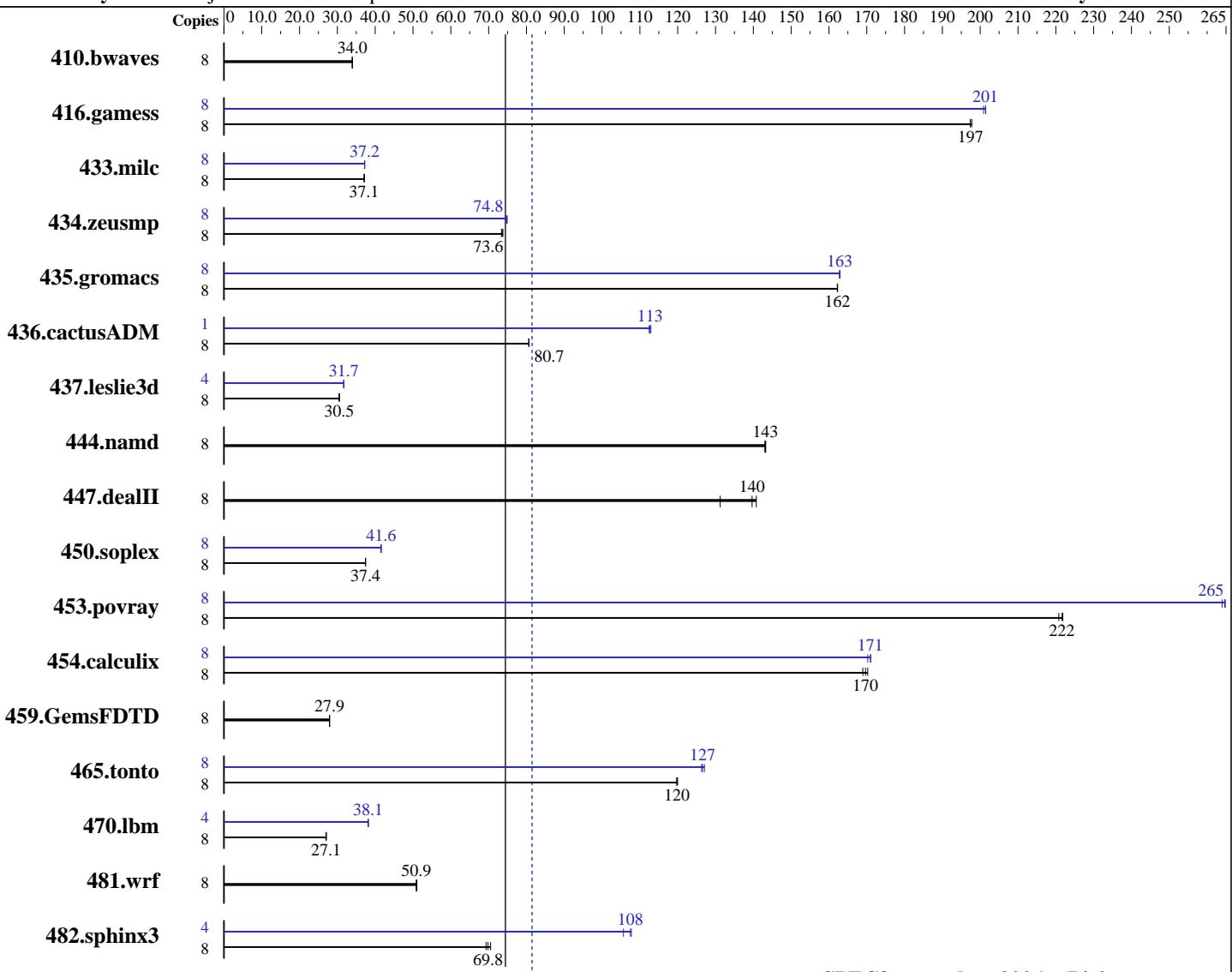
Test date: Oct-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Oct-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2008



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Hardware

CPU Name: Intel Xeon X5470
CPU Characteristics: 1333 MHz system bus
CPU MHz: 3333
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
Auto Parallel: Yes
File System: ext3
System State: Multi-User Run Level 3
Base Pointers: 64-bit

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L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x SATA, 120 GB, 5400 rpm
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3201	34.0	<u>3202</u>	<u>34.0</u>	3202	34.0	8	3201	34.0	<u>3202</u>	<u>34.0</u>	3202	34.0
416.gamess	8	794	197	792	198	<u>793</u>	<u>197</u>	8	780	201	778	201	<u>778</u>	<u>201</u>
433.milc	8	1979	37.1	<u>1979</u>	<u>37.1</u>	1980	37.1	8	1974	37.2	<u>1973</u>	<u>37.2</u>	1973	37.2
434.zeusmp	8	992	73.4	<u>990</u>	<u>73.6</u>	987	73.8	8	975	74.7	<u>973</u>	<u>74.8</u>	972	74.9
435.gromacs	8	352	162	352	162	<u>352</u>	<u>162</u>	8	351	163	<u>351</u>	<u>163</u>	351	163
436.cactusADM	8	<u>1185</u>	<u>80.7</u>	1185	80.7	1186	80.6	1	<u>106</u>	<u>113</u>	106	113	106	112
437.leslie3d	8	2463	30.5	2470	30.4	<u>2466</u>	<u>30.5</u>	4	1186	31.7	<u>1187</u>	<u>31.7</u>	1187	31.7
444.namd	8	<u>448</u>	<u>143</u>	448	143	449	143	8	<u>448</u>	<u>143</u>	448	143	449	143
447.dealII	8	697	131	650	141	<u>655</u>	<u>140</u>	8	697	131	650	141	<u>655</u>	<u>140</u>
450.soplex	8	<u>1782</u>	<u>37.4</u>	1781	37.5	1782	37.4	8	1607	41.5	<u>1605</u>	<u>41.6</u>	1604	41.6
453.povray	8	193	221	192	222	<u>192</u>	<u>222</u>	8	<u>161</u>	<u>265</u>	161	265	161	264
454.calculix	8	<u>389</u>	<u>170</u>	391	169	388	170	8	386	171	388	170	<u>386</u>	<u>171</u>
459.GemsFDTD	8	<u>3039</u>	<u>27.9</u>	3040	27.9	3039	27.9	8	<u>3039</u>	<u>27.9</u>	3040	27.9	3039	27.9
465.tonto	8	657	120	656	120	<u>657</u>	<u>120</u>	8	619	127	623	126	<u>622</u>	<u>127</u>
470.lbm	8	4069	27.0	4062	27.1	<u>4063</u>	<u>27.1</u>	4	<u>1441</u>	<u>38.1</u>	1441	38.1	1439	38.2
481.wrf	8	<u>1755</u>	<u>50.9</u>	1751	51.0	1757	50.8	8	<u>1755</u>	<u>50.9</u>	1751	51.0	1757	50.8
482.sphinx3	8	2249	69.3	<u>2233</u>	<u>69.8</u>	2211	70.5	4	<u>725</u>	<u>108</u>	738	106	724	108

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 taskset has been used to bind processes to cores except
 for 436.cactusADM peak

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 OMP_NUM_THREADS set to number of cores (default)
 KMP_AFFINITY set to "physical,0"
 KMP_STACKSIZE set to 64M



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Platform Notes

BIOS configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
```

Fortran benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc
```

```
482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc  
          -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib  
          -I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

C++ benchmarks (except as noted below):

```
icpc
```

```
450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc  
          -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib  
          -I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

Fortran benchmarks (except as noted below):

```
ifort
```

```
437.leslie3d: /opt/intel/Compiler/11.0/042/bin/ia32/ifort  
          -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib  
          -I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

Benchmarks using both Fortran and C:

```
icc ifort
```

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

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Peak Portability Flags (Continued)

```
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
    444.namd: -DSPEC_CPU_LP64  
    447.dealII: -DSPEC_CPU_LP64  
    453.povray: -DSPEC_CPU_LP64  
    454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
    465.tonto: -DSPEC_CPU_LP64  
    470.lbm: -DSPEC_CPU_LP64  
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
            -no-prec-div -static -fno-alias  
  
470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch  
          -auto-ilp32  
  
482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
```

C++ benchmarks:

```
444.namd: basepeak = yes  
  
447.dealII: basepeak = yes  
  
450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
            -no-prec-div -static -opt-malloc-options=3  
  
453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
            -no-prec-div -static -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes  
  
416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
            -no-prec-div -static -unroll2 -Ob0 -ansi-alias  
            -scalar-rep-  
  
434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
            -no-prec-div -static  
  
437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
              -no-prec-div -static -opt-malloc-options=3 -opt-prefetch
```

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Peak Optimization Flags (Continued)

459.GemsFDTD: basepeak = yes

```
465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3  
           -no-prec-div -static -unroll14 -auto
```

Benchmarks using both Fortran and C:

```
435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3  
             -no-prec-div -static -opt-prefetch -auto-ilp32
```

```
436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xsse4.1 -ipo -O3  
                -no-prec-div -static -unroll12 -opt-prefetch -parallel  
                -auto-ilp32
```

```
454.calculix: -xsse4.1 -ipo -O3 -no-prec-div -static -auto-ilp32
```

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.18.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.18.xml>

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For other inquiries, please contact webmaster@spec.org.

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