SPEC® CINT2006 Result

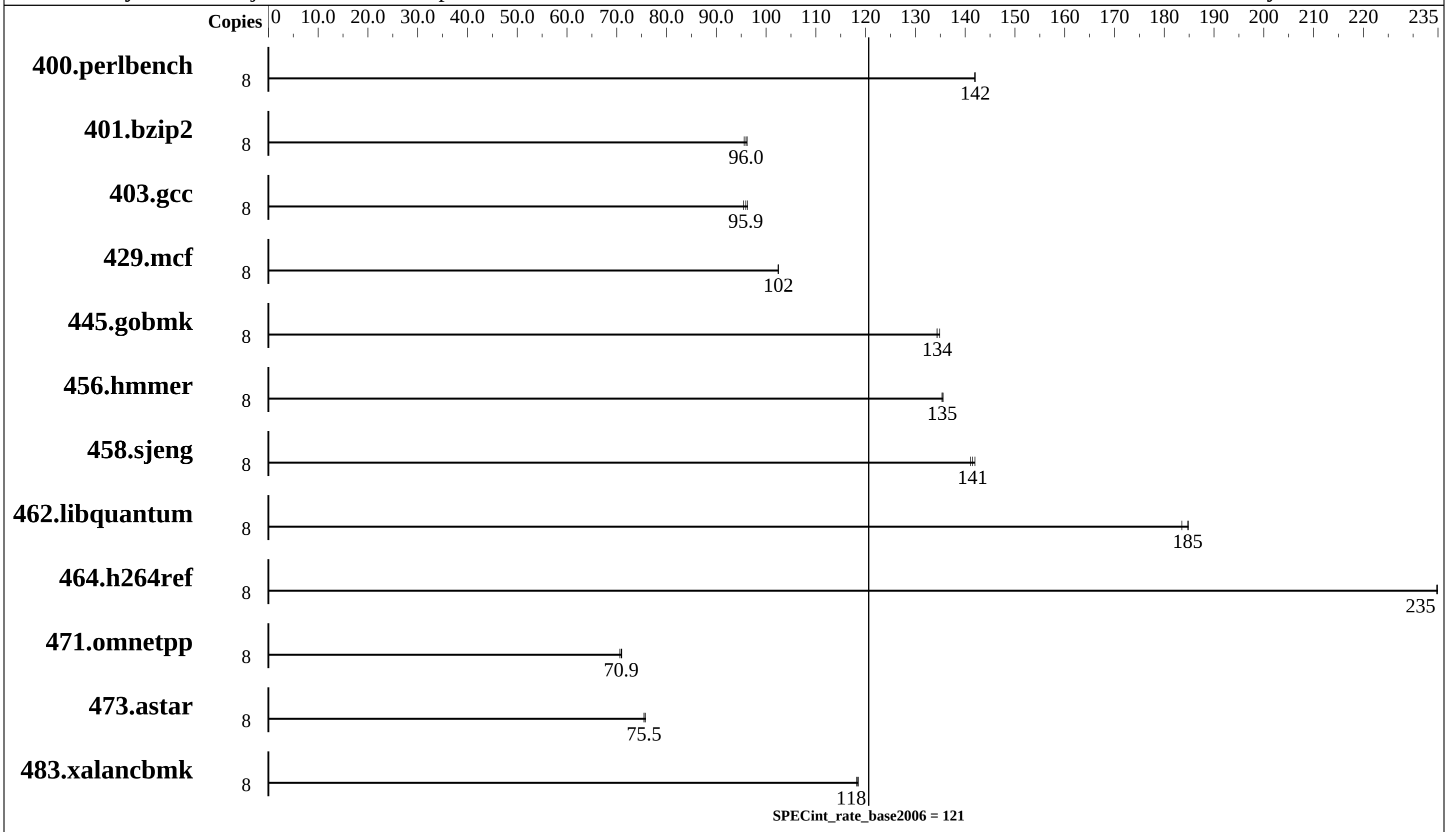
Fujitsu Siemens Computers

PRIMERGY BX620 S4, Intel Xeon L5430, 2.66 GHz

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 121

CPU2006 license:	22	**Test date:**	Oct-2008
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Oct-2008
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Nov-2008



Hardware

CPU Name:	Intel Xeon L5430
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	1x SATA, 120 GB, 5400 rpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap Library, Version 8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	551	142	**550**	**142**	550	142							
401.bzip2	8	803	96.2	**804**	**96.0**	808	95.6							
403.gcc	8	669	96.3	674	95.5	**671**	**95.9**							
429.mcf	8	712	102	712	102	**712**	**102**							
445.gobmk	8	622	135	**625**	**134**	625	134							
456.hmmer	8	551	135	**551**	**135**	551	136							
458.sjeng	8	682	142	**684**	**141**	686	141							
462.libquantum	8	903	184	**897**	**185**	897	185							
464.h264ref	8	754	235	754	235	**754**	**235**							
471.omnetpp	8	708	70.6	**705**	**70.9**	704	71.0							
473.astar	8	**744**	**75.5**	741	75.8	744	75.5							
483.xalancbmk	8	**466**	**118**	467	118	466	119							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset has been used to bind processes to cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable
```

General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
 icc

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3 -opt-prefetch`

C++ benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.16.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.16.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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