SPEC® CINT2006 Result

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Dell Inc.

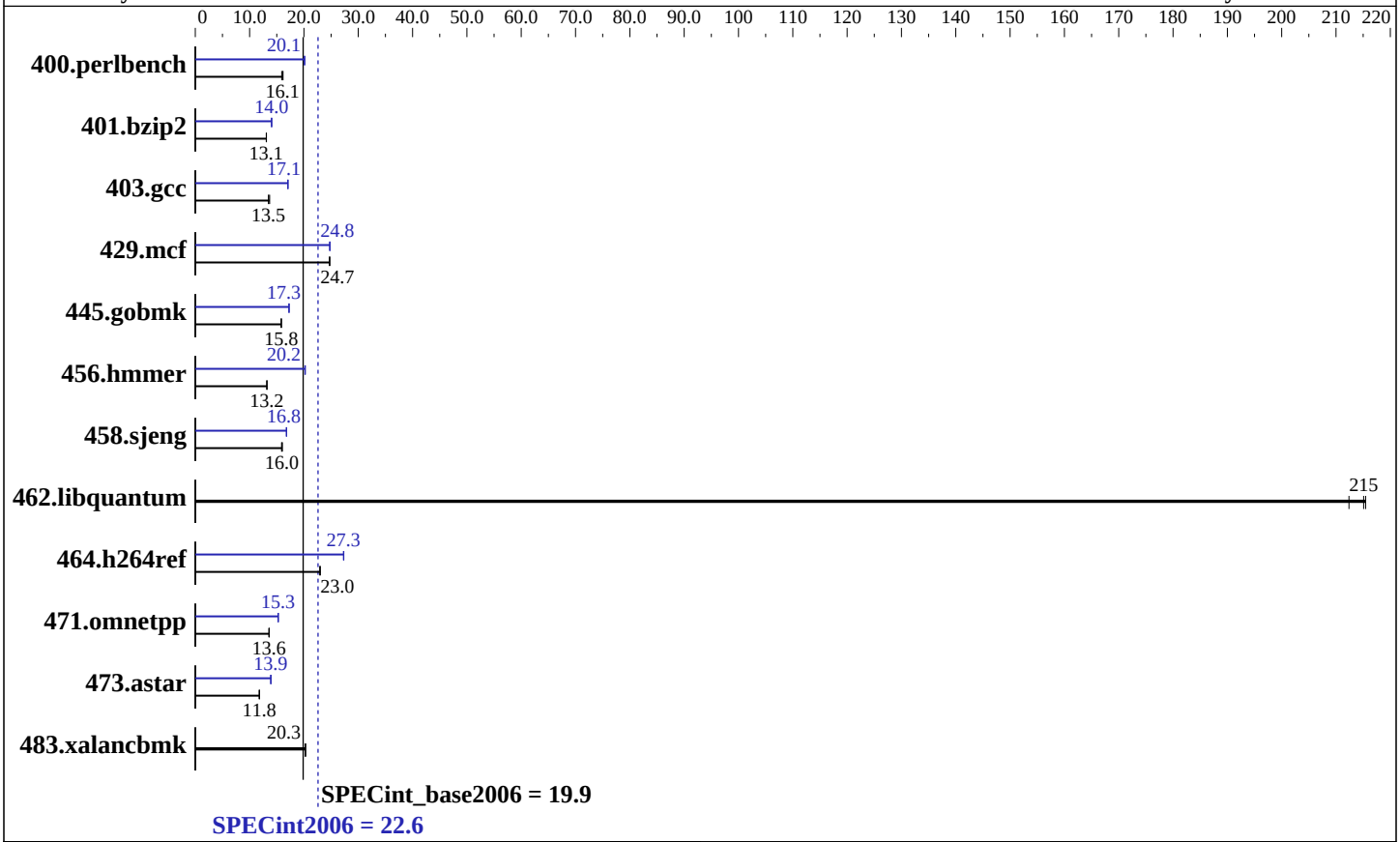
PowerEdge 1950 III (Intel Xeon E5410, 2.33 GHz)

SPECint®2006 = 22.6

SPECint_base2006 = 19.9

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB 667 MHz ECC CL5 FB-DIMM)
Disk Subsystem:	1 x 73 GB 10000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	605	16.2	**608**	**16.1**	614	15.9	**486**	**20.1**	486	20.1	485	20.1
401.bzip2	**738**	**13.1**	738	13.1	737	13.1	687	14.0	**687**	**14.0**	687	14.0
403.gcc	**597**	**13.5**	597	13.5	588	13.7	**472**	**17.1**	472	17.1	472	17.0
429.mcf	369	24.7	**369**	**24.7**	369	24.7	**368**	**24.8**	368	24.8	369	24.7
445.gobmk	663	15.8	662	15.8	**663**	**15.8**	**608**	**17.3**	608	17.2	608	17.3
456.hmmer	708	13.2	**707**	**13.2**	707	13.2	**461**	**20.2**	461	20.2	462	20.2
458.sjeng	755	16.0	**758**	**16.0**	762	15.9	721	16.8	721	16.8	**721**	**16.8**
462.libquantum	97.6	212	96.2	215	**96.3**	**215**	97.6	212	96.2	215	**96.3**	**215**
464.h264ref	962	23.0	966	22.9	**964**	**23.0**	811	27.3	809	27.3	**811**	**27.3**
471.omnetpp	462	13.5	459	13.6	**459**	**13.6**	409	15.3	**409**	**15.3**	409	15.3
473.astar	596	11.8	595	11.8	**596**	**11.8**	504	13.9	**505**	**13.9**	506	13.9
483.xalancbmk	340	20.3	341	20.3	**340**	**20.3**	340	20.3	341	20.3	**340**	**20.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

General Notes

OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2:
```
/opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include
```

456.hmmer:
```
/opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.xml

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For other inquiries, please contact webmaster@spec.org.

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