



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = Not Run

PowerEdge 1950 III (Intel Xeon E5410, 2.33 GHz)

SPECint\_rate\_base2006 = 113

CPU2006 license: 55

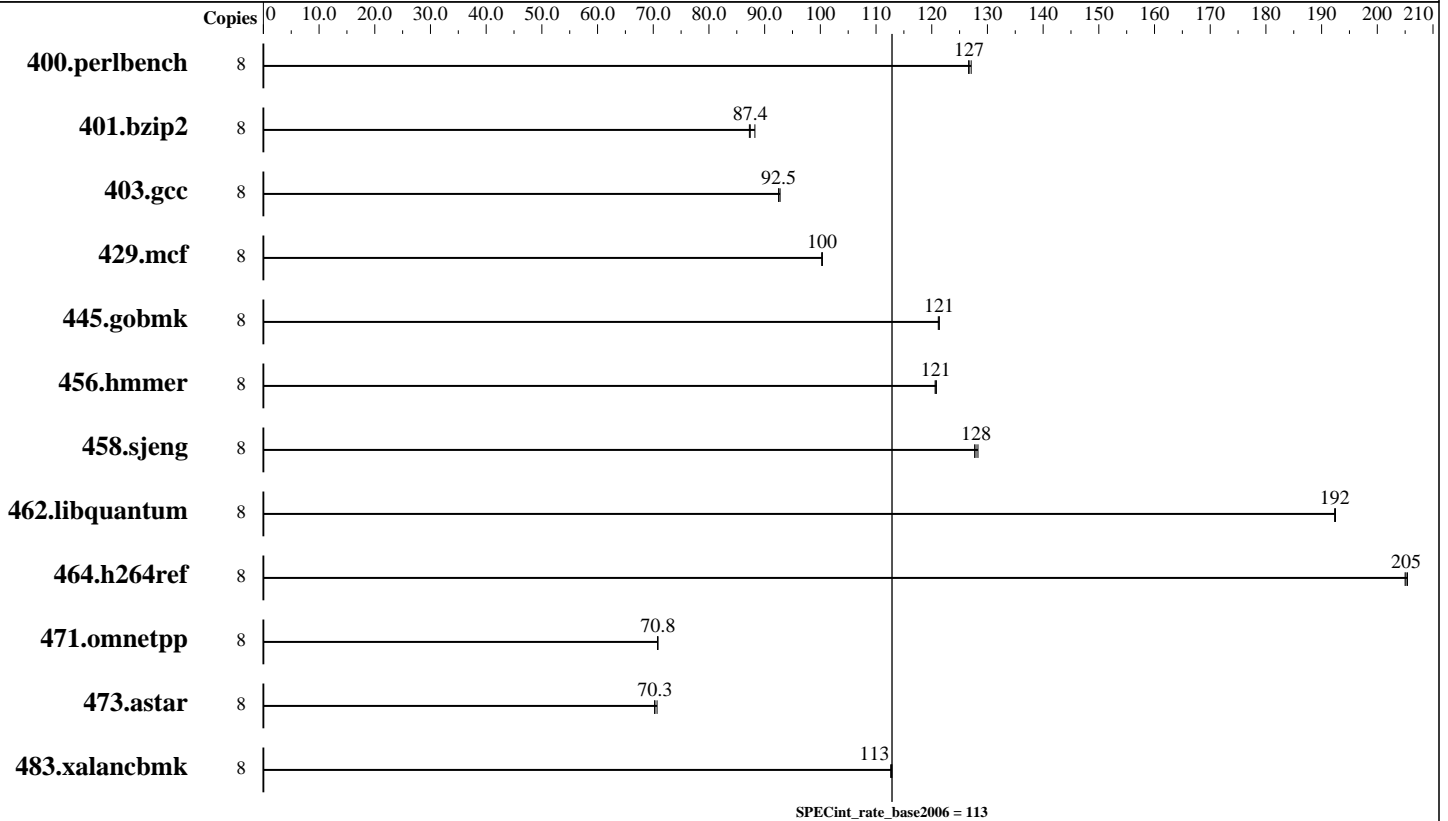
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Oct-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon E5410  
 CPU Characteristics: 2333  
 CPU MHz: 2333  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB 667 MHz ECC CL5 FB-DIMM)  
 Disk Subsystem: 1 x 73 GB 10000 RPM SAS  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16-60.0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	615	127	<b>617</b>	<b>127</b>	617	127							
401.bzip2	8	885	87.3	875	88.3	<b>883</b>	<b>87.4</b>							
403.gcc	8	694	92.8	<b>696</b>	<b>92.5</b>	696	92.5							
429.mcf	8	727	100	<b>727</b>	<b>100</b>	728	100							
445.gobmk	8	692	121	<b>692</b>	<b>121</b>	693	121							
456.hammer	8	618	121	619	121	<b>618</b>	<b>121</b>							
458.sjeng	8	758	128	754	128	<b>757</b>	<b>128</b>							
462.libquantum	8	861	192	<b>862</b>	<b>192</b>	862	192							
464.h264ref	8	<b>863</b>	<b>205</b>	862	205	864	205							
471.omnetpp	8	707	70.8	706	70.8	<b>706</b>	<b>70.8</b>							
473.astar	8	799	70.2	795	70.7	<b>799</b>	<b>70.3</b>							
483.xalancbmk	8	489	113	490	113	<b>489</b>	<b>113</b>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## Platform Notes

BIOS Settings:  
Hardware Prefetcher = Disabled (Default = Enabled)  
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)

## General Notes

taskset was used to bind processes to cores except for 462.libquantum peak

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

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