



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = Not Run

IBM System x3550 (Intel Xeon L5410)

SPECint_rate_base2006 = 113

CPU2006 license: 11

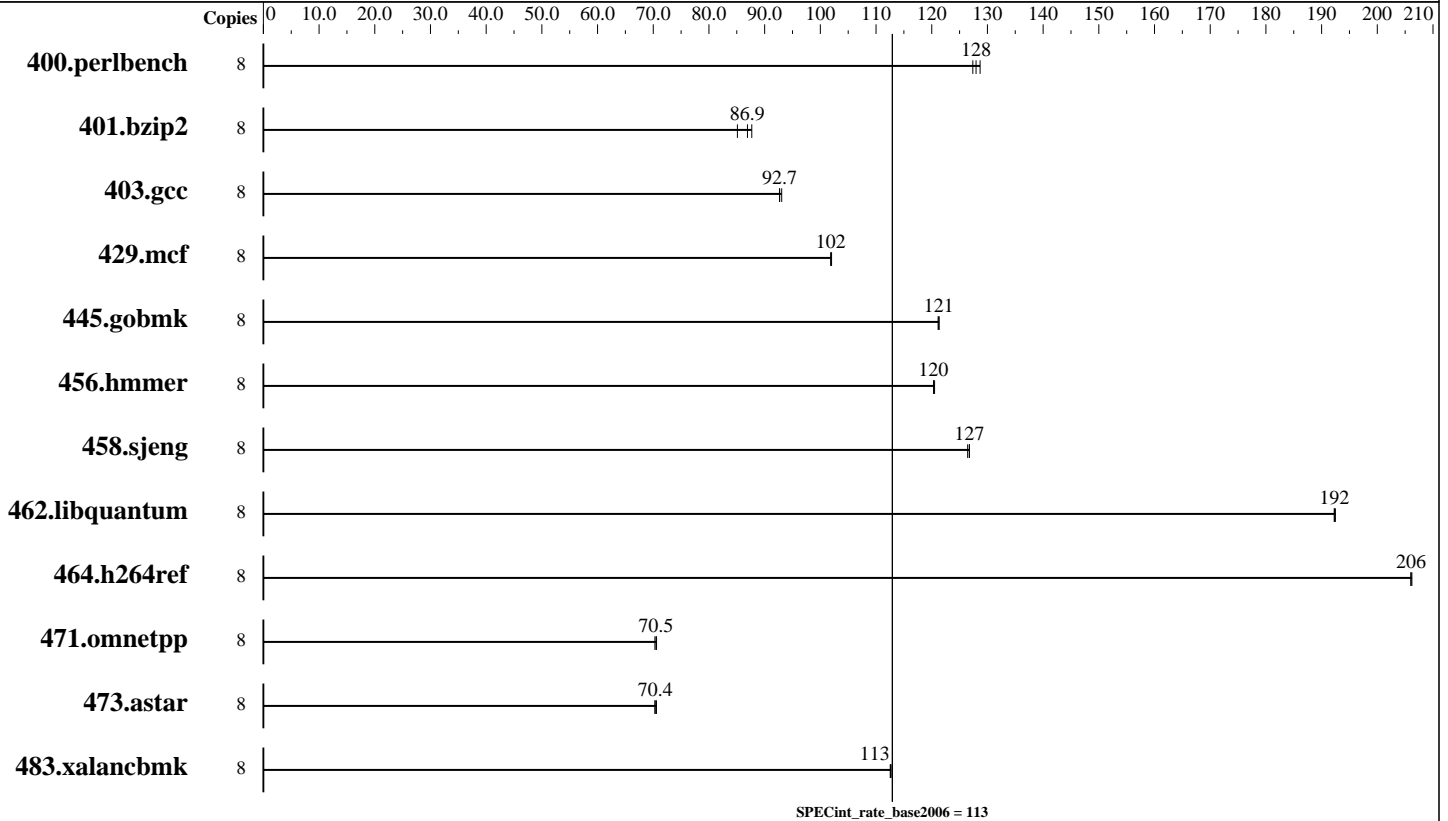
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Oct-2008

Hardware Availability: Nov-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon L5410
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 36 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	614	127	<u>611</u>	<u>128</u>	607	129							
401.bzip2	8	880	87.7	907	85.1	<u>888</u>	<u>86.9</u>							
403.gcc	8	692	93.1	695	92.7	<u>695</u>	<u>92.7</u>							
429.mcf	8	716	102	715	102	<u>716</u>	<u>102</u>							
445.gobmk	8	692	121	693	121	<u>692</u>	<u>121</u>							
456.hammer	8	620	120	<u>620</u>	<u>120</u>	619	121							
458.sjeng	8	764	127	<u>764</u>	<u>127</u>	766	126							
462.libquantum	8	861	192	<u>862</u>	<u>192</u>	862	192							
464.h264ref	8	859	206	859	206	<u>859</u>	<u>206</u>							
471.omnetpp	8	708	70.6	711	70.3	<u>709</u>	<u>70.5</u>							
473.astar	8	796	70.6	799	70.3	<u>798</u>	<u>70.4</u>							
483.xalancbmk	8	<u>490</u>	<u>113</u>	490	113	490	113							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores

General Notes

Hardware Prefetch Disabled, Adjacent Sector Prefetch Disabled
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

`-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch`

C++ benchmarks:

`-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.11.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.11.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.xml>

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For other inquiries, please contact webmaster@spec.org.

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