SPEC® CINT2006 Result

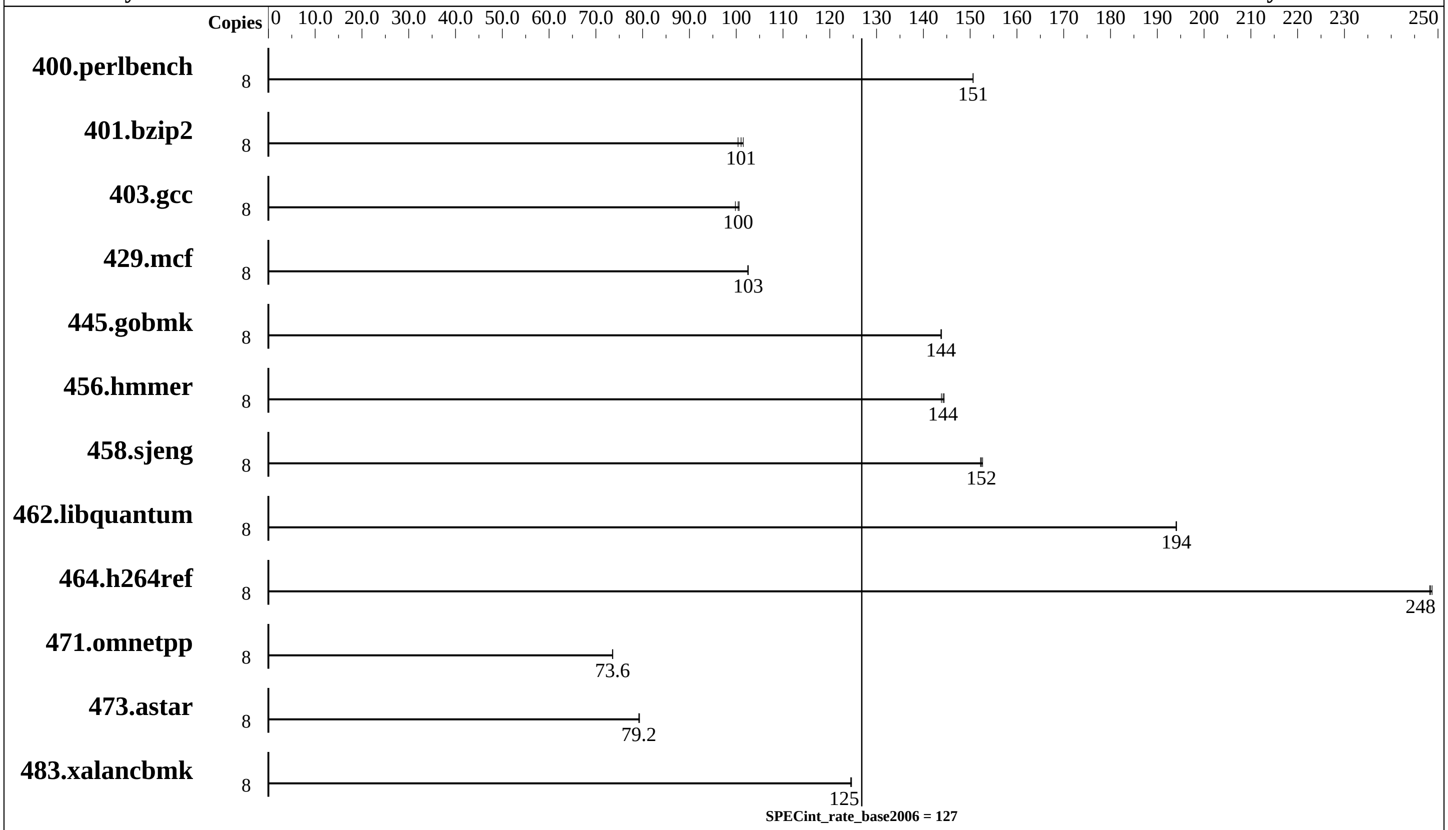
Dell Inc.

PowerEdge 1950 III (Intel Xeon E5440, 2.83 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 127

CPU2006 license:	55	**Test date:**	Sep-2008
Test sponsor:	Dell Inc.	**Hardware Availability:**	Nov-2007
Tested by:	Dell Inc.	**Software Availability:**	Nov-2008



Hardware

CPU Name:	Intel Xeon E5440
CPU Characteristics:	
CPU MHz:	2833
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB 667 MHz ECC CL5 FB-DIMM)
Disk Subsystem:	1 x 73 GB 10000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Binutils 2.18.50.0.7.20080502 Microquill SmartHeap V8.1

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Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	519	151	519	151	**519**	**151**							
401.bzip2	8	**764**	**101**	761	102	769	100							
403.gcc	8	645	99.8	**641**	**100**	640	101							
429.mcf	8	711	103	712	102	**711**	**103**							
445.gobmk	8	**584**	**144**	584	144	583	144							
456.hmmer	8	519	144	**518**	**144**	517	144							
458.sjeng	8	**635**	**152**	636	152	634	153							
462.libquantum	8	854	194	854	194	**854**	**194**							
464.h264ref	8	713	248	**713**	**248**	712	249							
471.omnetpp	8	**680**	**73.6**	679	73.6	680	73.6							
473.astar	8	**709**	**79.2**	709	79.2	708	79.3							
483.xalancbmk	8	**443**	**125**	444	124	443	125							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores
```

Operating System Notes

```
 'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

Platform Notes

```
 BIOS Settings:
 Hardware Prefetcher = Disabled (Default = Enabled)
 Adjacent Cache Line Prefetch = Disabled (Default = Enabled)
```

Base Compiler Invocation

C benchmarks:
 icc

C++ benchmarks:
 icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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