SPEC® CINT2006 Result

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Hewlett-Packard Company

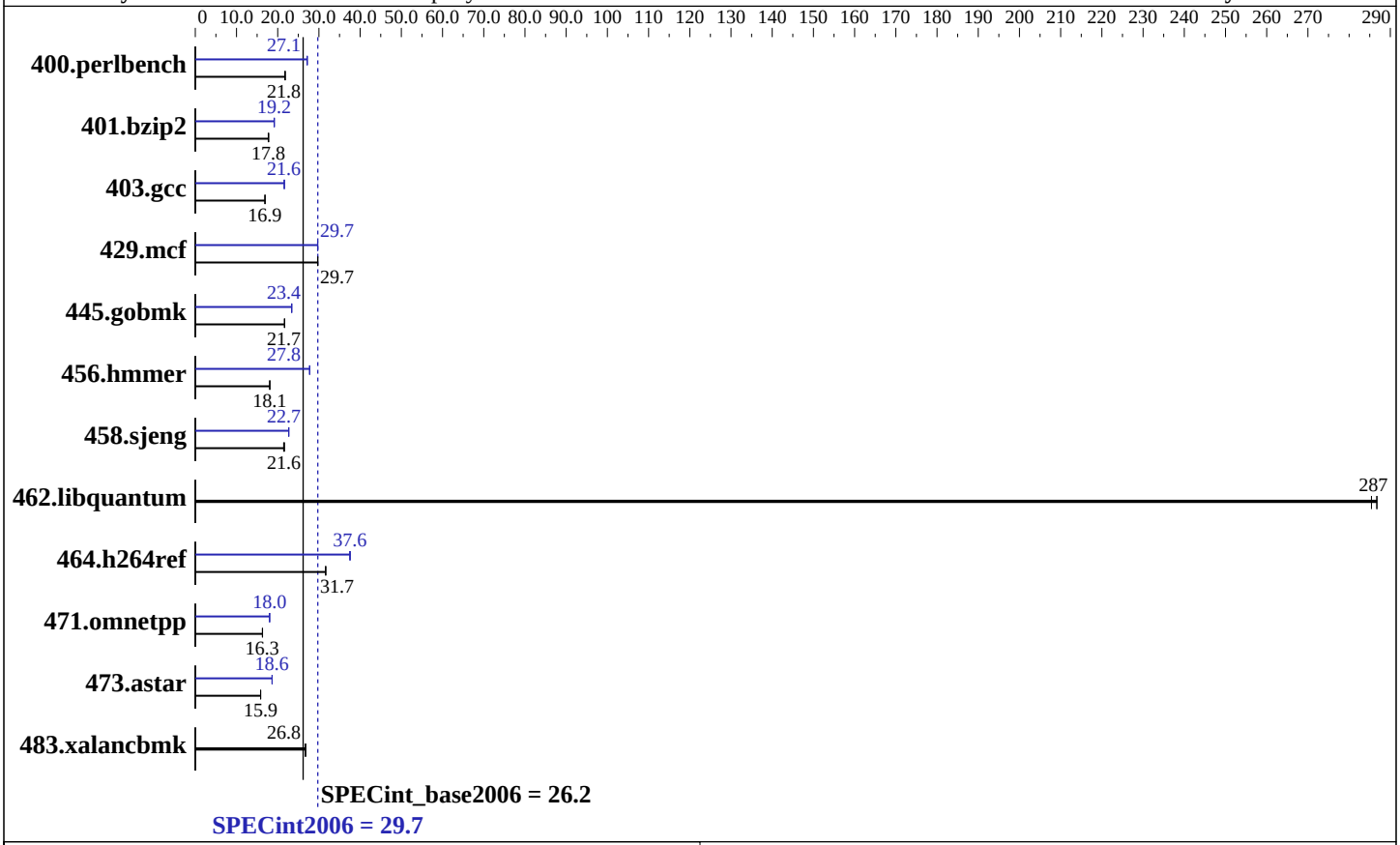
ProLiant DL160 G5p
(3.20 GHz, Intel Xeon X5482)

SPECint®2006 = 29.7

SPECint_base2006 = 26.2

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon X5482
CPU Characteristics:	3.20 GHz, 2x6 MB L2 Shared, 1600 MHz system bus
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2GB PC2-6400F CL5)
Disk Subsystem:	1x72 GB 15 K SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12.smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	447	21.9	**448**	**21.8**	450	21.7	**360**	**27.1**	360	27.1	359	27.2
401.bzip2	545	17.7	**543**	**17.8**	542	17.8	**504**	**19.2**	504	19.2	503	19.2
403.gcc	**476**	**16.9**	477	16.9	475	16.9	373	21.6	**373**	**21.6**	372	21.6
429.mcf	307	29.7	307	29.7	**307**	**29.7**	307	29.7	307	29.7	**307**	**29.7**
445.gobmk	484	21.7	485	21.6	**484**	**21.7**	**448**	**23.4**	448	23.4	448	23.4
456.hmmer	**516**	**18.1**	516	18.1	516	18.1	336	27.7	336	27.8	**336**	**27.8**
458.sjeng	564	21.4	**561**	**21.6**	559	21.6	533	22.7	**534**	**22.7**	534	22.7
462.libquantum	72.6	285	72.3	287	**72.3**	**287**	72.6	285	72.3	287	**72.3**	**287**
464.h264ref	700	31.6	699	31.7	**699**	**31.7**	**589**	**37.6**	591	37.5	588	37.6
471.omnetpp	**383**	**16.3**	384	16.3	383	16.3	347	18.0	**347**	**18.0**	347	18.0
473.astar	443	15.9	**443**	**15.9**	443	15.8	376	18.6	377	18.6	**377**	**18.6**
483.xalancbmk	258	26.8	**258**	**26.8**	258	26.7	258	26.8	**258**	**26.8**	258	26.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -par-runtime-control -opt-prefetch`

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Base Optimization Flags (Continued)

C++ benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/042/bin/intel64/icc -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib -I/opt/intel/Compiler/11.0/042/ipp/em64t/include`

456.hmmer: `/opt/intel/Compiler/11.0/042/bin/intel64/icc -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib -I/opt/intel/Compiler/11.0/042/ipp/em64t/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch`

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Peak Optimization Flags (Continued)

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3`

429.mcf: `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.xml`

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