SPEC® CINT2006 Result

Hewlett-Packard Company

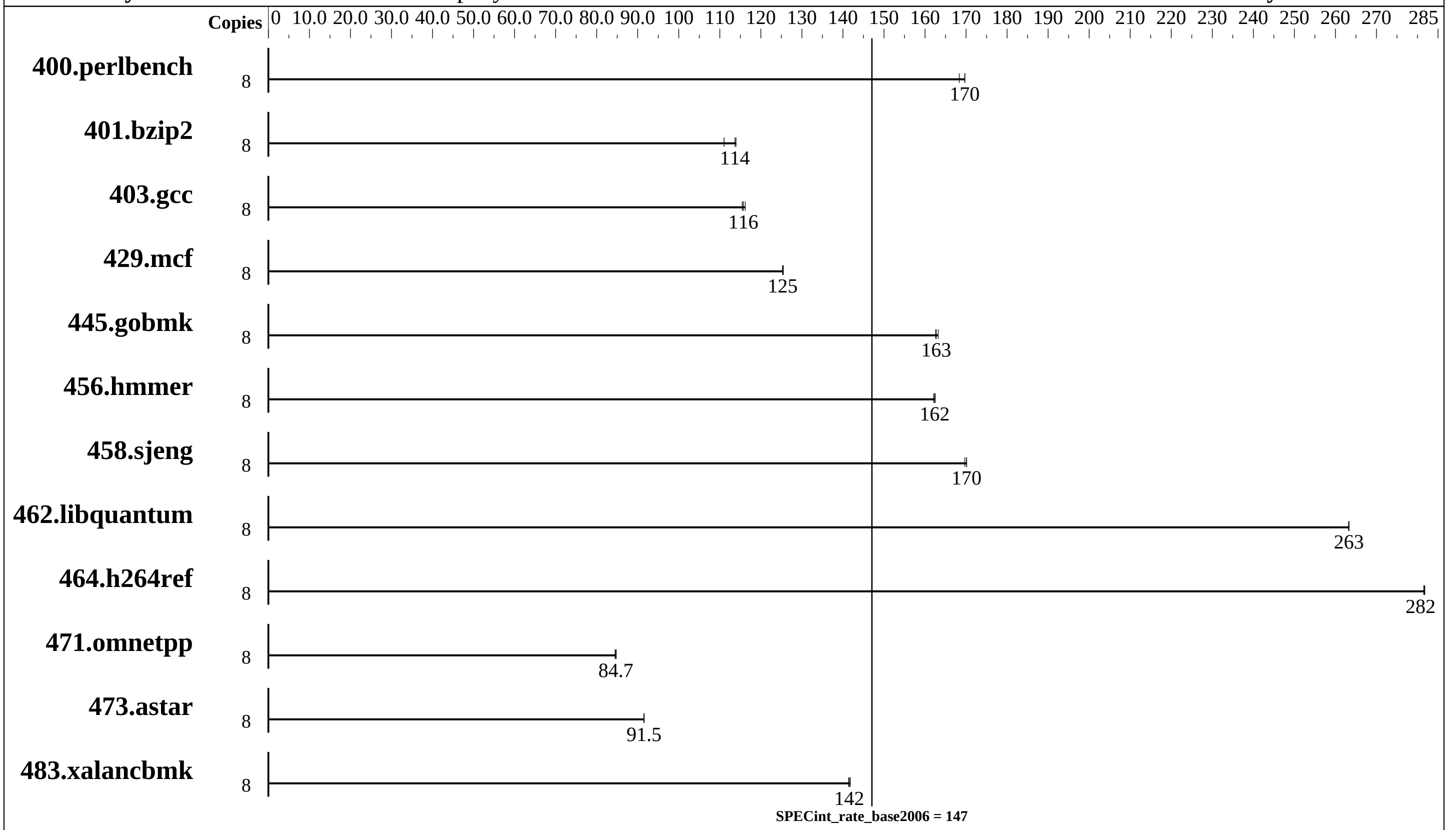
ProLiant DL160 G5p
(3.20 GHz, Intel Xeon X5482)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 147

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewelett-Packard Company

Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon X5482
CPU Characteristics:	3.20 GHz, 2x6 MB L2 Shared, 1600 MHz system bus
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-6400F CL5)
Disk Subsystem:	1x72 GB 15 K SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result

Hewlett-Packard Company

ProLiant DL160 G5p
(3.20 GHz, Intel Xeon X5482)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 147

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewelett-Packard Company
Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	461	170	464	168	**461**	**170**							
401.bzip2	8	695	111	**679**	**114**	677	114							
403.gcc	8	**556**	**116**	558	115	554	116							
429.mcf	8	582	125	581	125	**582**	**125**							
445.gobmk	8	**516**	**163**	516	163	514	163							
456.hmmer	8	**460**	**162**	460	162	459	162							
458.sjeng	8	569	170	570	170	**569**	**170**							
462.libquantum	8	**630**	**263**	630	263	630	263							
464.h264ref	8	629	282	**629**	**282**	628	282							
471.omnetpp	8	590	84.8	592	84.5	**590**	**84.7**							
473.astar	8	614	91.5	613	91.6	**614**	**91.5**							
483.xalancbmk	8	**390**	**142**	389	142	390	141							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores except
 for 462.libquantum peak
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS configuration:
Adjacent Cache Line Prefetch Disabled
Hardware Prefetch Disabled
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 20:33:45 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 29 October 2008.