SPEC® CINT2006 Result

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Fujitsu Siemens Computers

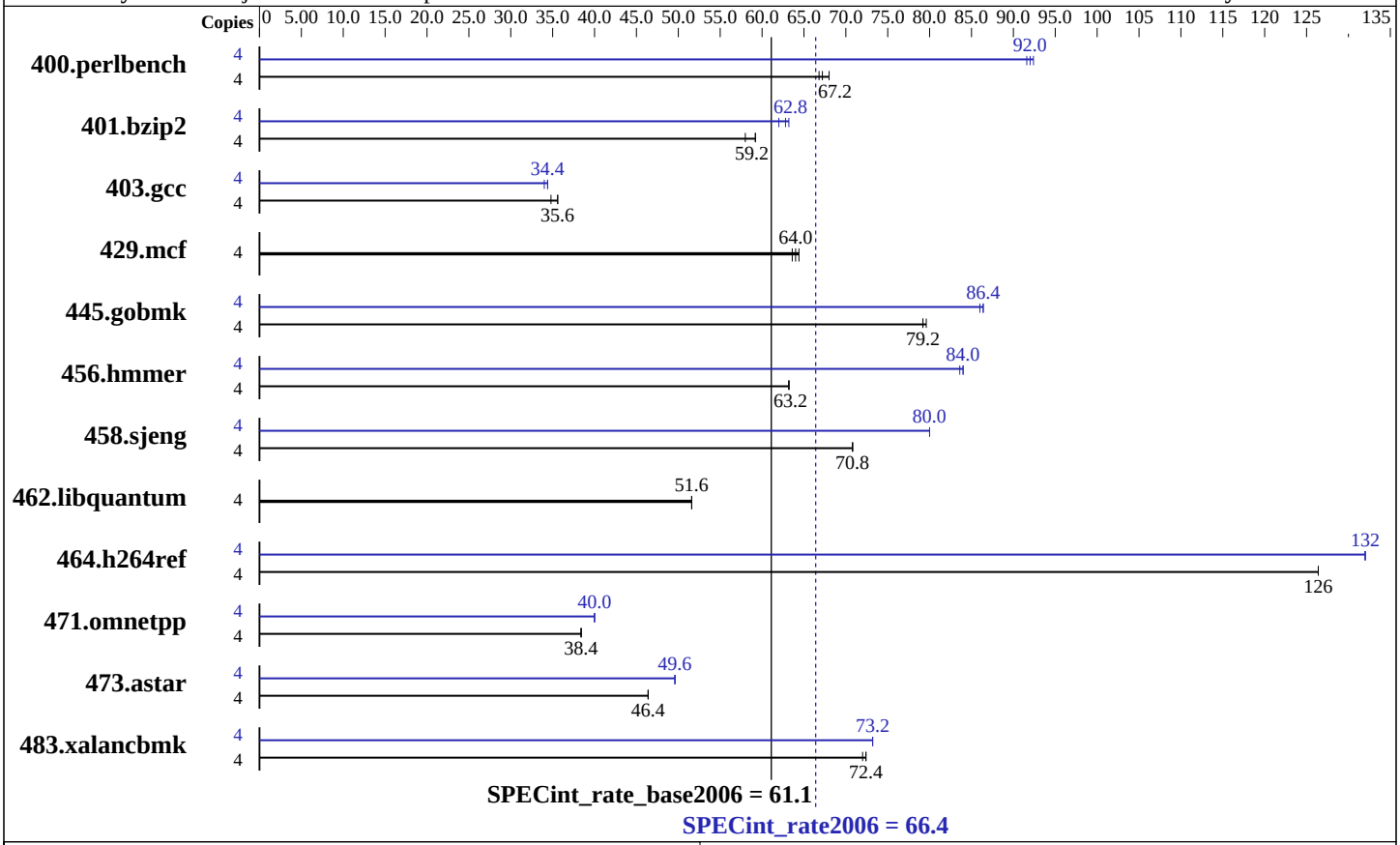
CELSIUS M460, Intel Core 2 Quad Q9650 processor

SPECint®_rate2006 = 66.4

SPECint_rate_base2006 = 61.1

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Sep-2008
Hardware Availability: Sep-2008
Software Availability: Feb-2008



Hardware

CPU Name:	Intel Core 2 Quad Q9650
CPU Characteristics:	
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1 GB PC2-6400 CL6 SDRAM)
Disk Subsystem:	1 x 400 GB SATA 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate (x64)
Compiler:	Intel C++ Compiler for applications running on IA-32, Version 10.1, Build 20080212 Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20080212 Microsoft Visual Studio 2005 with SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap Library Version 9 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	576	68.0	**583**	**67.2**	584	66.8	4	**425**	**92.0**	424	92.4	426	91.6
401.bzip2	4	652	59.2	664	58.0	**652**	**59.2**	4	611	63.2	**615**	**62.8**	622	62.0
403.gcc	4	**909**	**35.6**	908	35.6	921	34.8	4	941	34.4	**942**	**34.4**	946	34.0
429.mcf	4	**569**	**64.0**	573	63.6	566	64.4	4	**569**	**64.0**	573	63.6	566	64.4
445.gobmk	4	528	79.6	529	79.2	**529**	**79.2**	4	486	86.4	**487**	**86.4**	487	86.0
456.hmmer	4	592	63.2	592	63.2	**592**	**63.2**	4	446	83.6	444	84.0	**444**	**84.0**
458.sjeng	4	684	70.8	**684**	**70.8**	685	70.8	4	**606**	**80.0**	606	80.0	606	80.0
462.libquantum	4	**1609**	**51.6**	1609	51.6	1610	51.6	4	**1609**	**51.6**	1609	51.6	1610	51.6
464.h264ref	4	701	126	**701**	**126**	700	126	4	671	132	671	132	**671**	**132**
471.omnetpp	4	653	38.4	653	38.4	**653**	**38.4**	4	627	40.0	628	40.0	**627**	**40.0**
473.astar	4	606	46.4	**607**	**46.4**	607	46.4	4	566	49.6	566	49.6	**566**	**49.6**
483.xalancbmk	4	**382**	**72.4**	382	72.0	382	72.4	4	376	73.2	376	73.2	**376**	**73.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Screen Saver was disabled
Power scheme was set to Always On, modified to Turnoff monitor: Never.
```

Platform Notes

```
BIOS default settings have been used.
```

General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast -Qvec-guard-write -F512000000

C++ benchmarks:
-fast -Qcxx_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icl -Qvc8 -Qc99

401.bzip2: C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Bin\icl.exe
-IC:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Include
-link -LIBPATH:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Lib
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\PlatformSDK\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib

456.hmmer: C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Bin\icl.exe
-IC:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Include
-link -LIBPATH:C:\DevelTools\Intel\Compiler\C++\10.1.021\EM64T\Lib
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\PlatformSDK\lib\AMD64
-link -LIBPATH:C:\ProgramFiles\MicrosoftVisualStudio8\VC\lib

C++ benchmarks:
icl -Qvc8

Peak Portability Flags

401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_WIN32
456.hmmer: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qprefetch -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch -F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec-div- -Qansi-alias -F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive -F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 -F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=block -Qcxx_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=routine -Qcxx_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.xml`

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For other inquiries, please contact webmaster@spec.org.

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