



SPEC® CFP2006 Result

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Fujitsu Siemens Computers

SPECfp®_rate2006 = 34.1

CELSIUS M460, Intel Core 2 Duo E8600 processor

SPECfp_rate_base2006 = 31.4

CPU2006 license: 22

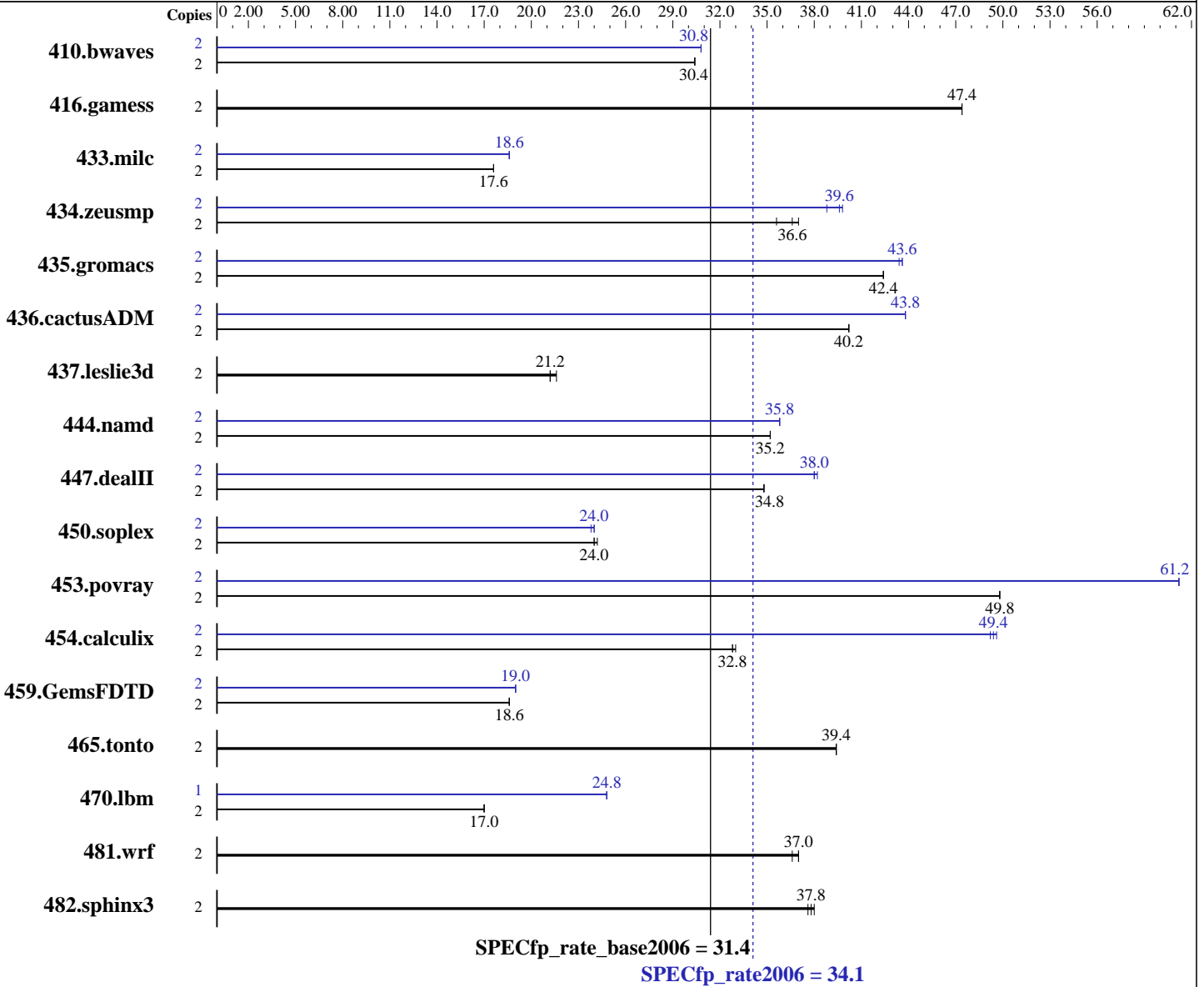
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Oct-2008

Hardware Availability: Sep-2008

Software Availability: Feb-2008



Hardware

CPU Name: Intel Core 2 Duo E8600
 CPU Characteristics: 3333
 CPU MHz: Integrated
 FPU: 2 cores, 1 chip, 2 cores/chip
 CPU(s) enabled: 1 chip
 CPU(s) orderable: 32 KB I + 32 KB D on chip per core
 Primary Cache: 6 MB I+D on chip per chip
 Secondary Cache:

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Software

Operating System: Windows Vista Ultimate (x64)

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L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
 Disk Subsystem: 1 x 400 GB SATA 7200 RPM
 Other Hardware: None

Compiler: Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20080212
 Intel Visual Fortran Compiler for applications running on Intel 64, Version 10.1, Build 20080212
 Microsoft Visual Studio 2005 with SP1 (for libraries)

Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other Software: SmartHeap Library Version 8.0 (64 bit) from <http://www.microquill.com/>

Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|------------|-------------|-------------|-------------|-------------|-------------|--|--|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | | |
| 410.bwaves | 2 | 895 | 30.4 | 892 | 30.4 | 891 | 30.4 | 2 | 883 | 30.8 | 882 | 30.8 | 883 | 30.8 | | |
| 416.gamess | 2 | 826 | 47.4 | 826 | 47.4 | 826 | 47.4 | 2 | 826 | 47.4 | 826 | 47.4 | 826 | 47.4 | | |
| 433.milc | 2 | 1048 | 17.6 | 1047 | 17.6 | 1048 | 17.6 | 2 | 987 | 18.6 | 987 | 18.6 | 986 | 18.6 | | |
| 434.zeusmp | 2 | 498 | 36.6 | 491 | 37.0 | 512 | 35.6 | 2 | 459 | 39.6 | 458 | 39.8 | 470 | 38.8 | | |
| 435.gromacs | 2 | 336 | 42.4 | 336 | 42.4 | 336 | 42.4 | 2 | 329 | 43.4 | 328 | 43.6 | 328 | 43.6 | | |
| 436.cactusADM | 2 | 594 | 40.2 | 594 | 40.2 | 594 | 40.2 | 2 | 545 | 43.8 | 545 | 43.8 | 545 | 43.8 | | |
| 437.leslie3d | 2 | 874 | 21.6 | 887 | 21.2 | 885 | 21.2 | 2 | 874 | 21.6 | 887 | 21.2 | 885 | 21.2 | | |
| 444.namd | 2 | 455 | 35.2 | 455 | 35.2 | 455 | 35.2 | 2 | 449 | 35.8 | 448 | 35.8 | 448 | 35.8 | | |
| 447.dealII | 2 | 657 | 34.8 | 656 | 34.8 | 656 | 34.8 | 2 | 600 | 38.2 | 601 | 38.0 | 603 | 38.0 | | |
| 450.soplex | 2 | 697 | 24.0 | 697 | 24.0 | 690 | 24.2 | 2 | 701 | 23.8 | 693 | 24.0 | 695 | 24.0 | | |
| 453.povray | 2 | 213 | 49.8 | 213 | 49.8 | 213 | 49.8 | 2 | 174 | 61.2 | 174 | 61.2 | 174 | 61.2 | | |
| 454.calculix | 2 | 504 | 32.8 | 504 | 32.8 | 501 | 33.0 | 2 | 336 | 49.2 | 334 | 49.4 | 332 | 49.6 | | |
| 459.GemsFDTD | 2 | 1145 | 18.6 | 1144 | 18.6 | 1144 | 18.6 | 2 | 1117 | 19.0 | 1115 | 19.0 | 1116 | 19.0 | | |
| 465.tonto | 2 | 499 | 39.4 | 499 | 39.4 | 499 | 39.4 | 2 | 499 | 39.4 | 499 | 39.4 | 499 | 39.4 | | |
| 470.lbm | 2 | 1607 | 17.0 | 1608 | 17.0 | 1609 | 17.0 | 1 | 554 | 24.8 | 554 | 24.8 | 554 | 24.8 | | |
| 481.wrf | 2 | 610 | 36.6 | 605 | 37.0 | 605 | 37.0 | 2 | 610 | 36.6 | 605 | 37.0 | 605 | 37.0 | | |
| 482.sphinx3 | 2 | 1035 | 37.6 | 1030 | 37.8 | 1028 | 38.0 | 2 | 1035 | 37.6 | 1030 | 37.8 | 1028 | 38.0 | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Screen Saver was disabled
Power scheme was set to Always On, modified to Turnoff monitor: Never.



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Platform Notes

BIOS default settings have been used.

General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase -assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 -TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-fast -F950000000 sh1w64M.lib

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Base Optimization Flags (Continued)

C++ benchmarks:

-fast -Qcxx-features -F950000000 shlW64M.lib

Fortran benchmarks:

-fast -F950000000

Benchmarks using both Fortran and C:

-fast -F950000000

Peak Compiler Invocation

C benchmarks:

icl -Qvc8 -Qc99

C++ benchmarks:

icl -Qvc8

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc8 -Qc99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

470.lbm: -fast -Qunroll2 -Qscalar-rep- -Qprefetch -F950000000

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -fast -Oa -F950000000 shlW64M.lib

447.deallI: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
-F950000000

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Peak Optimization Flags (Continued)

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

416.gamess: basepeak = yes

434.zeusmp: -QxT -O2 -Qprec-div- -Qunroll10 -Qscalar-rep- -F950000000

437.leslie3d: basepeak = yes

459.GemsFDTD: -fast -Qunroll12 -Ob0 -Qprefetch -F950000000

465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

436.cactusADM: -fast -Qunroll12 -Qprefetch -F950000000

454.calculix: -fast -Qunroll-aggressive -F950000000

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.00.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.00.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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