



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®2006 = 26.3

IBM BladeCenter HS21 (Intel Xeon X5470)

SPECfp_base2006 = 25.1

CPU2006 license: 11

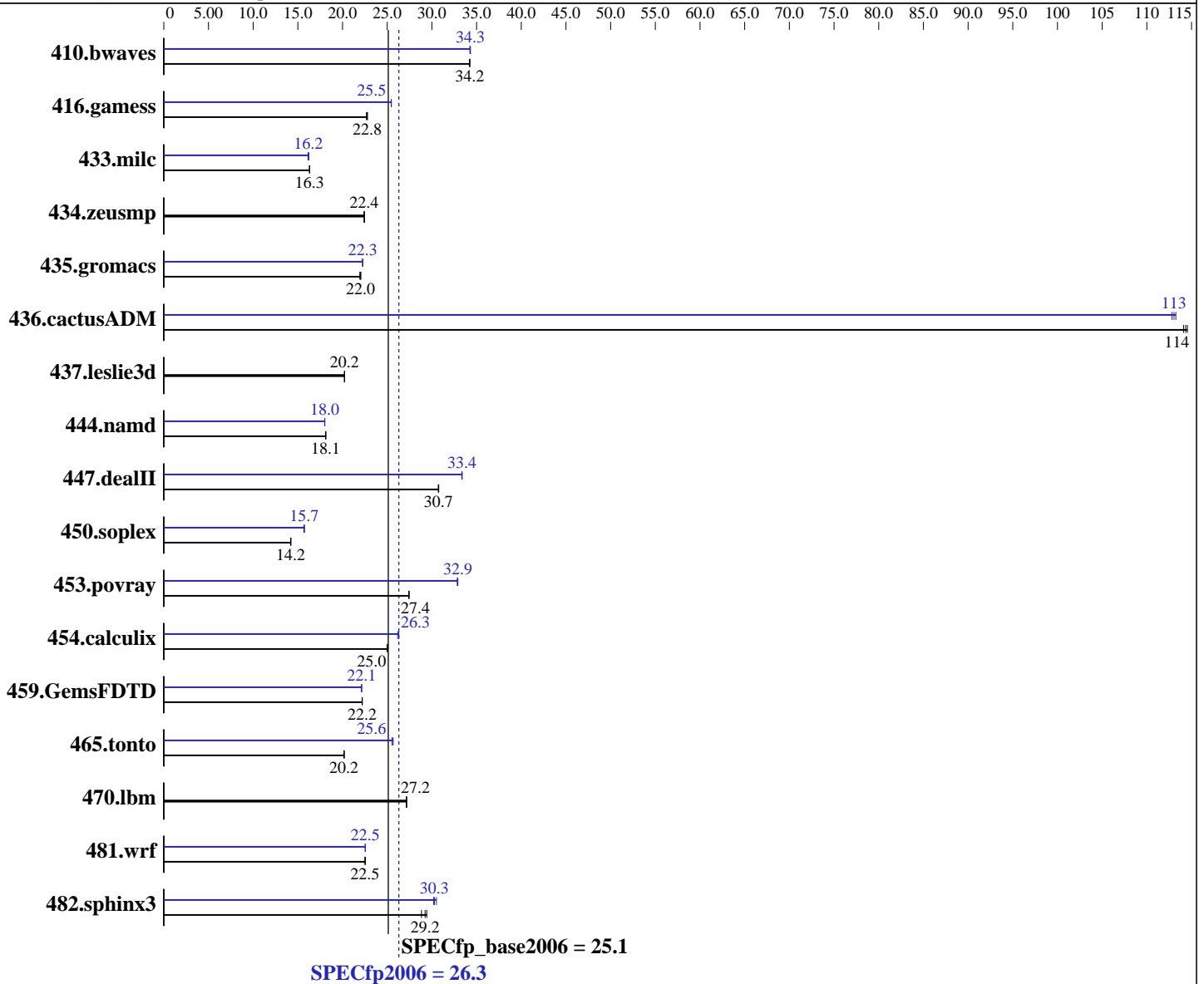
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Sep-2008

Hardware Availability: Oct-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5470
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 36 GB SAS, 10000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>397</u>	<u>34.2</u>	397	34.2	397	34.2	397	34.2	396	34.3	<u>396</u>	<u>34.3</u>
416.gamess	<u>860</u>	<u>22.8</u>	860	22.8	864	22.7	769	25.5	769	25.4	<u>769</u>	<u>25.5</u>
433.milc	564	16.3	<u>563</u>	<u>16.3</u>	562	16.3	565	16.2	569	16.1	<u>568</u>	<u>16.2</u>
434.zeusmp	406	22.4	<u>406</u>	<u>22.4</u>	405	22.4	406	22.4	<u>406</u>	<u>22.4</u>	405	22.4
435.gromacs	<u>324</u>	<u>22.0</u>	324	22.0	326	21.9	<u>321</u>	<u>22.3</u>	321	22.3	321	22.2
436.cactusADM	<u>105</u>	<u>114</u>	104	115	105	114	<u>106</u>	<u>113</u>	106	113	106	113
437.leslie3d	<u>465</u>	<u>20.2</u>	465	20.2	465	20.2	<u>465</u>	<u>20.2</u>	465	20.2	465	20.2
444.namd	<u>443</u>	<u>18.1</u>	443	18.1	442	18.1	445	18.0	445	18.0	<u>445</u>	<u>18.0</u>
447.dealII	372	30.8	<u>372</u>	<u>30.7</u>	372	30.7	<u>343</u>	<u>33.4</u>	343	33.3	343	33.4
450.soplex	<u>587</u>	<u>14.2</u>	586	14.2	587	14.2	530	15.7	<u>531</u>	<u>15.7</u>	532	15.7
453.povray	194	27.4	194	27.4	<u>194</u>	<u>27.4</u>	162	32.9	<u>162</u>	<u>32.9</u>	162	32.8
454.calculix	330	25.0	<u>330</u>	<u>25.0</u>	330	25.0	<u>314</u>	<u>26.3</u>	315	26.2	314	26.3
459.GemsFDTD	478	22.2	<u>478</u>	<u>22.2</u>	478	22.2	<u>479</u>	<u>22.1</u>	480	22.1	479	22.1
465.tonto	487	20.2	488	20.1	<u>488</u>	<u>20.2</u>	383	25.7	385	25.5	<u>385</u>	<u>25.6</u>
470.lbm	506	27.2	<u>506</u>	<u>27.2</u>	506	27.2	506	27.2	<u>506</u>	<u>27.2</u>	506	27.2
481.wrf	<u>496</u>	<u>22.5</u>	495	22.6	496	22.5	496	22.5	495	22.6	<u>495</u>	<u>22.5</u>
482.sphinx3	662	29.4	<u>667</u>	<u>29.2</u>	676	28.8	639	30.5	<u>644</u>	<u>30.3</u>	645	30.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 200M
Hardware Prefetch Enabled, Adjacent Sector Prefetch Enabled
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc
             -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
             -I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

C++ benchmarks (except as noted below):

icpc

```
450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc
            -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
            -I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
          -no-prec-div -static -fno-alias
```

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Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-
-opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch
-parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

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Peak Optimization Flags (Continued)

481.wrf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.html>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.13.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.13.xml>

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