



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY TX150 S6, Intel Pentium Dual Core E2200, 2.20 GHz

**SPECint®\_rate2006 = 25.0**

**SPECint\_rate\_base2006 = 22.5**

CPU2006 license: 22

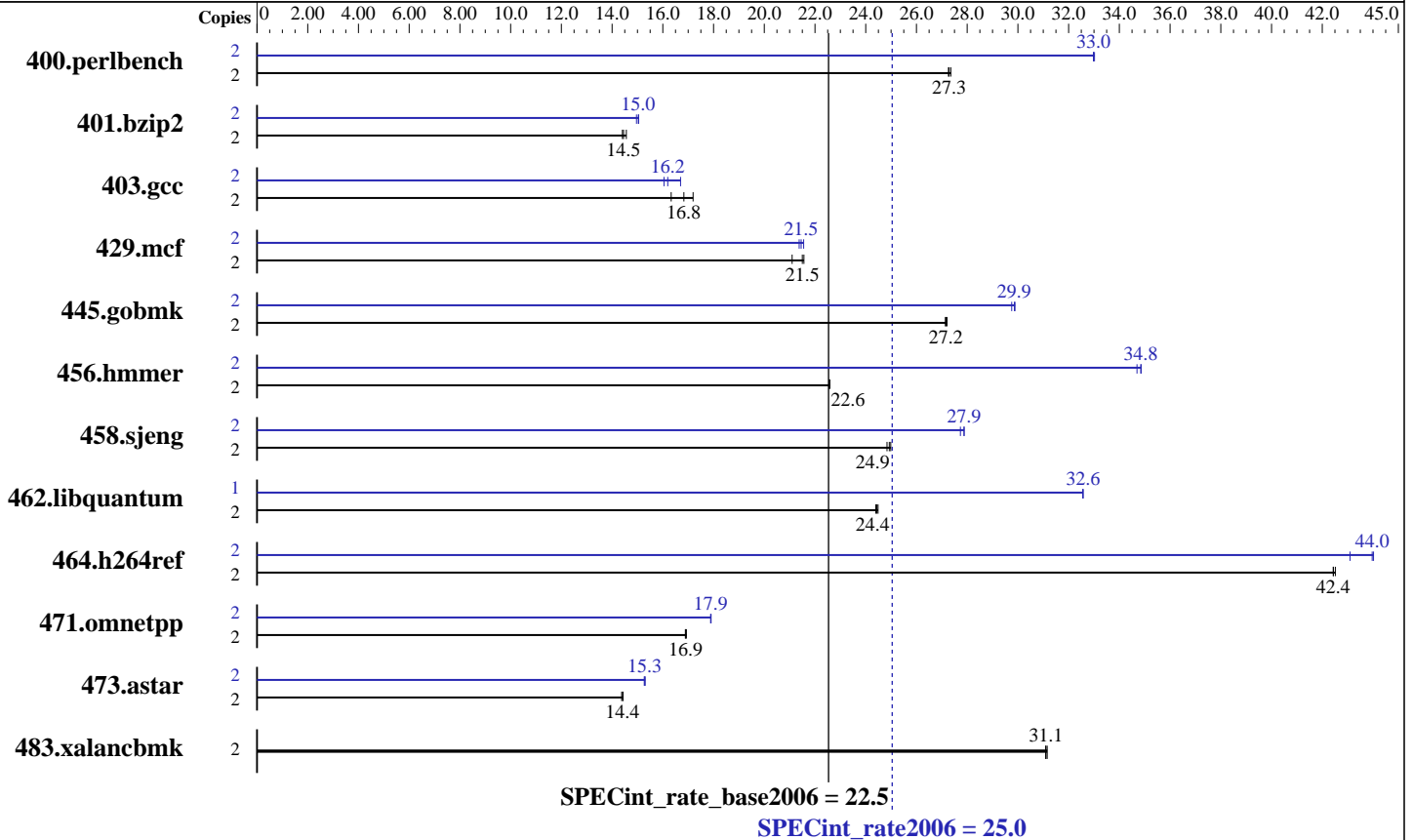
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Sep-2008

Hardware Availability: Sep-2008

Software Availability: May-2008



### Hardware

CPU Name: Intel Pentium Dual Core E2200  
 CPU Characteristics: 800 MHz system bus  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CL 6-6-6, ECC)  
 Disk Subsystem: 1x SATA, 80 GB, 7200 rpm  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64) SP2, kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap Library, Version 8.1  
 binutils-2.17.50.0.5-0.1.x86\_64



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	<b>716</b>	<b>27.3</b>	717	27.3	714	27.4	2	592	33.0	<b>592</b>	<b>33.0</b>	592	33.0
401.bzip2	2	1325	14.6	<b>1335</b>	<b>14.5</b>	1340	14.4	2	1283	15.0	1290	15.0	<b>1284</b>	<b>15.0</b>
403.gcc	2	936	17.2	986	16.3	<b>957</b>	<b>16.8</b>	2	<b>994</b>	<b>16.2</b>	964	16.7	1003	16.0
429.mcf	2	865	21.1	<b>848</b>	<b>21.5</b>	846	21.6	2	<b>850</b>	<b>21.5</b>	853	21.4	846	21.6
445.gobmk	2	<b>772</b>	<b>27.2</b>	771	27.2	773	27.1	2	<b>703</b>	<b>29.9</b>	705	29.8	702	29.9
456.hammer	2	826	22.6	827	22.6	<b>827</b>	<b>22.6</b>	2	<b>536</b>	<b>34.8</b>	535	34.9	538	34.7
458.sjeng	2	<b>971</b>	<b>24.9</b>	974	24.8	969	25.0	2	873	27.7	868	27.9	<b>868</b>	<b>27.9</b>
462.libquantum	2	1699	24.4	1692	24.5	<b>1696</b>	<b>24.4</b>	1	<b>636</b>	<b>32.6</b>	636	32.6	636	32.6
464.h264ref	2	1041	42.5	<b>1043</b>	<b>42.4</b>	1043	42.4	2	1027	43.1	<b>1007</b>	<b>44.0</b>	1005	44.0
471.omnetpp	2	740	16.9	738	16.9	<b>739</b>	<b>16.9</b>	2	698	17.9	699	17.9	<b>699</b>	<b>17.9</b>
473.astar	2	<b>974</b>	<b>14.4</b>	973	14.4	977	14.4	2	<b>918</b>	<b>15.3</b>	917	15.3	920	15.3
483.xalancbmk	2	443	31.2	<b>444</b>	<b>31.1</b>	444	31.1	2	443	31.2	<b>444</b>	<b>31.1</b>	444	31.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores (default)

## Platform Notes

BIOS configuration:  
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable

## General Notes

For information about Fujitsu Siemens Computers please see:  
<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include
```

C++ benchmarks:

```
/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include
```



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmarheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include

401.bzip2: icc  
456.hmmer: icc

C++ benchmarks:  
/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-alloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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