SPEC® CINT2006 Result

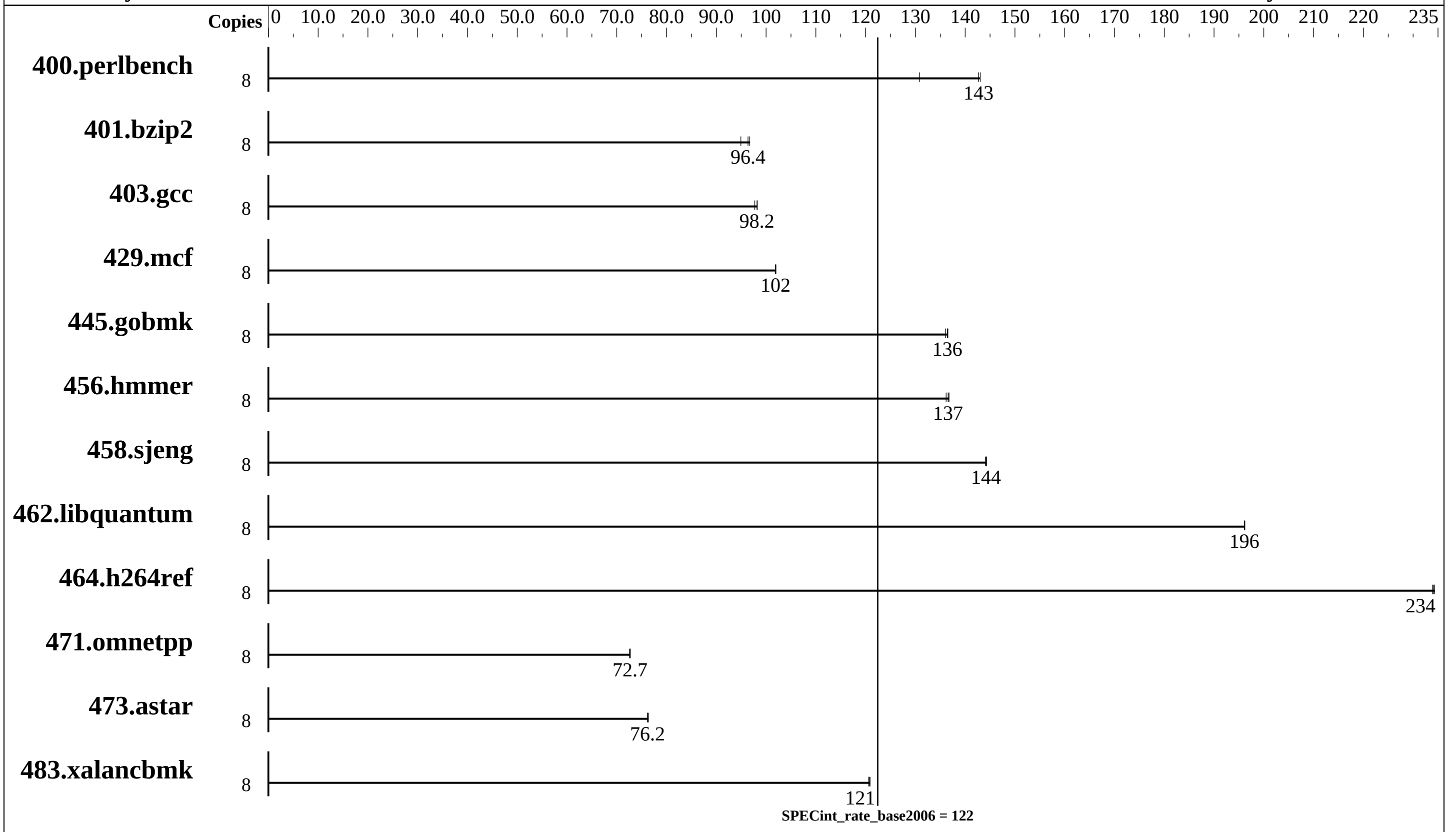
Dell Inc.

PowerEdge M600 (Intel Xeon E5430, 2.66 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 122

CPU2006 license: 55		**Test date:**	Sep-2008
Test sponsor:	Dell Inc.	**Hardware Availability:**	Sep-2008
Tested by:	Dell Inc.	**Software Availability:**	Nov-2008



Hardware

CPU Name:	Intel Xeon E5430
CPU Characteristics:	
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4 x 4 GB 667 MHz ECC CL5 FB-DIMM)
Disk Subsystem:	1 x 80 GB 5400 RPM SATA
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cc_b_11.0.042
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	597	131	**548**	**143**	546	143							
401.bzip2	8	813	94.9	798	96.7	**801**	**96.4**							
403.gcc	8	656	98.2	659	97.7	**656**	**98.2**							
429.mcf	8	**716**	**102**	715	102	716	102							
445.gobmk	8	617	136	**615**	**136**	615	137							
456.hmmer	8	**547**	**137**	546	137	548	136							
458.sjeng	8	**671**	**144**	671	144	672	144							
462.libquantum	8	**845**	**196**	845	196	845	196							
464.h264ref	8	757	234	756	234	**756**	**234**							
471.omnetpp	8	689	72.5	**688**	**72.7**	688	72.7							
473.astar	8	737	76.2	736	76.3	**737**	**76.2**							
483.xalancbmk	8	458	121	**457**	**121**	457	121							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

Platform Notes

```
BIOS Settings:
Hardware Prefetcher = Disabled (Default = Enabled)
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)
```

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
taskset was used to bind processes to cores except
for 462.libquantum peak
```

Base Compiler Invocation

C benchmarks:
    icc

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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