SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

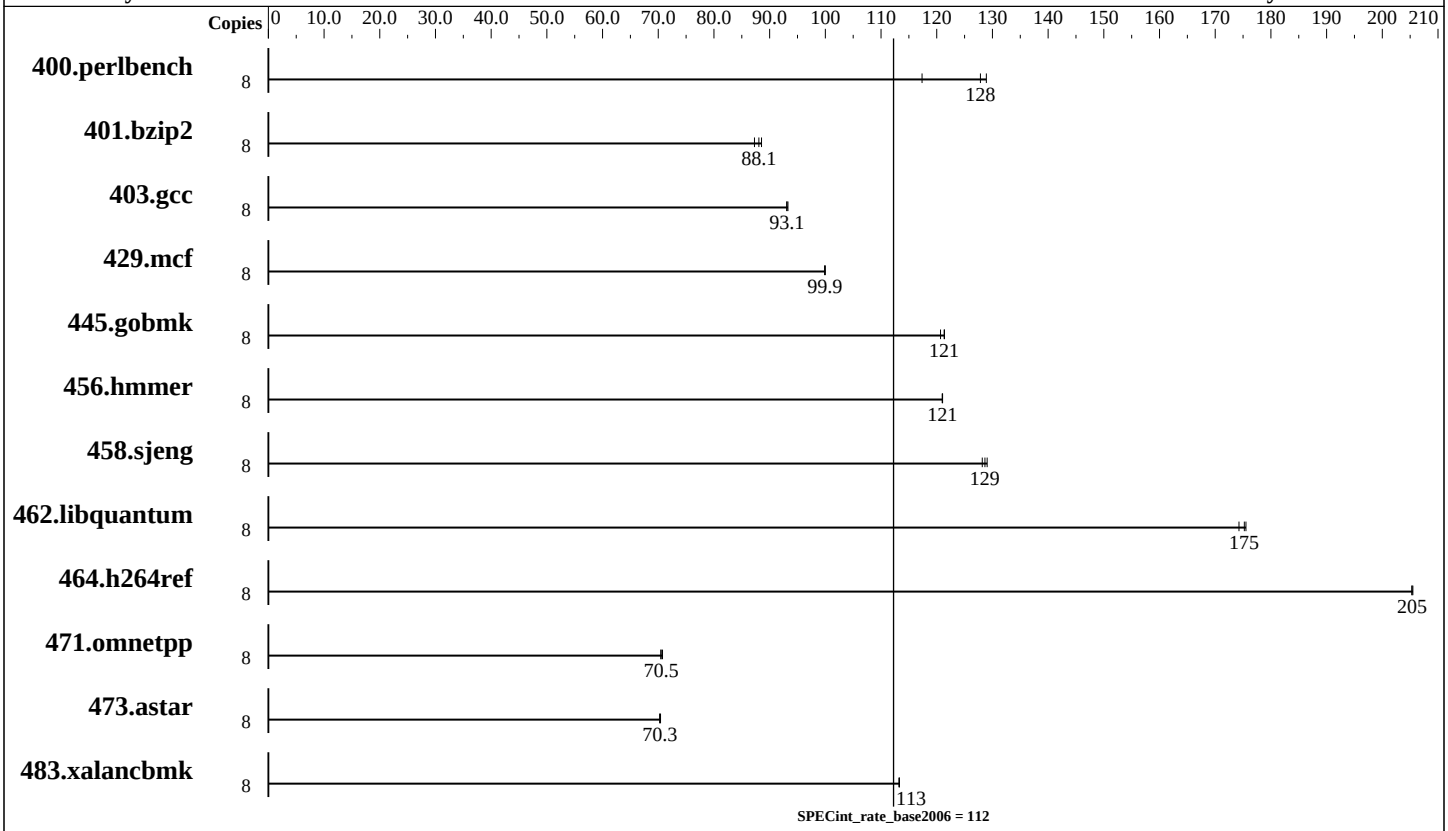
PowerEdge M600 (Intel Xeon E5410, 2.33 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 112

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4x4 GB, DDR2-667 MHz, CL5, Reg, Dual Rank)
Disk Subsystem:	1 x 80 GB SATA 5400 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cc_b_11.0.042
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge M600 (Intel Xeon E5410, 2.33 GHz)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 112

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	666	117	606	129	**611**	**128**							
401.bzip2	8	**877**	**88.1**	885	87.3	872	88.5							
403.gcc	8	692	93.1	**692**	**93.1**	690	93.3							
429.mcf	8	731	99.9	730	100	**730**	**99.9**							
445.gobmk	8	**692**	**121**	695	121	691	121							
456.hmmer	8	**617**	**121**	617	121	617	121							
458.sjeng	8	755	128	750	129	**752**	**129**							
462.libquantum	8	951	174	**946**	**175**	944	176							
464.h264ref	8	862	205	862	205	**862**	**205**							
471.omnetpp	8	**709**	**70.5**	710	70.5	707	70.8							
473.astar	8	799	70.3	**799**	**70.3**	798	70.4							
483.xalancbmk	8	487	113	**487**	**113**	488	113							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

Platform Notes

```
BIOS Settings:
Hardware Prefetcher = Disabled (Default = Enabled)
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)
```

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
taskset was used to bind processes to cores except
for 462.libquantum peak
```

Base Compiler Invocation

C benchmarks:

```
icc
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge M600 (Intel Xeon E5410, 2.33 GHz)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 112

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 22:15:16 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 14 October 2008.