SPEC® CINT2006 Result

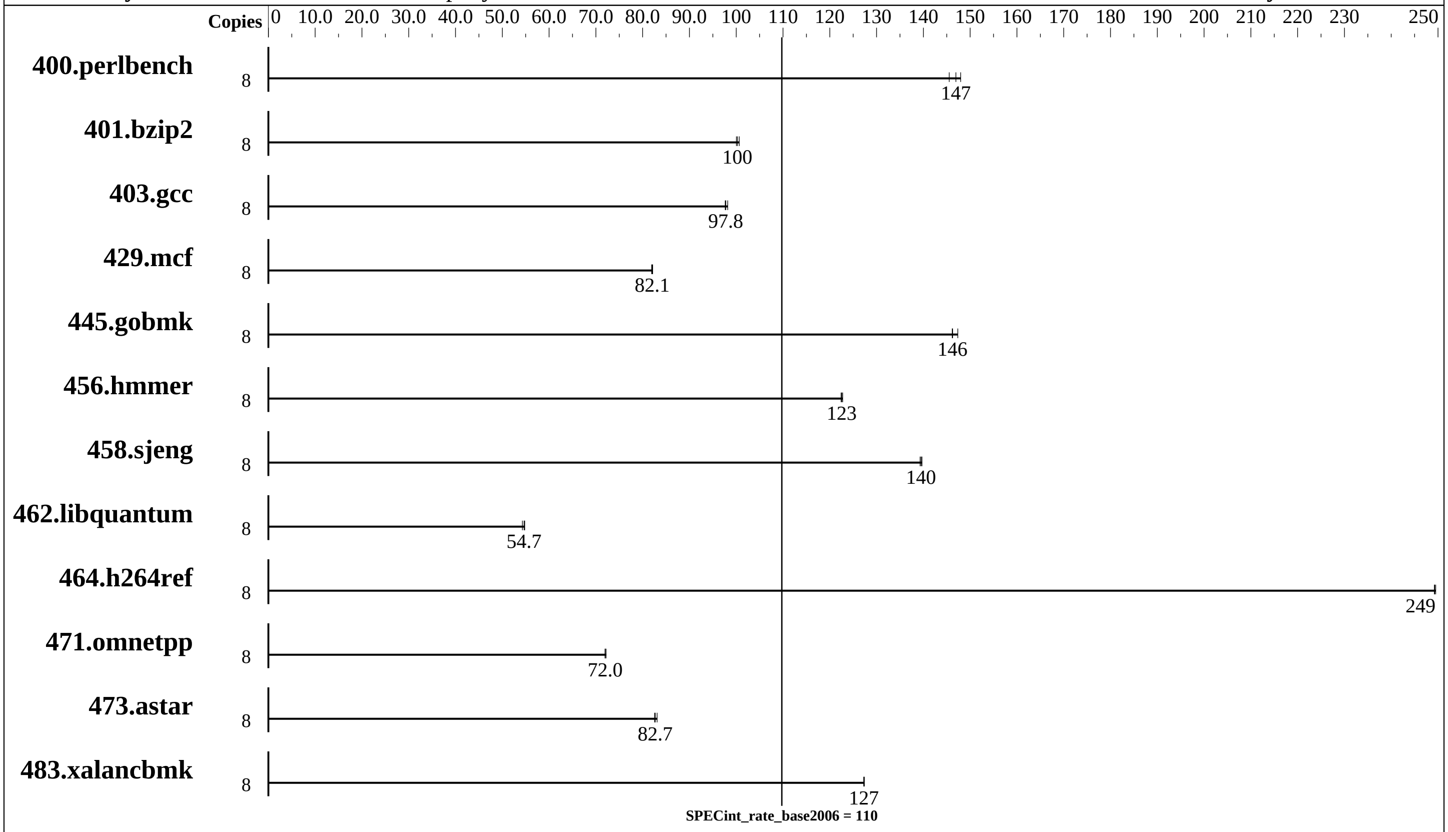
Hewlett-Packard Company

ProLiant DL380 G5
(3.0 GHz, Intel Xeon E5450)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 110

CPU2006 license: 3		**Test date:**	Sep-2008
Test sponsor:	Hewlett-Packard Company	**Hardware Availability:**	Jan-2008
Tested by:	Hewlett-Packard Company	**Software Availability:**	Jun-2008



Hardware

CPU Name:	Intel Xeon E5450
CPU Characteristics:	3.0 GHz, 2x6 MB L2 shared, 1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem:	1x146 GB 15 K SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20080602 Package ID: l_cc_p_10.1.017
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library 8.1 binutils-2.17.50

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**532**	**147**	537	146	528	148							
401.bzip2	8	**770**	**100**	767	101	772	100							
403.gcc	8	**659**	**97.8**	660	97.6	656	98.2							
429.mcf	8	**889**	**82.1**	891	81.9	888	82.1							
445.gobmk	8	569	147	574	146	**574**	**146**							
456.hmmer	8	**609**	**123**	610	122	608	123							
458.sjeng	8	693	140	**694**	**140**	695	139							
462.libquantum	8	3054	54.3	**3033**	**54.7**	3025	54.8							
464.h264ref	8	710	250	**710**	**249**	710	249							
471.omnetpp	8	**694**	**72.0**	695	72.0	693	72.2							
473.astar	8	**679**	**82.7**	680	82.6	676	83.1							
483.xalancbmk	8	**434**	**127**	434	127	433	127							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS configuration:
  Power Regulator set to Static High Performance Mode
  Adjacent Sector Prefetch Disabled
  Hardware Prefetcher Disabled
```

Base Compiler Invocation

C benchmarks:
    `icc`

C++ benchmarks:
    `icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/cpu2006/SmartHeap_8.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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