



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 89.6

CELSIUS R650, Intel Xeon X5270 processor

SPECint_rate_base2006 = 84.2

CPU2006 license: 22

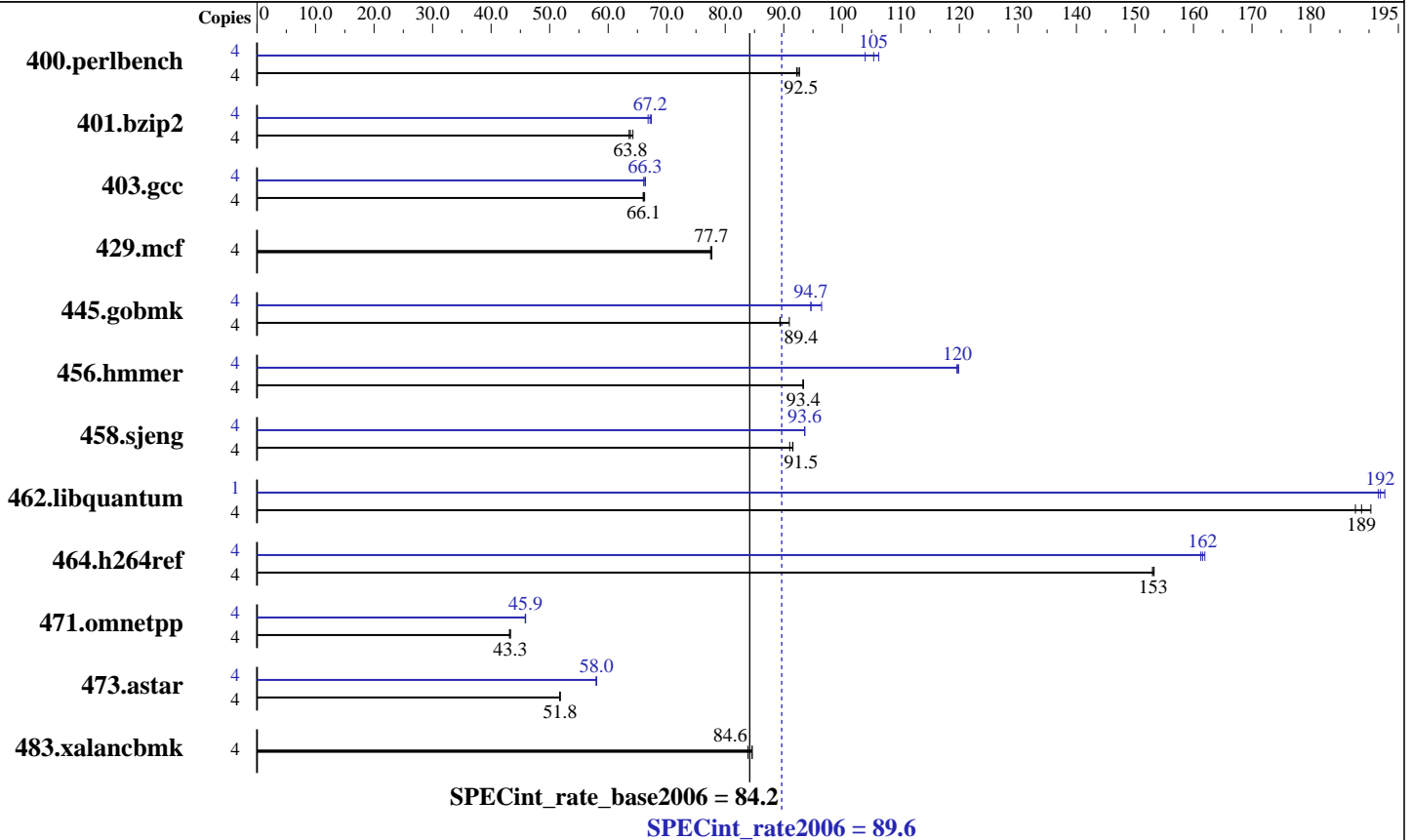
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Sep-2008

Hardware Availability: Oct-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5270
 CPU Characteristics:
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP2
 kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux
 Build 20080730, Package ID l_cproc_b_11.0.042
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User, Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<u>423</u>	<u>92.5</u>	424	92.2	422	92.7	4	376	104	368	106	<u>371</u>	<u>105</u>
401.bzip2	4	607	63.6	601	64.2	<u>605</u>	<u>63.8</u>	4	573	67.4	578	66.8	<u>575</u>	<u>67.2</u>
403.gcc	4	<u>487</u>	<u>66.1</u>	486	66.2	488	66.0	4	485	66.4	487	66.1	<u>486</u>	<u>66.3</u>
429.mcf	4	471	77.5	469	77.7	<u>470</u>	<u>77.7</u>	4	471	77.5	469	77.7	<u>470</u>	<u>77.7</u>
445.gobmk	4	461	90.9	<u>469</u>	<u>89.4</u>	470	89.3	4	435	96.5	<u>443</u>	<u>94.7</u>	443	94.6
456.hammer	4	400	93.4	<u>400</u>	<u>93.4</u>	400	93.3	4	311	120	312	120	<u>312</u>	<u>120</u>
458.sjeng	4	<u>529</u>	<u>91.5</u>	529	91.6	532	91.0	4	<u>517</u>	<u>93.6</u>	517	93.5	517	93.6
462.libquantum	4	442	188	435	190	<u>439</u>	<u>189</u>	1	108	192	108	193	<u>108</u>	<u>192</u>
464.h264ref	4	579	153	578	153	<u>578</u>	<u>153</u>	4	547	162	549	161	<u>548</u>	<u>162</u>
471.omnetpp	4	578	43.3	<u>578</u>	<u>43.3</u>	580	43.1	4	546	45.8	545	45.9	<u>545</u>	<u>45.9</u>
473.astar	4	543	51.7	542	51.8	<u>542</u>	<u>51.8</u>	4	<u>484</u>	<u>58.0</u>	484	58.0	485	57.9
483.xalancbmk	4	329	83.9	<u>326</u>	<u>84.6</u>	326	84.6	4	329	83.9	<u>326</u>	<u>84.6</u>	326	84.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except
for 462.libquantum peak

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M

Platform Notes

BIOS configuration:
Enhanced Speedstep Technology = Disable
C1 Enhanced Mode = Disable
Adjacent Sector Prefetch = Disable
SnoopFilter = Enable

General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>



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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias
403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3
429.mcf: basepeak = yes
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias
456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4
462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

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Peak Optimization Flags (Continued)

```
473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
          -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
          -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap
```

```
483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.08.html>

<http://www.spec.org/cpu2006/flags/FSC-SLES10-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.08.xml>

<http://www.spec.org/cpu2006/flags/FSC-SLES10-Platform.xml>

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For other inquiries, please contact webmaster@spec.org.

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