SPEC® CFP2006 Result

Fujitsu Siemens Computers

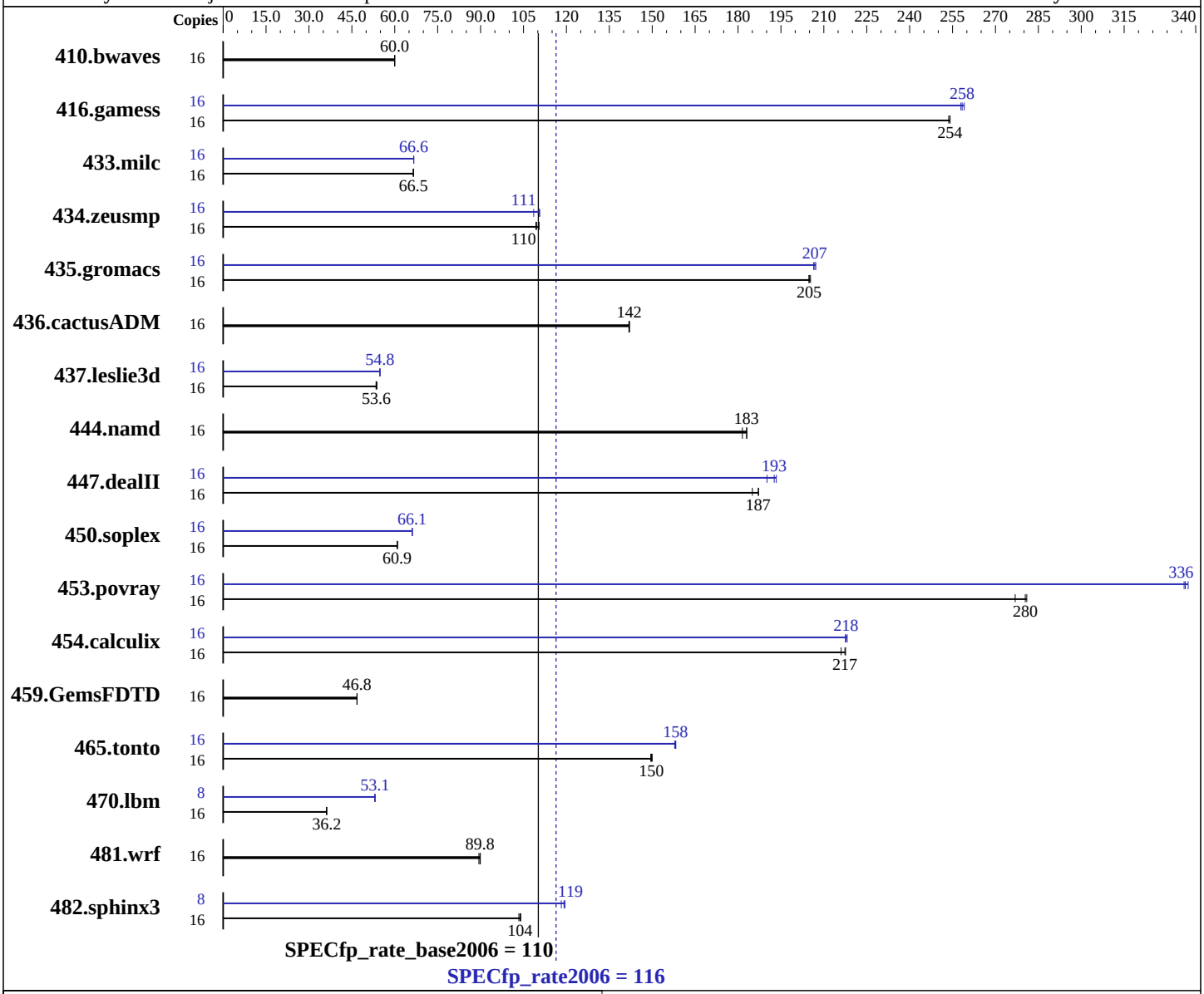
PRIMERGY RX600 S4, Intel Xeon E7430, 2.13 GHz

SPECfp®_rate2006 = 116

SPECfp_rate_base2006 = 110

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Sep-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon E7430
CPU Characteristics:	1067 MHz system bus
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip
CPU(s) orderable:	1,2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip, 3 MB shared / 2 cores

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	64-bit
Peak Pointers:	32/64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 64 GB (16x4 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x SAS, 73 GB, 15000 rpm
Other Hardware: None

Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	3628	59.9	3624	60.0	**3625**	**60.0**	16	3628	59.9	3624	60.0	**3625**	**60.0**
416.gamess	16	1233	254	1235	254	**1233**	**254**	16	**1213**	**258**	1215	258	1209	259
433.milc	16	2211	66.4	**2208**	**66.5**	2207	66.6	16	**2204**	**66.6**	2205	66.6	2204	66.6
434.zeusmp	16	**1329**	**110**	1318	110	1332	109	16	1316	111	**1317**	**111**	1341	109
435.gromacs	16	558	205	556	205	**557**	**205**	16	553	206	551	207	**552**	**207**
436.cactusADM	16	1348	142	**1347**	**142**	1346	142	16	1348	142	**1347**	**142**	1346	142
437.leslie3d	16	2814	53.4	**2808**	**53.6**	2798	53.7	16	2745	54.8	2743	54.8	**2743**	**54.8**
444.namd	16	**701**	**183**	701	183	707	182	16	**701**	**183**	701	183	707	182
447.dealII	16	989	185	978	187	**979**	**187**	16	**950**	**193**	963	190	947	193
450.soplex	16	2189	61.0	**2192**	**60.9**	2194	60.8	16	2019	66.1	2017	66.1	**2019**	**66.1**
453.povray	16	307	277	**304**	**280**	303	281	16	252	337	**253**	**336**	253	336
454.calculix	16	611	216	607	218	**607**	**217**	16	607	218	605	218	**606**	**218**
459.GemsFDTD	16	3625	46.8	**3626**	**46.8**	3629	46.8	16	3625	46.8	**3626**	**46.8**	3629	46.8
465.tonto	16	1050	150	**1051**	**150**	1053	149	16	**995**	**158**	995	158	997	158
470.lbm	16	6074	36.2	6078	36.2	**6076**	**36.2**	8	**2071**	**53.1**	2074	53.0	2068	53.1
481.wrf	16	**1990**	**89.8**	1999	89.4	1988	89.9	16	**1990**	**89.8**	1999	89.4	1988	89.9
482.sphinx3	16	**3003**	**104**	2997	104	3016	103	8	**1308**	**119**	1305	119	1319	118

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
All binaries were built with 64-bit mode except:
437.leslie3d, 450.soplex and 482.sphinx3 in peak
were built with 32-bit mode.
```

Submit Notes

```
The config file option 'submit' was used.
 taskset has been used to bind processes to cores except
 for 436.cactusADM peak
 For peak modules using 1/2 the number of available cores,
 each copy was assigned to a single L2 cache using mysubmit.pl script.
 See the flags description file for mysubmit.pl details.
```

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
High Bandwidth option = Enable

General Notes

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Fortran benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

482.sphinx3: `/opt/intel/Compiler/11.0/042/bin/ia32/icc`
`-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib`
`-I/opt/intel/Compiler/11.0/042/ipp/ia32/include`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/Compiler/11.0/042/bin/ia32/icpc`
`-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib`
`-I/opt/intel/Compiler/11.0/042/ipp/ia32/include`

Fortran benchmarks (except as noted below):
`ifort`

437.leslie3d: `/opt/intel/Compiler/11.0/042/bin/ia32/ifort`
`-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib`
`-I/opt/intel/Compiler/11.0/042/ipp/ia32/include`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

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Peak Optimization Flags (Continued)

434.zeusmp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static`

437.leslie3d: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `-xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.11.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.08.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.11.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.08.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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