SPEC® CFP2006 Result

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Dell Inc.

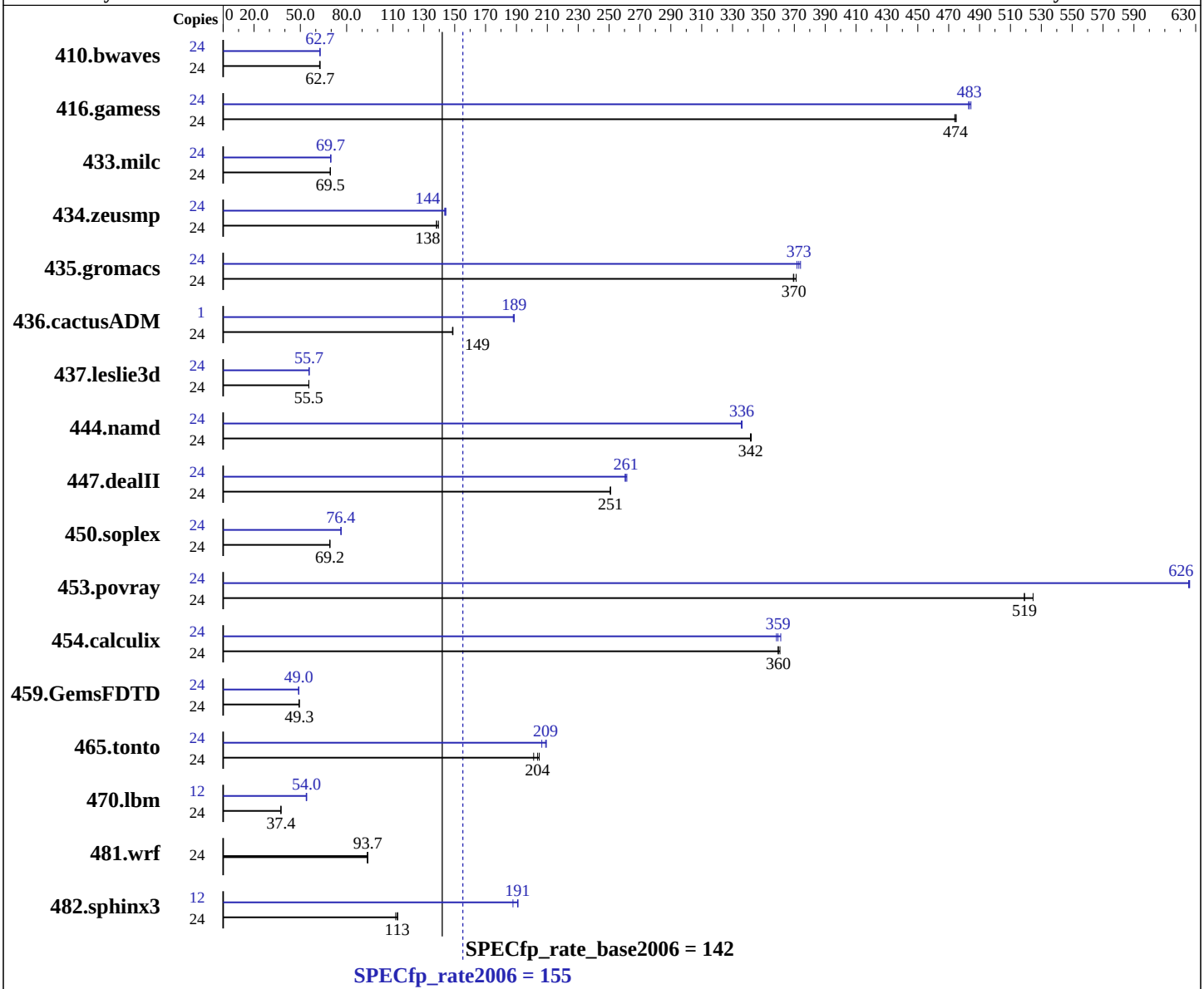
PowerEdge R900 (Intel Xeon X7460, 2.66 GHz)

SPECfp®_rate2006 = 155

SPECfp_rate_base2006 = 142

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon X7460
CPU Characteristics:	
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	24 cores, 4 chips, 6 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	9 MB I+D on chip per chip, 3 MB shared / 2 cores

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 16 MB I+D on chip per chip
Other Cache: None
Memory: 64 GB (16 x 4GB DDR2-667 FBDIMM)
Disk Subsystem: 2 x 36 GB SAS 15000 RPM (RAID-0) for OS, 1 x 73 GB SAS 10000 RPM for benchmark
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	5221	62.5	**5198**	**62.7**	5198	62.7	24	**5198**	**62.7**	5199	62.7	5198	62.8
416.gamess	24	**991**	**474**	990	475	992	474	24	970	484	973	483	**972**	**483**
433.milc	24	**3172**	**69.5**	3174	69.4	3172	69.5	24	3161	69.7	**3161**	**69.7**	3160	69.7
434.zeusmp	24	**1579**	**138**	1565	140	1580	138	24	**1519**	**144**	1514	144	1521	144
435.gromacs	24	462	371	464	369	**464**	**370**	24	461	372	**459**	**373**	458	374
436.cactusADM	24	1930	149	**1929**	**149**	1926	149	1	63.6	188	63.4	189	**63.4**	**189**
437.leslie3d	24	4062	55.5	**4063**	**55.5**	4064	55.5	24	4051	55.7	**4052**	**55.7**	4052	55.7
444.namd	24	563	342	563	342	**563**	**342**	24	**573**	**336**	573	336	573	336
447.dealII	24	**1095**	**251**	1095	251	1094	251	24	1050	262	1055	260	**1052**	**261**
450.soplex	24	2901	69.0	**2893**	**69.2**	2893	69.2	24	2621	76.4	**2621**	**76.4**	2622	76.3
453.povray	24	**246**	**519**	243	525	246	519	24	**204**	**626**	204	626	204	625
454.calculix	24	549	361	551	359	**550**	**360**	24	548	361	552	358	**551**	**359**
459.GemsFDTD	24	5184	49.1	**5164**	**49.3**	5161	49.3	24	5210	48.9	5196	49.0	**5199**	**49.0**
465.tonto	24	1174	201	**1159**	**204**	1153	205	24	1128	209	1144	206	**1131**	**209**
470.lbm	24	8828	37.4	8803	37.5	**8805**	**37.4**	12	**3053**	**54.0**	3048	54.1	3064	53.8
481.wrf	24	2872	93.4	2860	93.7	**2861**	**93.7**	24	2872	93.4	2860	93.7	**2861**	**93.7**
482.sphinx3	24	4185	112	**4147**	**113**	4135	113	12	1224	191	1246	188	**1226**	**191**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
For peak modules using 1/2 the number of available cores, each
copy was assigned to a single L2 cache using mysubmit.pl script.
See the flags description file for mysubmit.pl details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

Platform Notes

```
BIOS Settings:
Hardware Prefetcher = Disabled (Default = Enabled)
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)
High Bandwidth = Enabled (Default = Disabled)
```

General Notes

```
All benchmarks compiled in 64-bit mode except 437.leslie3d, 450.soplex
and 482.sphinx3, at peak, are compiled in 32-bit mode
taskset was used to bind processes to cores except
for 436.cactusADM peak
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icc ifort`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
450.soplex: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`

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Base Portability Flags (Continued)

481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Fortran benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

482.sphinx3: `/opt/intel/Compiler/11.0/042/bin/ia32/icc -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib -I/opt/intel/Compiler/11.0/042/ipp/ia32/include`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/Compiler/11.0/042/bin/ia32/icpc -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib -I/opt/intel/Compiler/11.0/042/ipp/ia32/include`

Fortran benchmarks (except as noted below):
`ifort`

437.leslie3d: `/opt/intel/Compiler/11.0/042/bin/ia32/ifort -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib -I/opt/intel/Compiler/11.0/042/ipp/ia32/include`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

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Peak Optimization Flags (Continued)

434.zeusmp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static`

437.leslie3d: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch`

459.GemsFDTD: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch`

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32`

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32`

454.calculix: `-xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.05.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.06.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.05.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.06.xml

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Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 20:56:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 October 2008.