SPEC® CINT2006 Result

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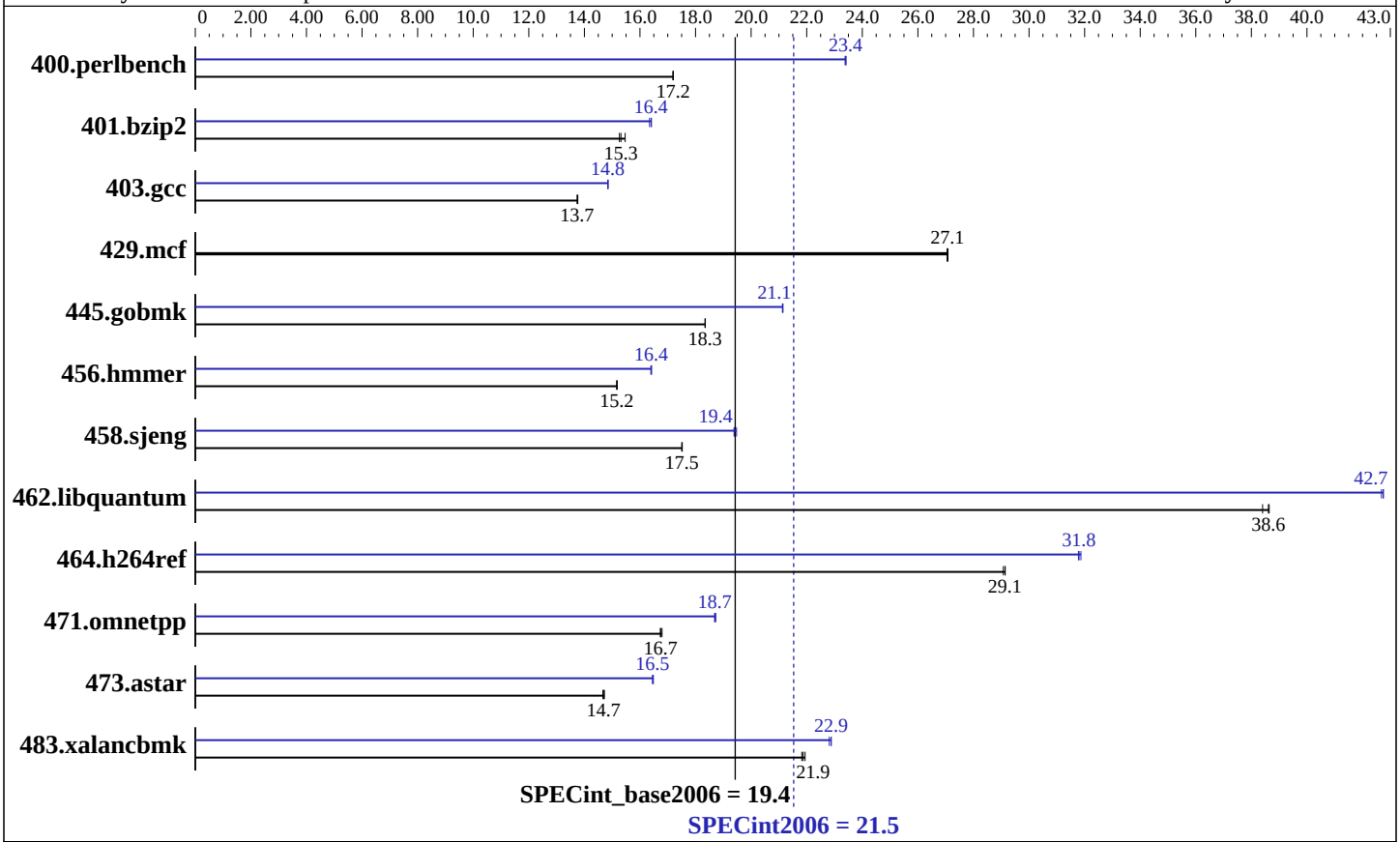
Intel Corporation
Lenovo Thinkpad T400

SPECint®2006 = 21.5

SPECint_base2006 = 19.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Aug-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Duo T9600
CPU Characteristics:	
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Micron PC3-8500 DDR3-1066 CL7)
Disk Subsystem:	Hitachi Travelstar* HTS722020K9SA00 SATA 200GB 7200RPM
Other Hardware:	None

Software

Operating System:	Windows XP Professional SP2
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	568	17.2	**568**	**17.2**	568	17.2	418	23.4	**417**	**23.4**	417	23.4
401.bzip2	624	15.5	**630**	**15.3**	632	15.3	590	16.3	**588**	**16.4**	588	16.4
403.gcc	585	13.8	586	13.7	**586**	**13.7**	542	14.8	**542**	**14.8**	542	14.9
429.mcf	**337**	**27.1**	337	27.1	337	27.1	**337**	**27.1**	337	27.1	337	27.1
445.gobmk	572	18.4	**572**	**18.3**	572	18.3	496	21.1	497	21.1	**496**	**21.1**
456.hmmer	615	15.2	**615**	**15.2**	615	15.2	**569**	**16.4**	569	16.4	568	16.4
458.sjeng	691	17.5	**691**	**17.5**	691	17.5	621	19.5	624	19.4	**624**	**19.4**
462.libquantum	**537**	**38.6**	540	38.4	536	38.6	**485**	**42.7**	485	42.7	485	42.8
464.h264ref	761	29.1	759	29.1	**760**	**29.1**	696	31.8	**696**	**31.8**	694	31.9
471.omnetpp	372	16.8	373	16.7	**373**	**16.7**	**334**	**18.7**	334	18.7	334	18.7
473.astar	477	14.7	479	14.7	**478**	**14.7**	426	16.5	**427**	**16.5**	427	16.5
483.xalancbmk	314	21.9	316	21.8	**315**	**21.9**	303	22.8	302	22.9	**302**	**22.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista32
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
libguide40.lib

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000

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Peak Optimization Flags (Continued)

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive /F512000000 libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always -Qparallel -Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=block -Qcxx_features /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.xml

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