



# SPEC® CFP2006 Result

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Intel Corporation	<b>SPECfp®2006 =</b>	10.8
Lenovo Thinkpad T60	<b>SPECfp_base2006 =</b>	10.6

CPU2006 license: 13

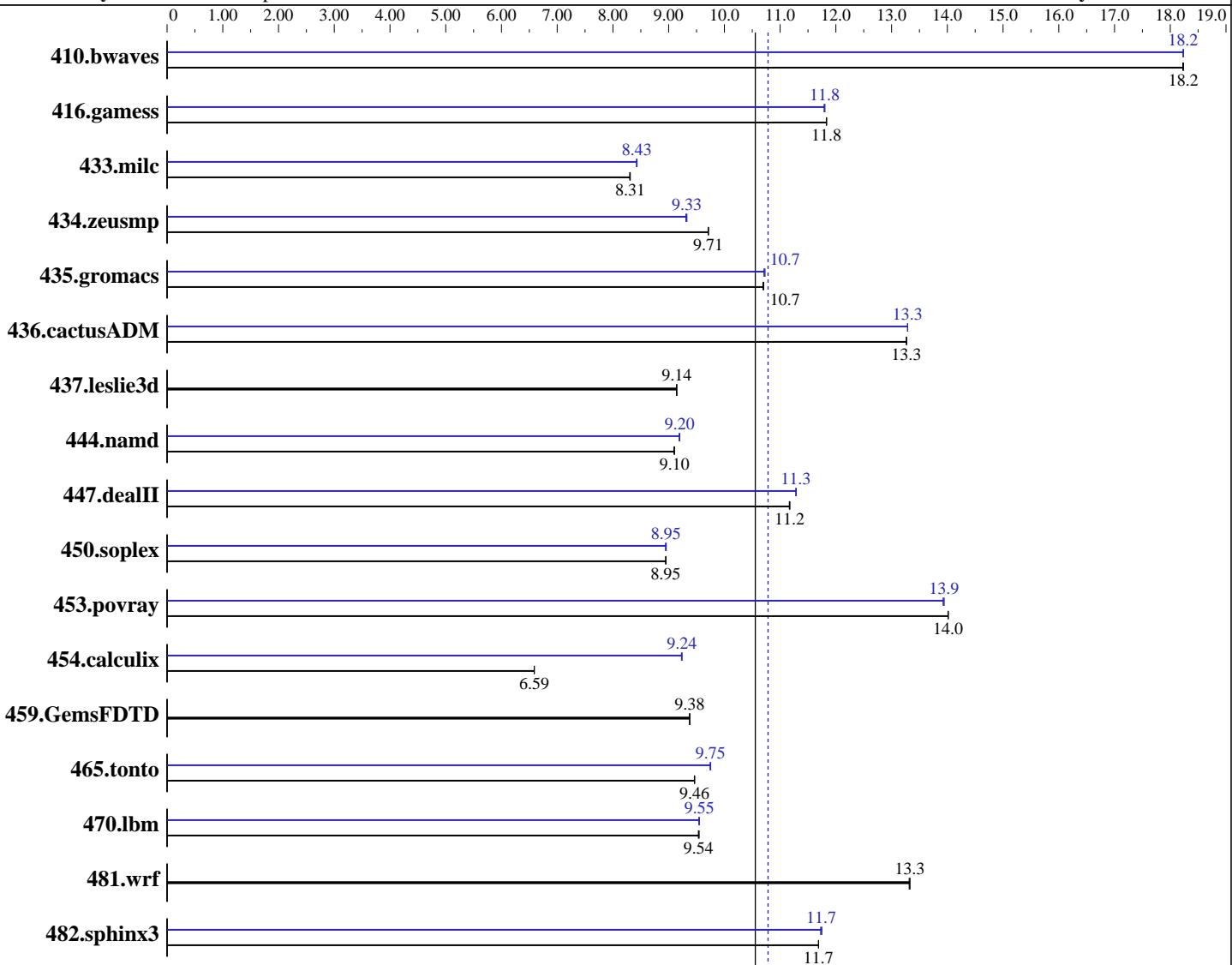
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2008

Hardware Availability: Sep-2006

Software Availability: Nov-2007



**SPECfp\_base2006 = 10.6**

**SPECfp2006 = 10.8**

<b>Hardware</b>		<b>Software</b>	
CPU Name:	Intel Core Duo T2700	Operating System:	Windows XP Professional SP2
CPU Characteristics:		Compiler:	Intel C++ Compiler for IA32 version 10.1
CPU MHz:	2333		Build 20070913 Package ID: w_cc_p_10.1.011
FPU:	Integrated		Intel Fortran Compiler for IA32 version 10.1
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip		Build 20070913 Package ID: w_fc_p_10.1.011
CPU(s) orderable:	1 chip		Microsoft Visual Studio 2005 SP1 (for libraries)
Primary Cache:	32 KB I + 32 KB D on chip per core	Auto Parallel:	Yes
Secondary Cache:	2 MB I+D on chip per chip	File System:	NTFS
Continued on next page		System State:	Default

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L3 Cache:	None	Base Pointers:	32-bit
Other Cache:	None	Peak Pointers:	32-bit
Memory:	2 GB (2x1GB Micron PC2-5300 DDR2-667 CL5)	Other Software:	SmartHeap Library Version 8.1 from <a href="http://www.microquill.com/">http://www.microquill.com/</a>
Disk Subsystem:	Hitachi Travelstar HTS721010G9SA00 SATA 100GB 7200RPM		
Other Hardware:	None		

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	745	18.2	<b>745</b>	<b>18.2</b>	746	18.2	<b>746</b>	<b>18.2</b>	<b>745</b>	<b>18.2</b>	745	18.2
416.gamess	1655	11.8	1655	11.8	<b>1655</b>	<b>11.8</b>	1659	11.8	1661	11.8	<b>1659</b>	<b>11.8</b>
433.milc	1106	8.30	<b>1105</b>	<b>8.31</b>	1104	8.31	1089	8.43	1090	8.42	<b>1089</b>	<b>8.43</b>
434.zeusmp	<b>937</b>	<b>9.71</b>	937	9.71	937	9.71	<b>976</b>	<b>9.33</b>	978	9.31	976	9.33
435.gromacs	<b>667</b>	<b>10.7</b>	668	10.7	667	10.7	<b>666</b>	<b>10.7</b>	667	10.7	666	10.7
436.cactusADM	901	13.3	901	13.3	<b>901</b>	<b>13.3</b>	899	13.3	899	13.3	<b>899</b>	<b>13.3</b>
437.leslie3d	1028	9.14	<b>1028</b>	<b>9.14</b>	1028	9.15	1028	9.14	<b>1028</b>	<b>9.14</b>	1028	9.15
444.namd	881	9.10	882	9.10	<b>881</b>	<b>9.10</b>	<b>872</b>	<b>9.20</b>	873	9.19	872	9.20
447.dealII	<b>1024</b>	<b>11.2</b>	1025	11.2	1024	11.2	<b>1014</b>	<b>11.3</b>	1014	11.3	1013	11.3
450.soplex	932	8.95	932	8.95	<b>932</b>	<b>8.95</b>	<b>932</b>	<b>8.95</b>	933	8.94	931	8.96
453.povray	380	14.0	380	14.0	<b>380</b>	<b>14.0</b>	<b>382</b>	<b>13.9</b>	382	13.9	382	13.9
454.calculix	1252	6.59	1252	6.59	<b>1252</b>	<b>6.59</b>	<b>893</b>	<b>9.24</b>	894	9.23	893	9.24
459.GemsFDTD	1131	9.38	<b>1131</b>	<b>9.38</b>	1132	9.38	<b>1131</b>	<b>9.38</b>	<b>1131</b>	<b>9.38</b>	1132	9.38
465.tonto	1040	9.46	<b>1040</b>	<b>9.46</b>	1039	9.47	1009	9.76	1010	9.74	<b>1009</b>	<b>9.75</b>
470.lbm	1439	9.55	<b>1441</b>	<b>9.54</b>	1441	9.53	<b>1439</b>	<b>9.55</b>	1440	9.54	1439	9.55
481.wrf	838	13.3	<b>838</b>	<b>13.3</b>	839	13.3	838	13.3	<b>838</b>	<b>13.3</b>	839	13.3
482.sphinx3	1667	11.7	<b>1668</b>	<b>11.7</b>	1668	11.7	<b>1660</b>	<b>11.7</b>	1662	11.7	1659	11.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Binaries were built on Windows Vista32

The following VS 2005 SP1 updates were applied: KB926601 and KB932232

## Base Compiler Invocation

C benchmarks:

  icl -Qvc8 -Qc99

C++ benchmarks:

  icl -Qvc8

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Hardware Availability: Sep-2006

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## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc8 -Qc99 ifort

## Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore

444.namd: -TP

447.deallII: -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG

453.povray: -DSPEC\_CPU\_WINDOWS\_ICL

454.calculix: -DSPEC\_CPU\_NOZMODIFIER -Qlowercase

481.wrf: -DSPEC\_CPU\_WINDOWS\_ICL

## Base Optimization Flags

C benchmarks:

-QxP -Qipo -O3 -Qprec-div- -Qparallel /F1000000000 libguide40.lib

C++ benchmarks:

-QxP -Qipo -O3 -Qprec-div- -Qparallel -Qcxx\_features /F1000000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

-QxP -Qipo -O3 -Qprec-div- -Qparallel /F1000000000 libguide40.lib

Benchmarks using both Fortran and C:

-QxP -Qipo -O3 -Qprec-div- -Qparallel /F1000000000 libguide40.lib

## Peak Compiler Invocation

C benchmarks:

icl -Qvc8 -Qc99

C++ benchmarks:

icl -Qvc8

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc8 -Qc99 ifort



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CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2008

Hardware Availability: Sep-2006

Software Availability: Nov-2007

## Peak Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore
 444.namd: -TP
 447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
 453.povray: -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
 481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

## Peak Optimization Flags

C benchmarks:

```
433.milc: -QxP -Qipo -O3 -Qprec-div- -Qunroll2 -Oa /F1000000000
           libguide40.lib

470.lbm: -QxP -Qipo -O3 -Qprec-div- -Qunroll2 -Qscalar-rep-
          -Qprefetch /F1000000000 libguide40.lib

482.sphinx3: -QxP -Qipo -O3 -Qprec-div- -Qunroll2 /F1000000000
              libguide40.lib
```

C++ benchmarks:

```
444.namd: -QxP -Qipo -O3 -Qprec-div- -Oa -Qcxx_features
           /F1000000000 shlw32m.lib libguide40.lib
           -link /FORCE:MULTIPLE

447.dealII: -QxP -Qipo -O3 -Qprec-div- -Qunroll2 -Qprefetch
            -Qcxx_features /F1000000000 shlw32m.lib libguide40.lib
            -link /FORCE:MULTIPLE

450.soplex: -QxP -Qipo -O3 -Qprec-div- -Qparallel -Qcxx_features
            /F1000000000 shlw32m.lib libguide40.lib
            -link /FORCE:MULTIPLE

453.povray: -QxP -Qipo -O3 -Qprec-div- -Qunroll4 -Qcxx_features
            /F1000000000 shlw32m.lib libguide40.lib
            -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
410.bwaves: -QxP -Qipo -O3 -Qprec-div- -Qparallel -Qprefetch
             /F1000000000 libguide40.lib

416.gamess: -QxP -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias
             -Qscalar-rep- /F1000000000 libguide40.lib

434.zeusmp: -QxP -O2 -Qprec-div- -Qunroll0 -Qscalar-rep- /F1000000000
             libguide40.lib
```

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## Peak Optimization Flags (Continued)

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxP -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000  
libguide40.lib

Benchmarks using both Fortran and C:

435.gromacs: -QxP -Qipo -O3 -Qprec-div- -Oa -Qprefetch /F1000000000  
libguide40.lib

436.cactusADM: -QxP -Qipo -O3 -Qprec-div- -Qunroll2 -Qparallel  
-Qprefetch /F1000000000 libguide40.lib

454.calculix: -QxP -Qipo -O3 -Qprec-div- -Qunroll-aggressive  
/F1000000000 libguide40.lib

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-win32-revC.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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