SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

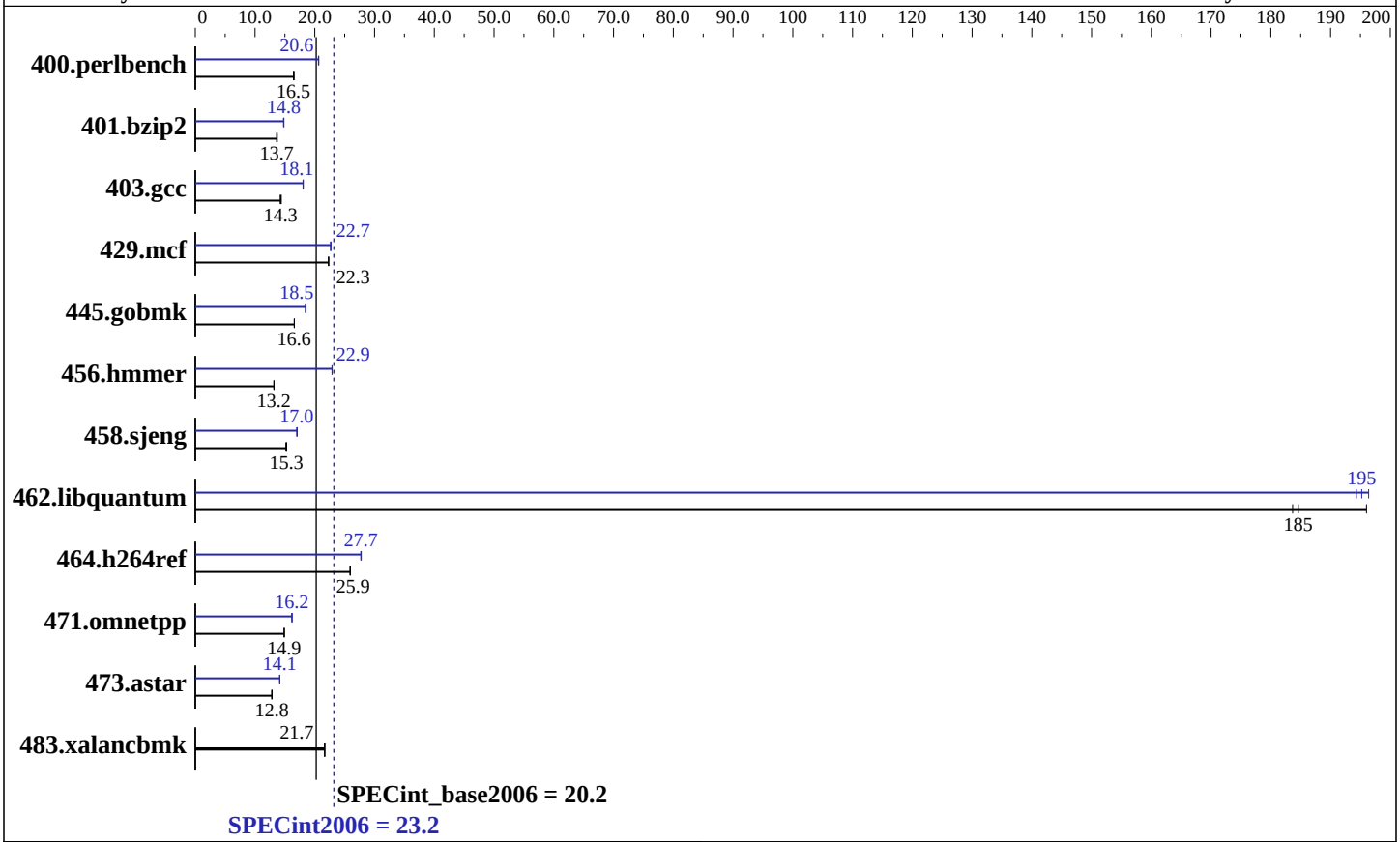
NovaScale B260+
(Intel Xeon L5420, 2.50 GHz)

SPECint®2006 = 23.2

SPECint_base2006 = 20.2

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Aug-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon L5420
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem:	1x73 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.17.50.0.15 SmartHeap library V8.1

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

NovaScale B260+
(Intel Xeon L5420, 2.50 GHz)

SPECint2006 = 23.2

SPECint_base2006 = 20.2

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Aug-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**593**	**16.5**	591	16.5	594	16.4	474	20.6	**474**	**20.6**	473	20.7
401.bzip2	709	13.6	703	13.7	**707**	**13.7**	**651**	**14.8**	651	14.8	654	14.8
403.gcc	**563**	**14.3**	566	14.2	559	14.4	446	18.1	447	18.0	**446**	**18.1**
429.mcf	409	22.3	**409**	**22.3**	409	22.3	402	22.7	402	22.7	**402**	**22.7**
445.gobmk	**633**	**16.6**	633	16.6	633	16.6	568	18.5	**568**	**18.5**	568	18.5
456.hmmer	709	13.2	**708**	**13.2**	708	13.2	407	22.9	408	22.9	**408**	**22.9**
458.sjeng	792	15.3	**793**	**15.3**	799	15.2	709	17.1	**711**	**17.0**	712	17.0
462.libquantum	**112**	**185**	113	184	106	196	106	196	**106**	**195**	107	194
464.h264ref	854	25.9	**854**	**25.9**	853	25.9	**798**	**27.7**	799	27.7	797	27.8
471.omnetpp	420	14.9	420	14.9	**420**	**14.9**	386	16.2	386	16.2	**386**	**16.2**
473.astar	**548**	**12.8**	546	12.9	548	12.8	496	14.2	497	14.1	**497**	**14.1**
483.xalancbmk	318	21.7	**318**	**21.7**	319	21.6	318	21.7	**318**	**21.7**	319	21.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to null
```

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
BIOS settings :
Hardware Prefetcher :                Enabled
Adjacent Cache-Line Prefetch :       Enabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090714.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 19:39:30 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 16 September 2008.