



SPEC[®] CINT2006 Result

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Bull SAS

SPECint[®]_rate2006 = 121

NovaScale B260+
(Intel Xeon E5430, 2.66 GHz)

SPECint_rate_base2006 = 99.0

CPU2006 license: 20

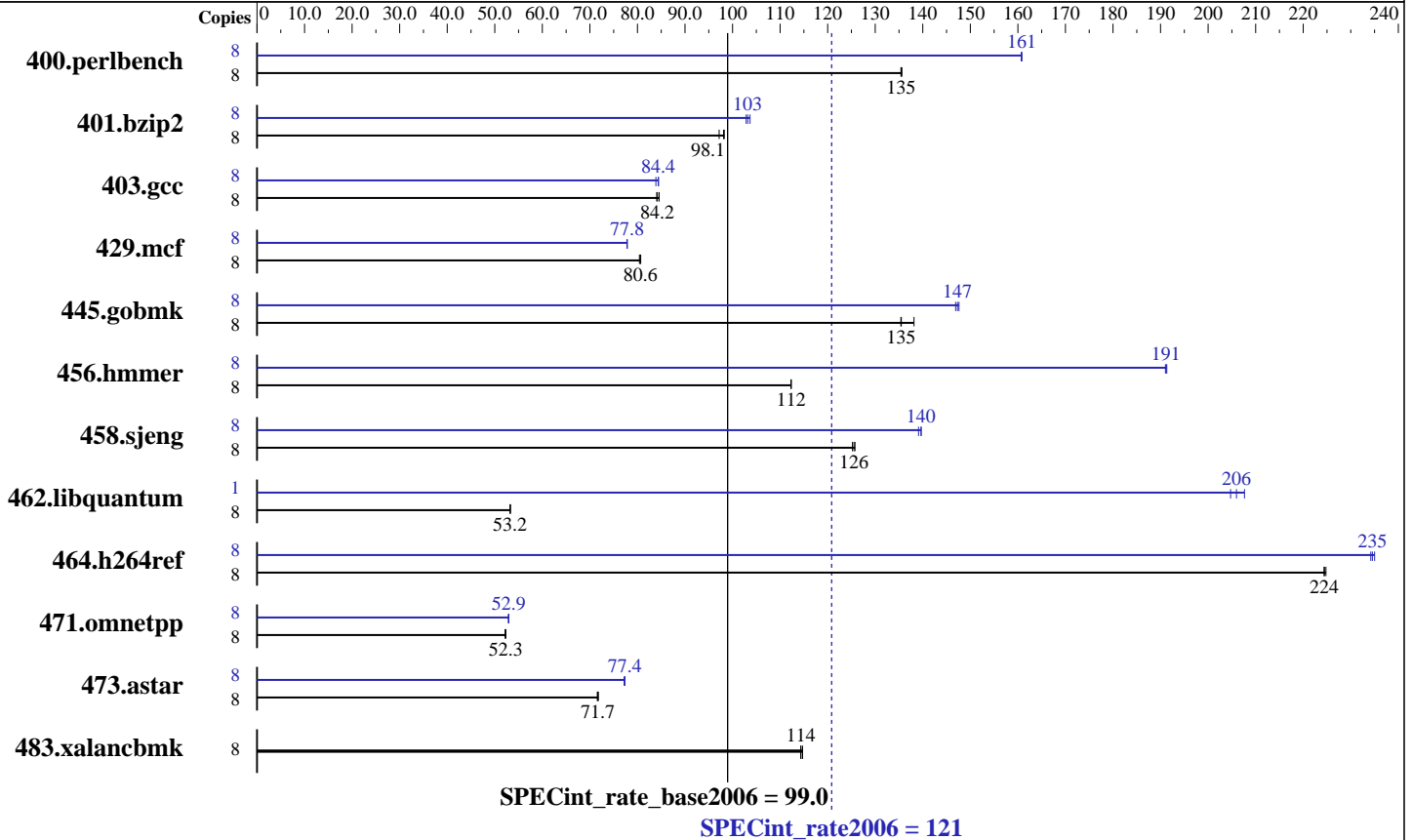
Test date: Jul-2008

Test sponsor: Bull SAS

Hardware Availability: Jan-2008

Tested by: Bull SAS

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB) FB-DIMM PC2-5300F ECC CL5
 Disk Subsystem: 1x73 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.50.0.15 SmartHeap library V8.1



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	577	135	<u>577</u>	<u>135</u>	576	136	8	486	161	486	161	<u>486</u>	<u>161</u>
401.bzip2	8	794	97.2	786	98.3	<u>787</u>	<u>98.1</u>	8	751	103	745	104	<u>748</u>	<u>103</u>
403.gcc	8	<u>765</u>	<u>84.2</u>	766	84.1	762	84.6	8	<u>763</u>	<u>84.4</u>	763	84.4	768	83.9
429.mcf	8	904	80.7	907	80.4	<u>905</u>	<u>80.6</u>	8	938	77.8	<u>937</u>	<u>77.8</u>	937	77.8
445.gobmk	8	607	138	<u>619</u>	<u>135</u>	620	135	8	<u>570</u>	<u>147</u>	571	147	568	148
456.hmmmer	8	<u>665</u>	<u>112</u>	665	112	664	112	8	391	191	<u>390</u>	<u>191</u>	390	191
458.sjeng	8	773	125	<u>771</u>	<u>126</u>	770	126	8	<u>693</u>	<u>140</u>	693	140	696	139
462.libquantum	8	3110	53.3	3115	53.2	<u>3114</u>	<u>53.2</u>	1	101	205	<u>101</u>	<u>206</u>	99.8	208
464.h264ref	8	789	224	<u>789</u>	<u>224</u>	788	225	8	756	234	753	235	<u>755</u>	<u>235</u>
471.omnetpp	8	956	52.3	958	52.2	<u>956</u>	<u>52.3</u>	8	<u>945</u>	<u>52.9</u>	944	52.9	946	52.9
473.astar	8	782	71.8	785	71.6	<u>784</u>	<u>71.7</u>	8	727	77.2	726	77.4	<u>726</u>	<u>77.4</u>
483.xalancbmk	8	483	114	<u>483</u>	<u>114</u>	481	115	8	483	114	<u>483</u>	<u>114</u>	481	115

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode
BIOS settings :
Hardware Prefetcher : Enabled
Adjacent Cache-Line Prefetch : Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-alloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090714.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090714.xml

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For other inquiries, please contact webmaster@spec.org.

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