SPEC® CFP2006 Result

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Bull SAS

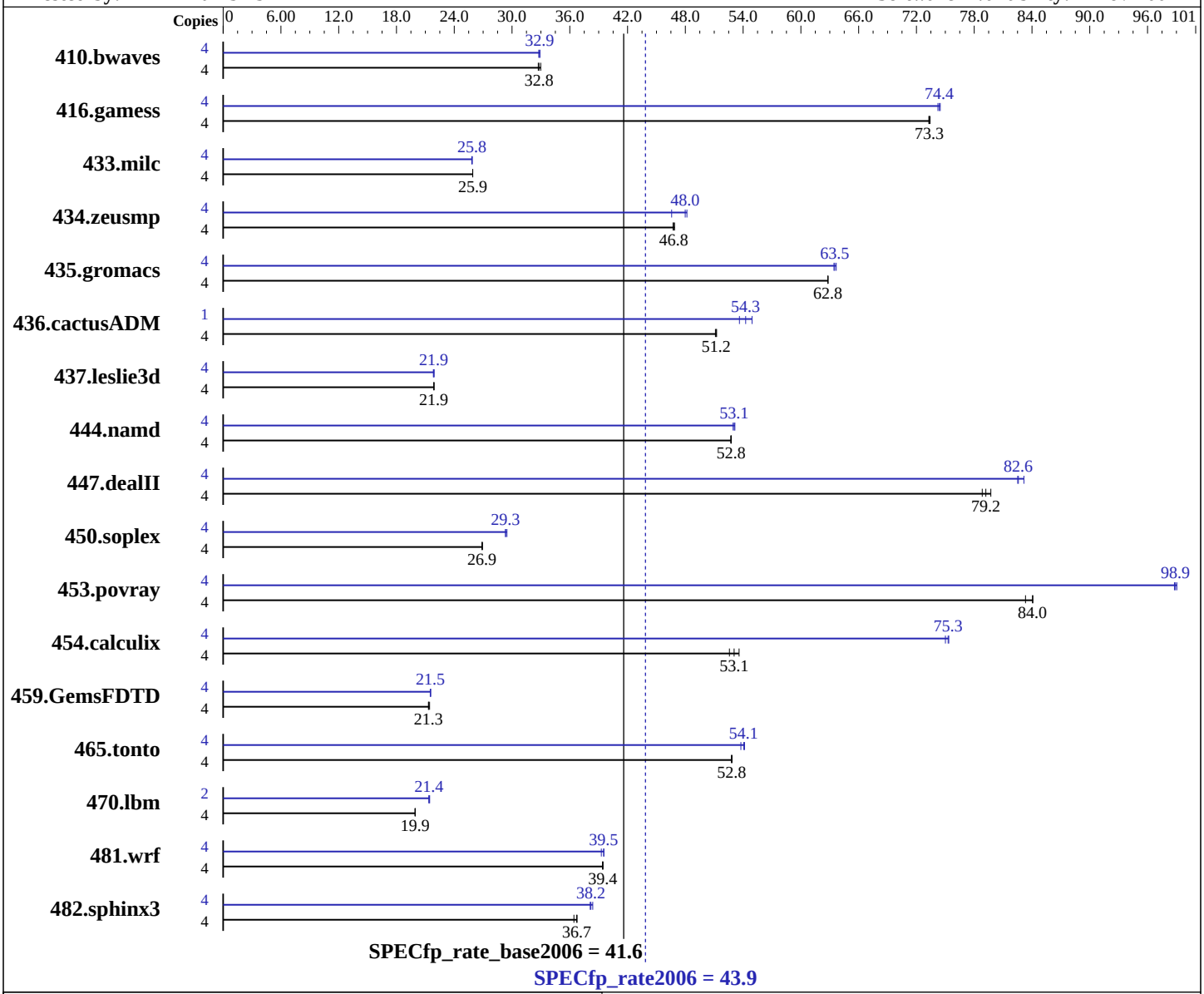
NovaScale T810 E1
(Intel Xeon X3320, 2.50 GHz)

SPECfp®_rate2006 = 43.9

SPECfp_rate_base2006 = 41.6

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Jul-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X3320
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip, 3 MB shared / 2 cores

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Software

Operating System:	SUSE LINUX Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel:	Yes
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB) FB-DIMM PC2-6400E ECC CL6
Disk Subsystem: 1x80 GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1648	33.0	**1657**	**32.8**	1661	32.7	4	1659	32.8	1652	32.9	**1654**	**32.9**
416.gamess	4	1067	73.4	1069	73.3	**1068**	**73.3**	4	**1053**	**74.4**	1052	74.5	1055	74.2
433.milc	4	1416	25.9	**1417**	**25.9**	1417	25.9	4	1422	25.8	1419	25.9	**1421**	**25.8**
434.zeusmp	4	776	46.9	**778**	**46.8**	779	46.7	4	**759**	**48.0**	756	48.2	781	46.6
435.gromacs	4	**455**	**62.8**	455	62.8	455	62.8	4	**450**	**63.5**	450	63.4	449	63.7
436.cactusADM	4	933	51.2	**934**	**51.2**	935	51.1	1	**220**	**54.3**	218	54.9	223	53.6
437.leslie3d	4	1716	21.9	**1718**	**21.9**	1719	21.9	4	**1719**	**21.9**	1716	21.9	1722	21.8
444.namd	4	609	52.7	608	52.8	**608**	**52.8**	4	606	53.0	604	53.1	**604**	**53.1**
447.dealII	4	574	79.7	580	78.8	**578**	**79.2**	4	**554**	**82.6**	555	82.5	550	83.2
450.soplex	4	1239	26.9	**1241**	**26.9**	1241	26.9	4	**1137**	**29.3**	1138	29.3	1132	29.5
453.povray	4	253	84.1	255	83.3	**253**	**84.0**	4	215	98.8	215	99.0	**215**	**98.9**
454.calculix	4	628	52.6	616	53.6	**622**	**53.1**	4	440	75.0	**438**	**75.3**	438	75.4
459.GemsFDTD	4	**1990**	**21.3**	1990	21.3	1979	21.4	4	1973	21.5	1969	21.6	**1970**	**21.5**
465.tonto	4	745	52.8	**745**	**52.8**	746	52.8	4	727	54.1	**728**	**54.1**	732	53.8
470.lbm	4	2757	19.9	2756	19.9	**2756**	**19.9**	2	1281	21.4	**1284**	**21.4**	1287	21.4
481.wrf	4	**1133**	**39.4**	1134	39.4	1133	39.5	4	1138	39.3	**1131**	**39.5**	1130	39.5
482.sphinx3	4	2140	36.4	2121	36.8	**2123**	**36.7**	4	**2041**	**38.2**	2031	38.4	2045	38.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
```

General Notes

```
All benchmarks compiled in 64-bit mode except 437.leslie3d, 450.soplex,
470.lbm and 482.sphinx3, at peak, are compiled in 32-bit mode
The Bull NovaScale T810 E1(Intel Xeon X3320, 2.50 GHz),
the Bull NovaScale T830 E1(Intel Xeon X3320, 2.50 GHz) and
the Bull NovaScale R410 E1(Intel Xeon X3320, 2.50 GHz) models are electronically equivalent.
The results have been measured on a Bull NovaScale R410 E1(Intel Xeon X3320, 2.50 GHz) model.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks (except as noted below):
`/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

433.milc: `icc`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

Fortran benchmarks (except as noted below):
`ifort`

437.leslie3d: `/opt/intel/fc/10.1.008/bin/ifort -L/opt/intel/fc/10.1.008/lib -I/opt/intel/fc/10.1.008/include`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

470.lbm: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -scalar-rep- -prefetch -opt-malloc-options=3`

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Peak Optimization Flags (Continued)

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.20090714.html

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.20090714.xml`

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For other inquiries, please contact webmaster@spec.org.

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