SPEC® CINT2006 Result

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Dell Inc.

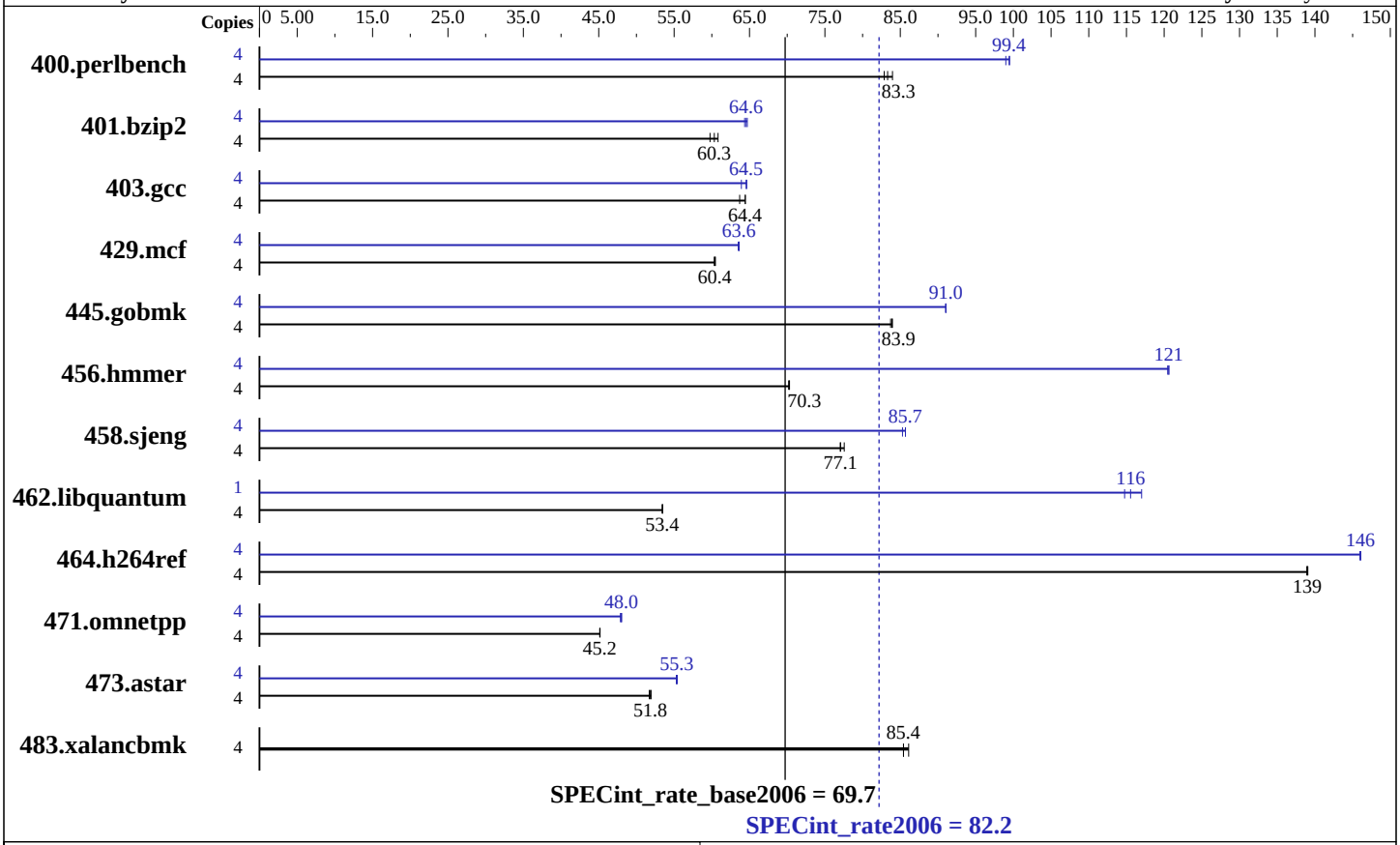
PowerEdge 2900 III (Intel Xeon X5260, 3.33 GHz)

SPECint®_rate2006 = 82.2

SPECint_rate_base2006 = 69.7

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2008
Hardware Availability: Jan-2008
Software Availability: May-2008



Hardware

CPU Name:	Intel Xeon X5260
CPU Characteristics:	
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB, DDR2-667 MHz, CL5, Reg, Dual Rank)
Disk Subsystem:	2 x 73 GB GB SAS 15000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20080312 Package ID: l_cc_p_10.1.015
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	472	82.9	465	84.0	**469**	**83.3**	4	395	99.0	393	99.5	**393**	**99.4**
401.bzip2	4	635	60.8	**640**	**60.3**	646	59.8	4	597	64.7	**598**	**64.6**	600	64.4
403.gcc	4	506	63.7	**500**	**64.4**	500	64.4	4	**499**	**64.5**	498	64.6	504	63.9
429.mcf	4	603	60.5	**604**	**60.4**	605	60.3	4	574	63.5	**573**	**63.6**	573	63.6
445.gobmk	4	**500**	**83.9**	501	83.7	500	84.0	4	**461**	**91.0**	461	91.1	461	91.0
456.hmmer	4	**531**	**70.3**	532	70.2	531	70.3	4	**309**	**121**	310	120	309	121
458.sjeng	4	624	77.6	628	77.0	**628**	**77.1**	4	565	85.7	567	85.3	**565**	**85.7**
462.libquantum	4	1551	53.4	**1551**	**53.4**	1550	53.5	1	181	115	177	117	**179**	**116**
464.h264ref	4	**637**	**139**	637	139	637	139	4	**606**	**146**	606	146	606	146
471.omnetpp	4	553	45.2	554	45.1	**554**	**45.2**	4	520	48.0	522	47.9	**521**	**48.0**
473.astar	4	541	51.9	543	51.7	**542**	**51.8**	4	**507**	**55.3**	508	55.3	507	55.4
483.xalancbmk	4	321	86.1	323	85.4	**323**	**85.4**	4	321	86.1	323	85.4	**323**	**85.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Platform Notes

```
BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default = Enabled)
```

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
taskset was used to bind processes to cores except
for 462.libquantum peak
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
```

Base Compiler Invocation

C benchmarks:

```
icc
```

C++ benchmarks:

```
icpc
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.015/bin/icc
-L/opt/intel/cce/10.1.015/lib
-I/opt/intel/cce/10.1.015/include

456.hmmer: /opt/intel/cce/10.1.015/bin/icc
-L/opt/intel/cce/10.1.015/lib
-I/opt/intel/cce/10.1.015/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page

Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.html`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.xml`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.xml`

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For other inquiries, please contact webmaster@spec.org.

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