SPEC® CINT2006 Result

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Dell Inc.

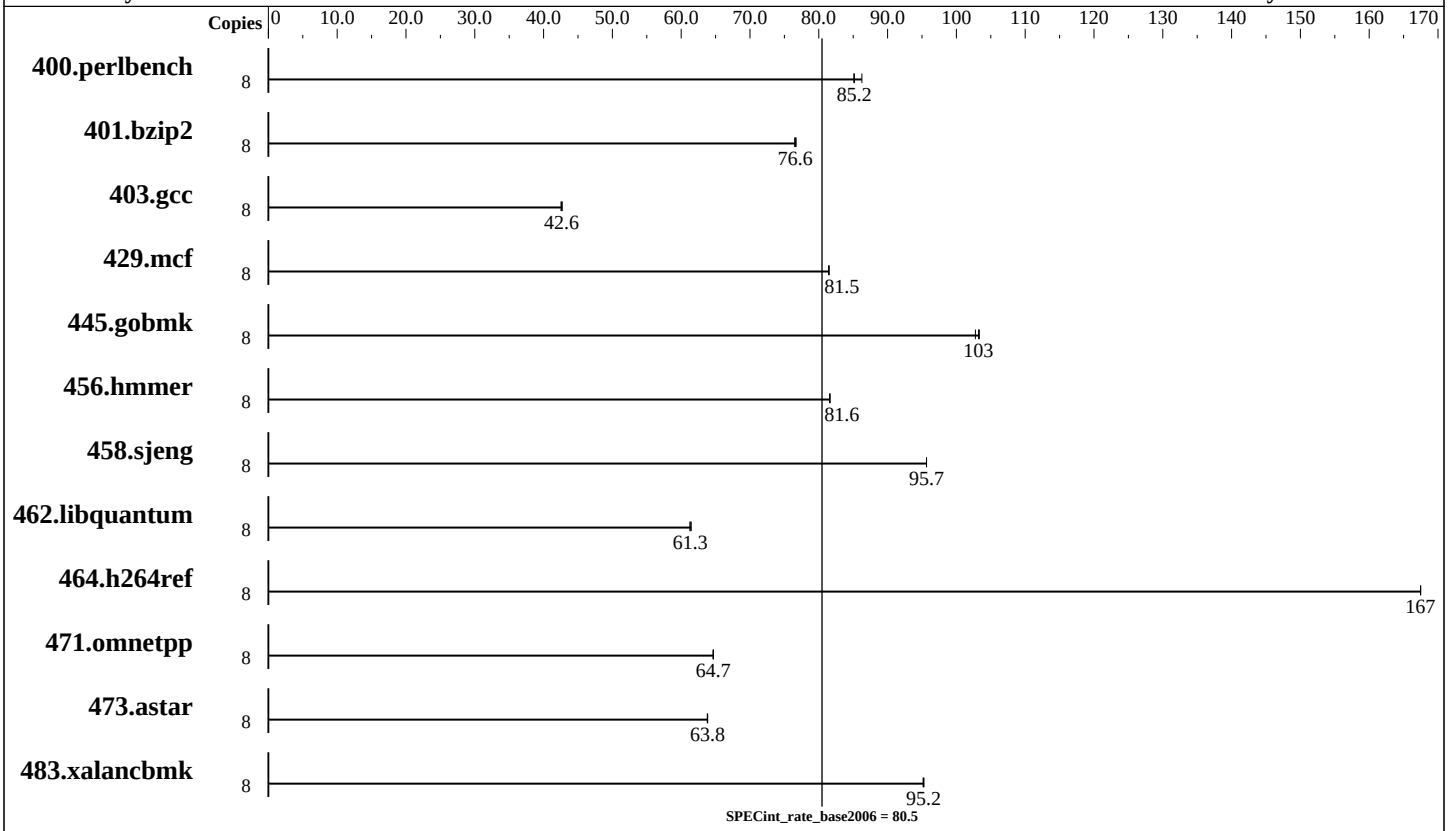
PowerEdge 2900 III (Intel Xeon E5405, 2.00 GHz)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 80.5

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Aug-2008
Hardware Availability: Nov-2007
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon E5405
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB, DDR2-667 MHz, CL5, Reg, Dual Rank)
Disk Subsystem:	2 x 73 GB GB SAS 15000 RPM (RAID-0)
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 R2 Enterprise x64 Edition
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	906	86.3	**918**	**85.2**	919	85.1							
401.bzip2	8	1009	76.5	**1008**	**76.6**	1006	76.7							
403.gcc	8	**1511**	**42.6**	1514	42.5	1508	42.7							
429.mcf	8	**895**	**81.5**	895	81.5	896	81.4							
445.gobmk	8	812	103	**813**	**103**	817	103							
456.hmmer	8	**915**	**81.6**	915	81.6	915	81.6							
458.sjeng	8	**1012**	**95.7**	1012	95.7	1012	95.7							
462.libquantum	8	2705	61.3	**2704**	**61.3**	2697	61.5							
464.h264ref	8	1057	167	1057	167	**1057**	**167**							
471.omnetpp	8	774	64.6	773	64.7	**773**	**64.7**							
473.astar	8	880	63.8	**880**	**63.8**	880	63.9							
483.xalancbmk	8	579	95.3	580	95.2	**580**	**95.2**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)
Hardware Prefetcher = Disabled (Default = Enabled)
```

General Notes

```
Binaries were built on Windows Vista
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-fast -Qvec-guard-write /F512000000
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
              -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.00.xml`

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For other inquiries, please contact webmaster@spec.org.

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