SPEC® CINT2006 Result

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ACTION S.A.

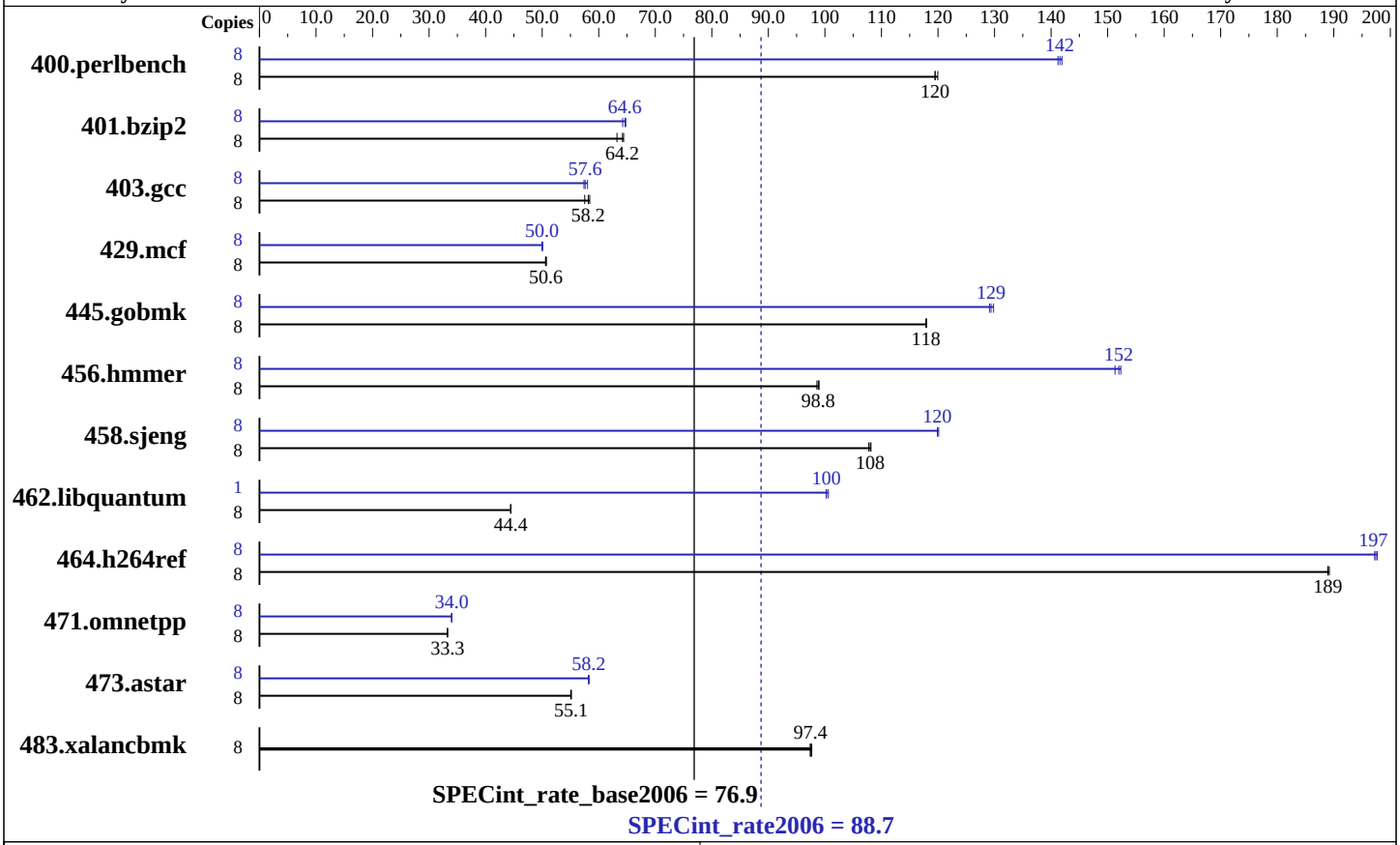
ACTINA SOLAR 442 S3 (Intel Xeon E7320)

SPECint®_rate2006 = 88.7

SPECint_rate_base2006 = 76.9

CPU2006 license: 3388
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Aug-2008
Hardware Availability: Dec-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E7320
CPU Characteristics:	1066 MHz bus
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x 2 GB PC2-5300 ECC FBDIMM)
Disk Subsystem:	1x 250 GB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**654**	**120**	652	120	654	119	8	**552**	**142**	553	141	551	142
401.bzip2	8	1221	63.2	1199	64.4	**1202**	**64.2**	8	**1194**	**64.6**	1202	64.2	1192	64.8
403.gcc	8	1103	58.4	**1107**	**58.2**	1120	57.5	8	1110	58.0	**1118**	**57.6**	1122	57.4
429.mcf	8	1438	50.7	1442	50.6	**1441**	**50.6**	8	1460	50.0	1457	50.1	**1460**	**50.0**
445.gobmk	8	711	118	712	118	**712**	**118**	8	650	129	646	130	**649**	**129**
456.hmmer	8	757	98.6	754	99.0	**755**	**98.8**	8	493	151	**491**	**152**	490	152
458.sjeng	8	898	108	**896**	**108**	895	108	8	807	120	**807**	**120**	806	120
462.libquantum	8	3727	44.5	**3731**	**44.4**	3731	44.4	1	206	101	207	100	**207**	**100**
464.h264ref	8	937	189	936	189	**937**	**189**	8	**897**	**197**	895	198	898	197
471.omnetpp	8	1503	33.3	**1504**	**33.3**	1505	33.2	8	1470	34.0	**1471**	**34.0**	1472	34.0
473.astar	8	1018	55.2	1020	55.1	**1019**	**55.1**	8	965	58.2	**964**	**58.2**	964	58.3
483.xalancbmk	8	565	97.7	**567**	**97.4**	567	97.4	8	565	97.7	**567**	**97.4**	567	97.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode.
Taskset command was used to bind processes to CPUs.
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

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Base Optimization Flags (Continued)

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/10.1.008/bin/icc`
`-L/opt/intel/cce/10.1.008/lib`
`-I/opt/intel/cce/10.1.008/include`

456.hmmer: `/opt/intel/cce/10.1.008/bin/icc`
`-L/opt/intel/cce/10.1.008/lib`
`-I/opt/intel/cce/10.1.008/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias`
`-prefetch`

Continued on next page

Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.00.xml

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