SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

ACTION S.A.

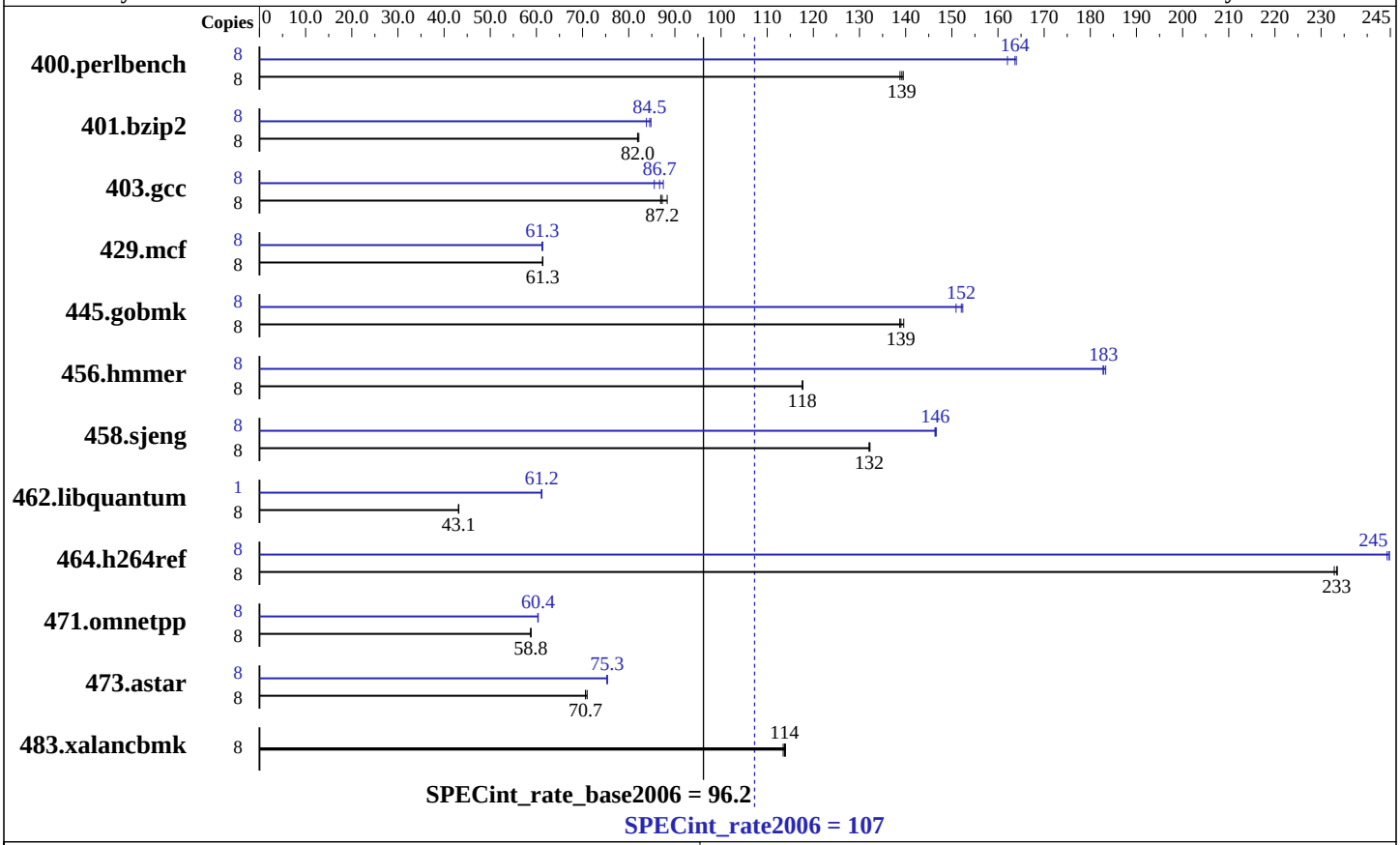
ACTINA SOLAR 442 S3 (Intel Xeon X7350, 2.93 GHz)

SPECint®_rate2006 = 107

SPECint_rate_base2006 = 96.2

CPU2006 license: 3388
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Aug-2008
Hardware Availability: Dec-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X7350
CPU Characteristics:	1066 MHz bus
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x 2 GB PC2-5300 ECC FBDIMM)
Disk Subsystem:	1x 250 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.17.50.0.15

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

ACTION S.A.

ACTINA SOLAR 442 S3 (Intel Xeon X7350, 2.93 GHz)

SPECint_rate2006 = 107

SPECint_rate_base2006 = 96.2

CPU2006 license: 3388
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Aug-2008
Hardware Availability: Dec-2007
Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	563	139	**562**	**139**	560	139	8	477	164	482	162	**478**	**164**
401.bzip2	8	**942**	**82.0**	942	81.9	939	82.2	8	910	84.8	**913**	**84.5**	921	83.8
403.gcc	8	729	88.3	**738**	**87.2**	741	86.9	8	736	87.5	**743**	**86.7**	753	85.5
429.mcf	8	1190	61.3	**1189**	**61.3**	1188	61.4	8	**1190**	**61.3**	1189	61.3	1192	61.2
445.gobmk	8	605	139	**604**	**139**	601	140	8	556	151	**552**	**152**	551	152
456.hmmer	8	**635**	**118**	635	118	634	118	8	408	183	**408**	**183**	407	183
458.sjeng	8	733	132	**733**	**132**	732	132	8	**661**	**146**	660	147	661	146
462.libquantum	8	**3843**	**43.1**	3844	43.1	3837	43.2	1	**339**	**61.2**	339	61.2	339	61.1
464.h264ref	8	758	234	**759**	**233**	760	233	8	723	245	**723**	**245**	725	244
471.omnetpp	8	**850**	**58.8**	850	58.8	851	58.8	8	829	60.3	828	60.4	**828**	**60.4**
473.astar	8	**795**	**70.7**	791	71.0	795	70.6	8	**746**	**75.3**	746	75.3	745	75.4
483.xalancbmk	8	484	114	**485**	**114**	486	113	8	484	114	**485**	**114**	486	113

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode.
Taskset command was used to bind processes to CPUs.
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/cce/10.1.008/bin/icc`
`-L/opt/intel/cce/10.1.008/lib`
`-I/opt/intel/cce/10.1.008/include`

456.hmmer: `/opt/intel/cce/10.1.008/bin/icc`
`-L/opt/intel/cce/10.1.008/lib`
`-I/opt/intel/cce/10.1.008/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias`
`-prefetch`

Continued on next page

Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090714.00.xml

Copyright 2006-2014 Standard Performance Evaluation Corporation

ACTION S.A.

ACTINA SOLAR 442 S3 (Intel Xeon X7350, 2.93 GHz)

SPECint_rate2006 = 107

SPECint_rate_base2006 = 96.2

CPU2006 license: 3388
Test sponsor: ACTION S.A.
Tested by: ACTION S.A.

Test date: Aug-2008
Hardware Availability: Dec-2007
Software Availability: Nov-2007

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 19:12:22 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 September 2008.