SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY RX300 S4, Intel Xeon X5260, 3.33 GHz

SPECint®_rate2006 =　42.5

SPECint_rate_base2006 =　37.0

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2008

Hardware Availability: Dec-2007

Software Availability: Nov-2007

SPECint_rate_base2006 = 37.0

SPECint_rate2006 = 42.5

Hardware

CPU Name:	Intel Xeon X5260
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F, 2 rank, CL 5-5-5, ECC)
Disk Subsystem:	1x SAS, 73 GB, 15000 rpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
Auto Parallel:	Yes
File System:	ext2
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.50.0.5-0.1.x86_64

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint_rate2006 = 42.5

PRIMERGY RX300 S4, Intel Xeon X5260, 3.33 GHz

SPECint_rate_base2006 = 37.0

Results Table
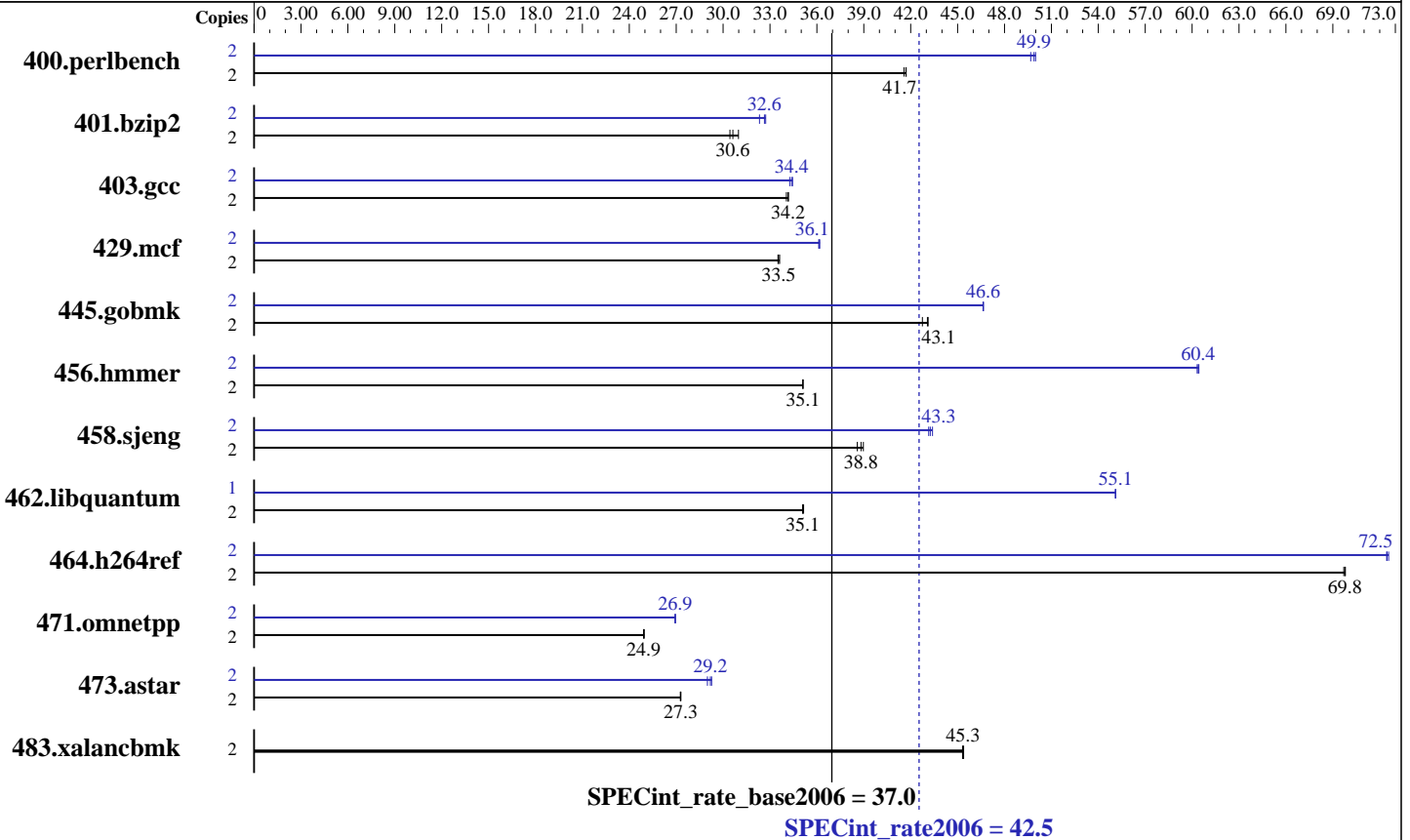
Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	470	41.6	**469**	**41.7**	469	41.7	2	393	49.7	391	50.0	**392**	**49.9**
401.bzip2	2	634	30.4	623	31.0	**630**	**30.6**	2	597	32.3	590	32.7	**591**	**32.6**
403.gcc	2	473	34.0	**471**	**34.2**	471	34.2	2	470	34.3	467	34.4	**468**	**34.4**
429.mcf	2	543	33.6	544	33.5	**544**	**33.5**	2	**505**	**36.1**	505	36.1	504	36.2
445.gobmk	2	491	42.7	487	43.1	**487**	**43.1**	2	450	46.6	450	46.6	**450**	**46.6**
456.hmmer	2	**532**	**35.1**	532	35.1	532	35.1	2	**309**	**60.4**	309	60.3	309	60.4
458.sjeng	2	**623**	**38.8**	621	39.0	627	38.6	2	561	43.1	**560**	**43.3**	558	43.4
462.libquantum	2	1181	35.1	**1181**	**35.1**	1179	35.1	1	376	55.1	376	55.1	**376**	**55.1**
464.h264ref	2	634	69.8	**634**	**69.8**	635	69.7	2	611	72.5	**611**	**72.5**	610	72.6
471.omnetpp	2	502	24.9	501	25.0	**501**	**24.9**	2	464	27.0	**464**	**26.9**	464	26.9
473.astar	2	**515**	**27.3**	514	27.3	515	27.3	2	480	29.3	484	29.0	**481**	**29.2**
483.xalancbmk	2	305	45.3	304	45.4	**304**	**45.3**	2	305	45.3	304	45.4	**304**	**45.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)
```

Platform Notes

```
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Disable
```

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint_rate2006 =   42.5

PRIMERGY RX300 S4, Intel Xeon X5260, 3.33 GHz

SPECint_rate_base2006 = 37.0

CPU2006 license: 22		**Test date:**	May-2008
Test sponsor: Fujitsu Siemens Computers		**Hardware Availability:**	Dec-2007
Tested by: Fujitsu Siemens Computers		**Software Availability:**	Nov-2007

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
        -L/opt/intel/cce/10.1.008/lib
        -I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
        -L/opt/intel/cce/10.1.008/lib
        -I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
```

Continued on next page

SPEC CINT2006 Result

Fujitsu Siemens Computers

SPECint_rate2006 =   42.5

PRIMERGY RX300 S4, Intel Xeon X5260, 3.33 GHz

SPECint_rate_base2006 = 37.0

CPU2006 license: 22		**Test date:**	May-2008
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Dec-2007
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Nov-2007

Peak Portability Flags (Continued)

  483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias`
                `-prefetch`

      401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch`

        403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

        429.mcf: `-fast -prefetch`

      445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo`
                `-no-prec-div -ansi-alias`

      456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

      458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

   462.libquantum: `-fast -unroll4 -Ob0 -prefetch`
                `-opt-streaming-stores always -vec-guard-write`
                `-opt-malloc-options=3 -parallel -par-runtime-control`

     464.h264ref: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2`
                `-ansi-alias`

C++ benchmarks:

     471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo`
                `-no-prec-div -ansi-alias -opt-ra-region-strategy=block`
                `-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

       473.astar: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo`
                `-no-prec-div -ansi-alias -opt-ra-region-strategy=routine`
                `-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

   483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

Fujitsu Siemens Computers

PRIMERGY RX300 S4, Intel Xeon X5260, 3.33 GHz

SPECint_rate2006 =   42.5

SPECint_rate_base2006 = 37.0

CPU2006 license: 22	**Test date:** May-2008
Test sponsor: Fujitsu Siemens Computers	**Hardware Availability:** Dec-2007
Tested by: Fujitsu Siemens Computers	**Software Availability:** Nov-2007

Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at

`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.html`

You can also download the XML flags source by saving the following link:

`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 19:04:54 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 September 2008.