SPEC® CINT2006 Result

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Bull SAS

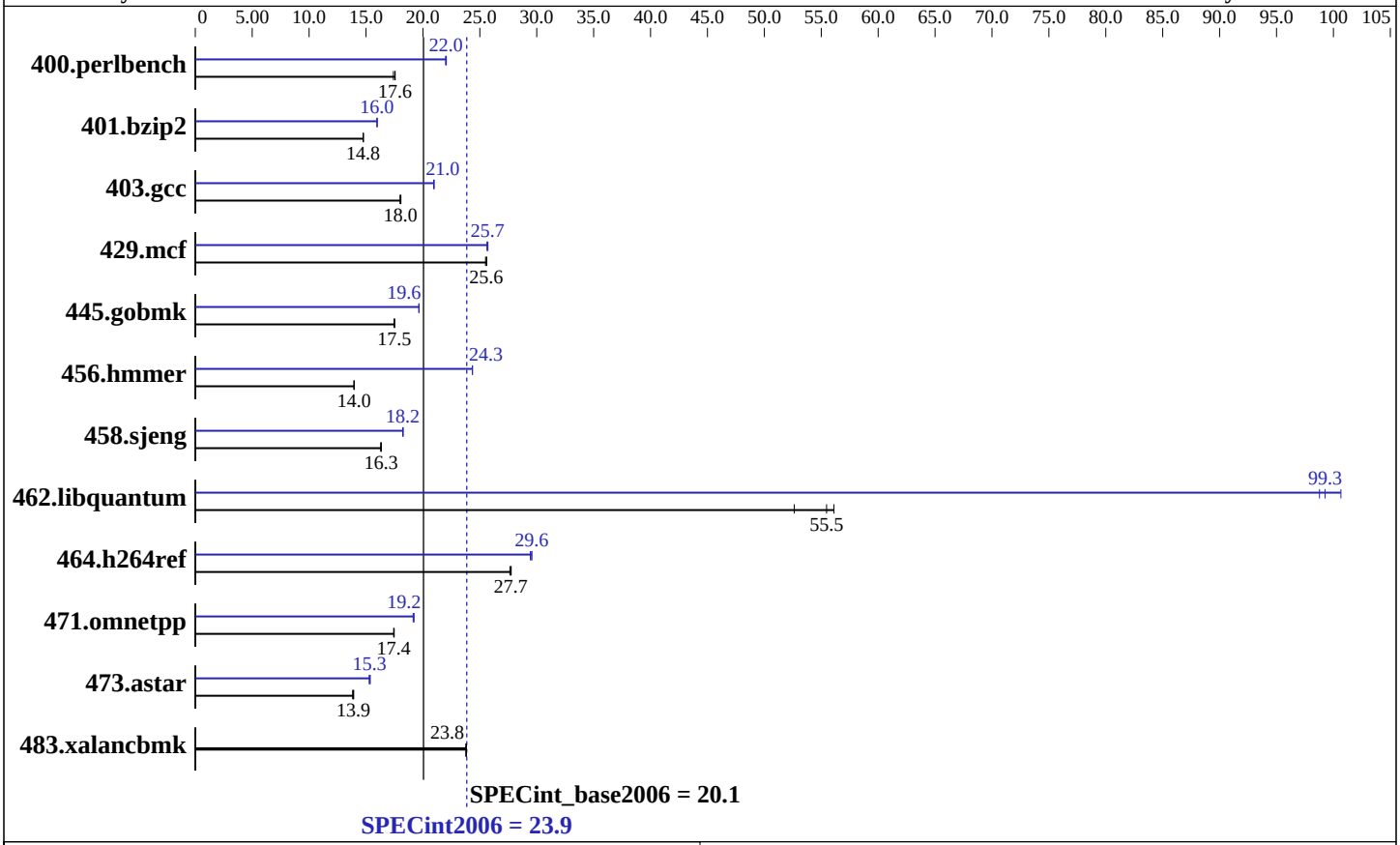
NovaScale T810 E1
(Intel Xeon X3350, 2.66 GHz)

SPECint®2006 = 23.9

SPECint_base2006 = 20.1

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Jul-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X3350
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB) FB-DIMM PC2-6400E ECC CL6
Disk Subsystem:	1x80 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 SP1 Kernel 2.6.16.46-0.12-smp for x86_64
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.17.50.0.15 SmartHeap library V8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	561	17.4	557	17.6	**557**	**17.6**	**443**	**22.0**	443	22.1	444	22.0
401.bzip2	**654**	**14.8**	653	14.8	654	14.7	**604**	**16.0**	604	16.0	604	16.0
403.gcc	446	18.1	448	18.0	**446**	**18.0**	**384**	**21.0**	384	21.0	384	20.9
429.mcf	**357**	**25.6**	358	25.5	356	25.6	356	25.6	**355**	**25.7**	355	25.7
445.gobmk	600	17.5	**600**	**17.5**	599	17.5	533	19.7	**534**	**19.6**	534	19.6
456.hmmer	668	14.0	670	13.9	**668**	**14.0**	383	24.4	383	24.3	**383**	**24.3**
458.sjeng	740	16.4	743	16.3	**741**	**16.3**	**664**	**18.2**	662	18.3	664	18.2
462.libquantum	394	52.6	369	56.1	**374**	**55.5**	206	101	210	98.8	**209**	**99.3**
464.h264ref	**798**	**27.7**	801	27.6	797	27.8	748	29.6	752	29.4	**749**	**29.6**
471.omnetpp	358	17.5	358	17.4	**358**	**17.4**	325	19.2	**326**	**19.2**	327	19.1
473.astar	504	13.9	**505**	**13.9**	508	13.8	457	15.4	**458**	**15.3**	460	15.3
483.xalancbmk	**290**	**23.8**	290	23.8	290	23.8	**290**	**23.8**	290	23.8	290	23.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to null
```

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
The Bull NovaScale T810 E1(Intel Xeon X3350, 2.66 GHz),
the Bull NovaScale T830 E1(Intel Xeon X3350, 2.66 GHz) and
the Bull NovaScale R410 E1(Intel Xeon X3350, 2.66 GHz) models are electronically equivalent.
The results have been measured on a Bull NovaScale R410 E1(Intel Xeon X3350, 2.66 GHz) model.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.00.xml`

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