SPEC® CINT2006 Result
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Dell Inc.

PowerEdge M600 (Intel Xeon E5405, 2.00 GHz)

SPECint®_rate2006 = 97.6

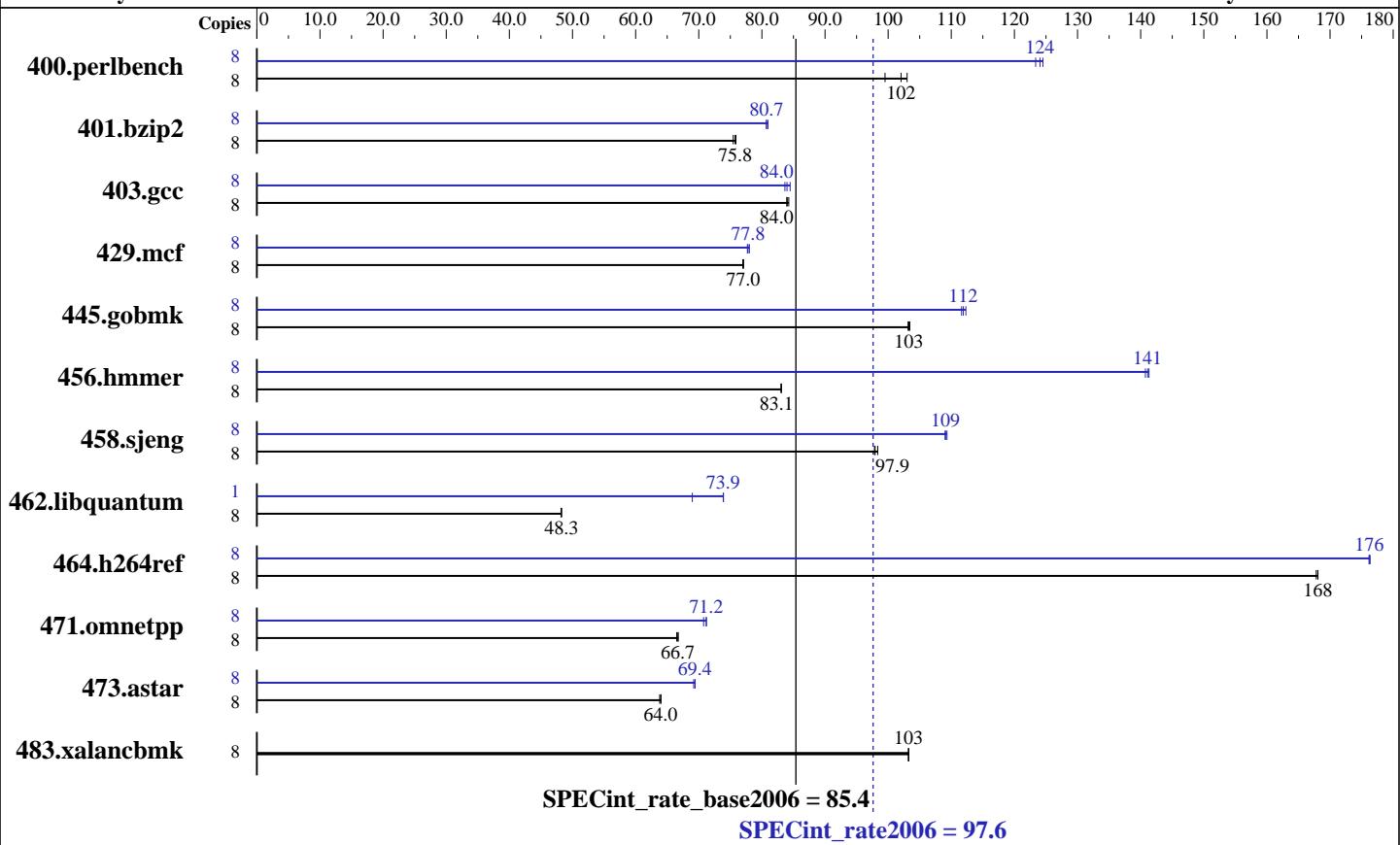SPECint_rate_base2006 = 85.4

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2008
Hardware Availability: Jan-2008
Software Availability: Jan-2008

Copies		
400.perlbench	8	124
	8	102
401.bzip2	8	80.7
	8	75.8
403.gcc	8	84.0
	8	84.0
429.mcf	8	77.8
	8	77.0
445.gobmk	8	112
	8	103
456.hmmer	8	141
	8	83.1
458.sjeng	8	109
	8	97.9
462.libquantum	1	73.9
	8	48.3
464.h264ref	8	176
	8	168
471.omnetpp	8	71.2
	8	66.7
473.astar	8	69.4
	8	64.0
483.xalancbmk	8	103

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Hardware

CPU Name:	Intel Xeon E5405
CPU Characteristics:	
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4x4 GB 667 MHz ECC CL5 FB-DIMM)
Disk Subsystem:	1 x 80 GB 5400 RPM SATA
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	785	99.5	759	103	**766**	**102**	8	**630**	**124**	628	125	634	123
401.bzip2	8	**1019**	**75.8**	1017	75.9	1023	75.5	8	954	81.0	956	80.7	**956**	**80.7**
403.gcc	8	767	83.9	**767**	**84.0**	764	84.3	8	**767**	**84.0**	770	83.6	762	84.5
429.mcf	8	946	77.1	**947**	**77.0**	948	77.0	8	**938**	**77.8**	935	78.0	939	77.7
445.gobmk	8	811	103	814	103	**813**	**103**	8	**750**	**112**	752	112	747	112
456.hmmer	8	899	83.0	898	83.1	**899**	**83.1**	8	**529**	**141**	530	141	528	141
458.sjeng	8	989	97.8	984	98.4	**988**	**97.9**	8	888	109	885	109	**887**	**109**
462.libquantum	8	3440	48.2	**3435**	**48.3**	3435	48.3	1	300	69.0	280	73.9	**280**	**73.9**
464.h264ref	8	**1053**	**168**	1053	168	1055	168	8	**1004**	**176**	1004	176	1005	176
471.omnetpp	8	**750**	**66.7**	749	66.7	752	66.5	8	**702**	**71.2**	702	71.2	707	70.8
473.astar	8	**878**	**64.0**	877	64.0	880	63.8	8	812	69.2	809	69.4	**809**	**69.4**
483.xalancbmk	8	535	103	535	103	**535**	**103**	8	535	103	535	103	**535**	**103**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

Platform Notes

```
BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default Enabled)
Hardware Prefetcher = Disabled (default Enabled)
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
           -L/opt/intel/cce/10.1.008/lib
           -I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
           -L/opt/intel/cce/10.1.008/lib
           -I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
```

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

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Peak Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10.1-int-linux64-revC.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10.1-int-linux64-revC.xml

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