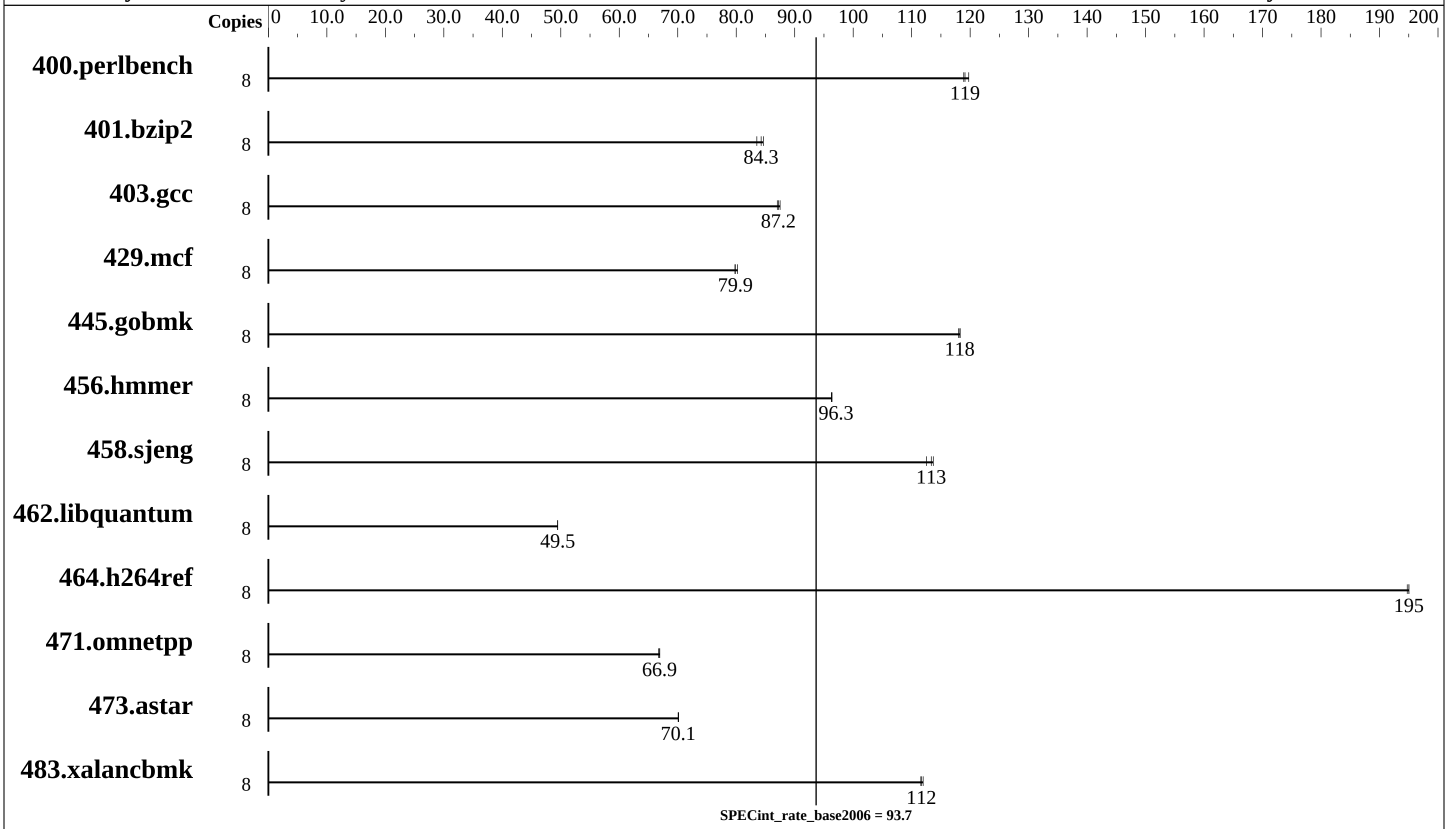
SPEC® CINT2006 Result

Sun Microsystems
Sun Fire X4150

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 93.7

CPU2006 license: 6		**Test date:**	Jun-2008
Test sponsor:	Sun Microsystems	**Hardware Availability:**	Feb-2008
Tested by:	Sun Microsystems	**Software Availability:**	Nov-2007



Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 (order by number of chips)
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2GB DDR2 PC2-5300F 2rank CAS 5-5-5 with ECC)
Disk Subsystem:	SAS, 72 GB, 10K RPM
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 SP1 for x86_64
Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070824
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	SmartHeap library V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	653	120	657	119	**656**	**119**							
401.bzip2	8	912	84.6	924	83.5	**916**	**84.3**							
403.gcc	8	740	87.0	736	87.5	**738**	**87.2**							
429.mcf	8	909	80.2	915	79.8	**914**	**79.9**							
445.gobmk	8	711	118	**710**	**118**	709	118							
456.hmmer	8	775	96.3	**775**	**96.3**	774	96.4							
458.sjeng	8	**854**	**113**	851	114	860	113							
462.libquantum	8	3351	49.5	3350	49.5	**3351**	**49.5**							
464.h264ref	8	**908**	**195**	908	195	909	195							
471.omnetpp	8	**748**	**66.9**	747	66.9	750	66.7							
473.astar	8	**801**	**70.1**	801	70.1	801	70.2							
483.xalancbmk	8	493	112	**494**	**112**	495	112							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited
OMP_NUM_THREADS set to 8
KMP_AFFINITY set to physical,0
```

Platform Notes

```
Default BIOS configurations were used.
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:

`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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