SPEC® CINT2006 Result

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Bull SAS

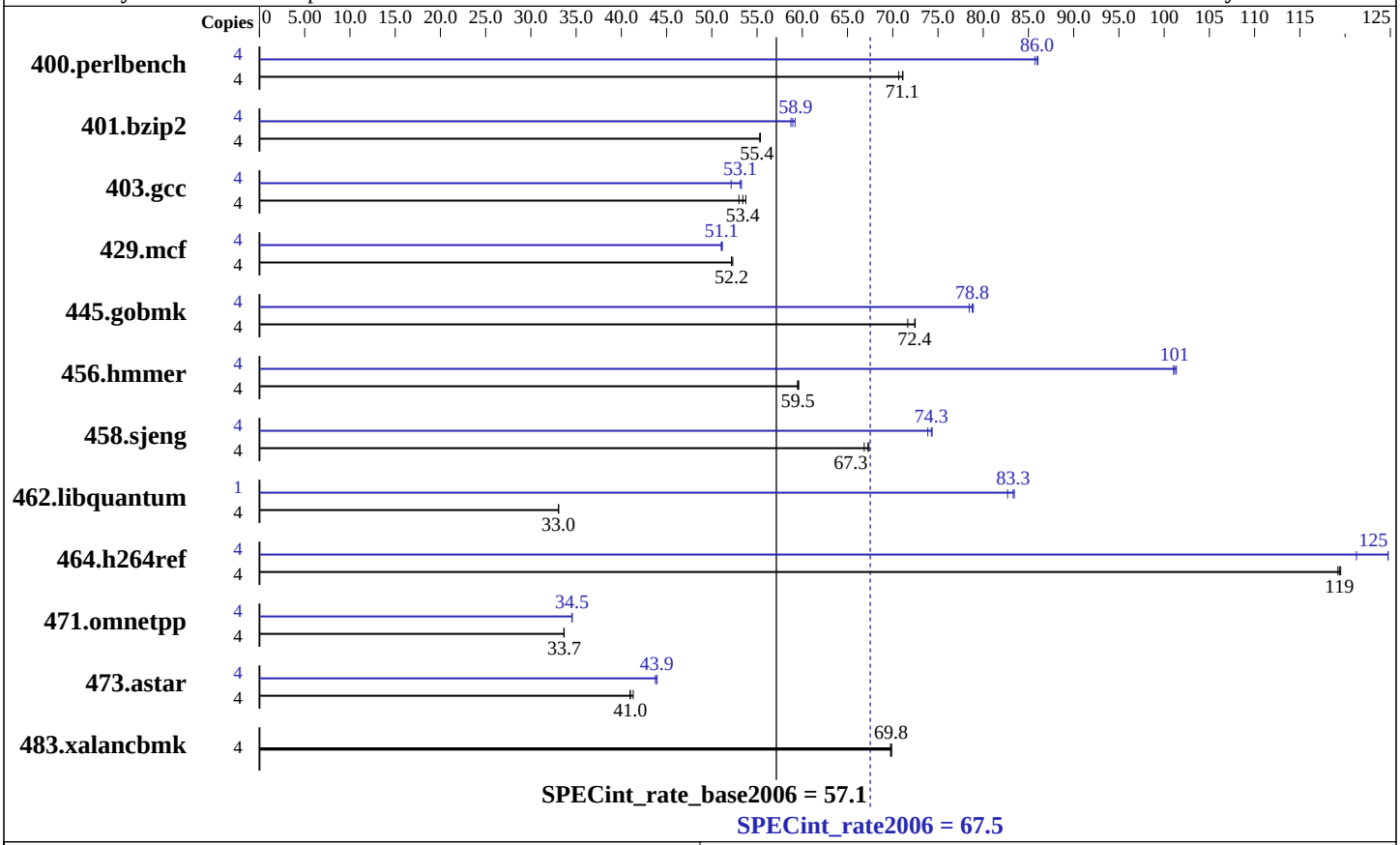
NovaScale R440 E1
(Intel Xeon E5440, 2.83 GHz)

SPECint®_rate2006 = 67.5

SPECint_rate_base2006 = 57.1

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: NEC Corporation

Test date: Jun-2008
Hardware Availability: Apr-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5440
CPU Characteristics:	2.83 GHz, 2x6 MB L2 shared, 1333 MHz bus
CPU MHz:	2833
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	1x73.2 GB SAS, 15000RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap library 8.1 binutils 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**550**	**71.1**	549	71.1	553	70.7	4	454	86.1	**455**	**86.0**	456	85.7
401.bzip2	4	698	55.3	697	55.4	**697**	**55.4**	4	**655**	**58.9**	657	58.7	652	59.2
403.gcc	4	599	53.8	**603**	**53.4**	608	53.0	4	604	53.3	618	52.1	**606**	**53.1**
429.mcf	4	699	52.2	697	52.3	**699**	**52.2**	4	713	51.2	**714**	**51.1**	715	51.0
445.gobmk	4	**579**	**72.4**	579	72.5	586	71.7	4	**533**	**78.8**	532	78.9	535	78.5
456.hmmer	4	628	59.5	**627**	**59.5**	626	59.6	4	**369**	**101**	368	101	369	101
458.sjeng	4	724	66.8	**720**	**67.3**	718	67.4	4	655	73.9	**652**	**74.3**	651	74.4
462.libquantum	4	2508	33.0	**2508**	**33.0**	2506	33.1	1	251	82.7	248	83.4	**249**	**83.3**
464.h264ref	4	741	120	743	119	**742**	**119**	4	**710**	**125**	710	125	730	121
471.omnetpp	4	742	33.7	**742**	**33.7**	743	33.6	4	**724**	**34.5**	723	34.6	724	34.5
473.astar	4	680	41.3	685	41.0	**685**	**41.0**	4	642	43.8	639	43.9	**639**	**43.9**
483.xalancbmk	4	**395**	**69.8**	396	69.8	395	69.9	4	**395**	**69.8**	396	69.8	395	69.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
```

Platform Notes

```
Bios settings:
Hardware Prefetcher:          Enabled
Adjacent Cache Line Prefetch: Disabled
Intel SpeedStep Technology:   Disabled
```

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2,
456.hmmer, for peak, are compiled in 64-bit mode

 The NEC Express5800/120Rh-1(Intel Xeon E5440),
 the NEC Express5800/120Rj-2(Intel Xeon E5440),
 the Bull NovaScale R440 E1 (Intel Xeon E5440,2.83GHz) and
 the Bull NovaScale R460 E1 (Intel Xeon E5440,2.83GHz) models are electronically equivalent.
 The results have been measured on a NEC Express5800/120Rj-2(Intel Xeon E5440) model.
```

Base Compiler Invocation

C benchmarks:

```
icc
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.xml`

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For other inquiries, please contact webmaster@spec.org.

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