SPEC® CINT2006 Result

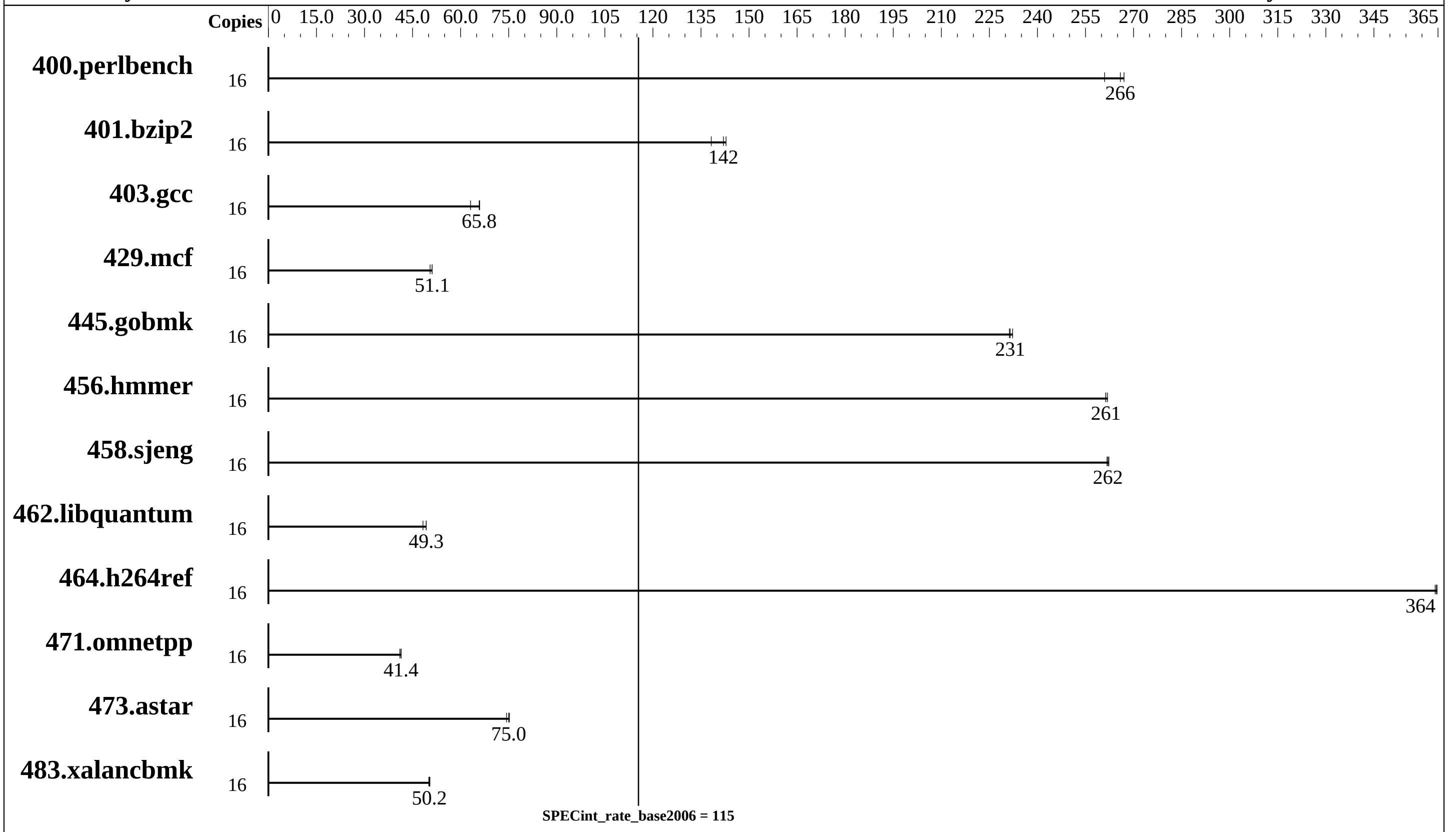
Supermicro
(Test Sponsor: Advanced Micro Devices)

SuperServer 8025C-3RB, Intel Xeon X7350

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 115

CPU2006 license: 49		**Test date:**	Apr-2008
Test sponsor:	Advanced Micro Devices	**Hardware Availability:**	May-2007
Tested by:	Advanced Micro Devices	**Software Availability:**	Mar-2008



Hardware

CPU Name:	Intel Xeon X7350
CPU Characteristics:	
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	32 GB (16 x 2GB DDR2-667 FBDIMM CL5 Dual Rank)
Disk Subsystem:	1 x 120 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	gcc, g++ 4.3.0
Auto Parallel:	No
File System:	ext3
System State:	Runlevel 3 (Full multiuser with network)
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	**588**	**266**	585	267	599	261							
401.bzip2	16	1117	138	1081	143	**1087**	**142**							
403.gcc	16	2041	63.1	**1958**	**65.8**	1953	66.0							
429.mcf	16	2891	50.5	2855	51.1	**2855**	**51.1**							
445.gobmk	16	723	232	**725**	**231**	726	231							
456.hmmer	16	571	261	570	262	**571**	**261**							
458.sjeng	16	**739**	**262**	738	262	740	262							
462.libquantum	16	6872	48.2	**6731**	**49.3**	6730	49.3							
464.h264ref	16	971	365	**972**	**364**	972	364							
471.omnetpp	16	2438	41.0	**2416**	**41.4**	2413	41.4							
473.astar	16	1511	74.3	1493	75.2	**1498**	**75.0**							
483.xalancbmk	16	2205	50.1	**2198**	**50.2**	2190	50.4							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'numactl' was used to bind copies to the cores
```

Base Compiler Invocation

C benchmarks:
`gcc`

C++ benchmarks:
`g++`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`

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Base Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-O3 -funroll-loops -fno-strict-aliasing`

C++ benchmarks:
`-O3 -funroll-loops -fno-strict-aliasing`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.05.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.05.xml

Tested with SPEC CPU2006 v1.0.
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