SPEC® CINT2006 Result

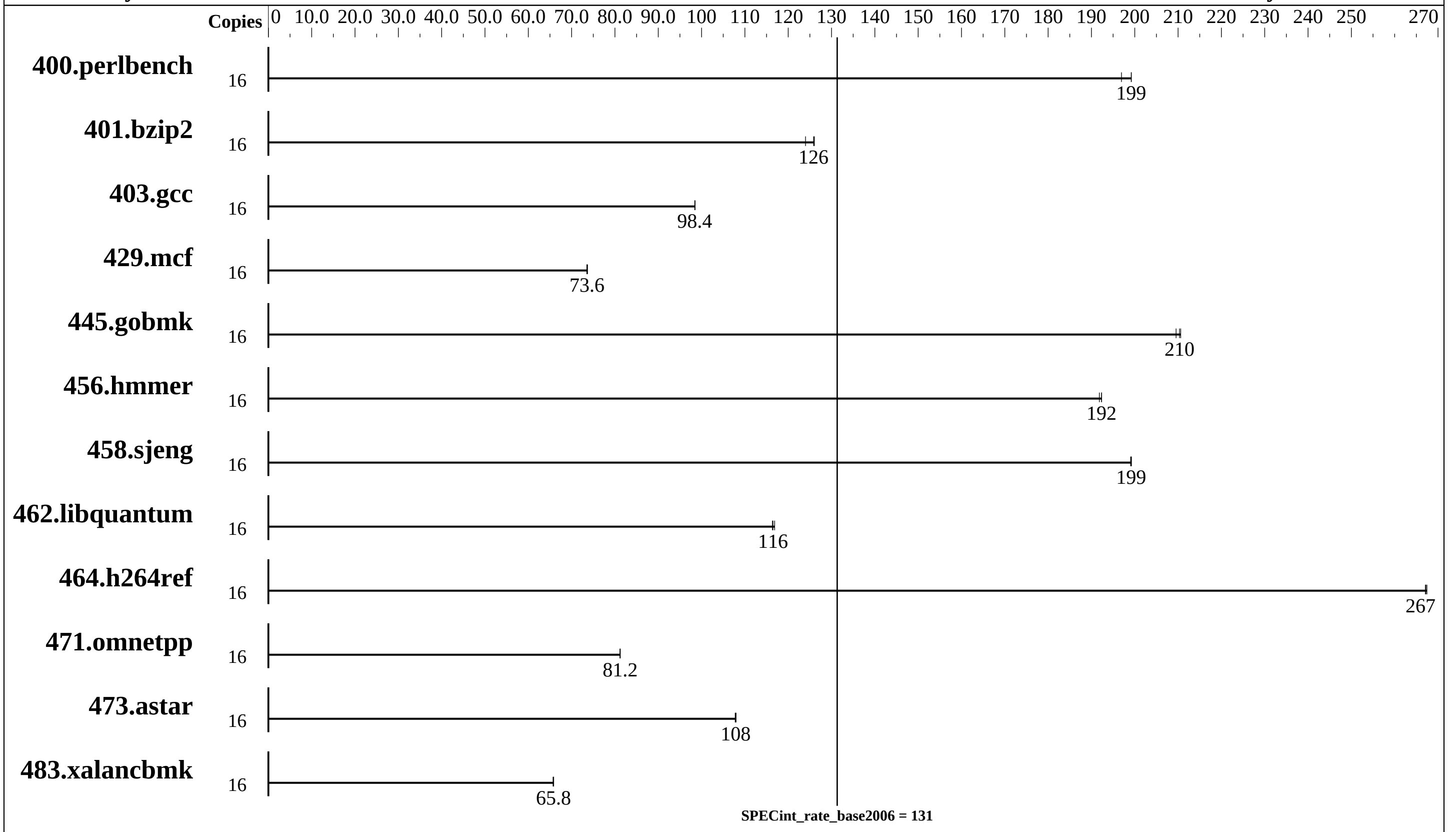
Tyan
(Test Sponsor: Advanced Micro Devices)

Transport TX46, AMD Opteron 8356

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 131

CPU2006 license: 49		**Test date:**	Apr-2008
Test sponsor:	Advanced Micro Devices	**Hardware Availability:**	Apr-2008
Tested by:	Advanced Micro Devices	**Software Availability:**	Mar-2008



Hardware

CPU Name:	AMD Opteron 8356
CPU Characteristics:	
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	2 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16 x 2GB, DDR2-667 CL5 ECC Reg Dual Rank)
Disk Subsystem:	1 x 250 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	gcc, g++ 4.3.0
Auto Parallel:	No
File System:	ext3
System State:	Runlevel 3 (Full multiuser with network)
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other Software:	None

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	**785**	**199**	794	197	785	199							
401.bzip2	16	1245	124	**1227**	**126**	1225	126							
403.gcc	16	1309	98.4	**1308**	**98.4**	1308	98.5							
429.mcf	16	**1984**	**73.6**	1985	73.5	1981	73.7							
445.gobmk	16	**798**	**210**	797	211	801	210							
456.hmmer	16	**776**	**192**	776	192	778	192							
458.sjeng	16	**972**	**199**	973	199	972	199							
462.libquantum	16	2849	116	**2846**	**116**	2837	117							
464.h264ref	16	**1325**	**267**	1324	267	1326	267							
471.omnetpp	16	1231	81.2	**1231**	**81.2**	1232	81.2							
473.astar	16	**1041**	**108**	1042	108	1040	108							
483.xalancbmk	16	1680	65.7	1676	65.9	**1677**	**65.8**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'numactl' was used to bind copies to the cores

Base Compiler Invocation

C benchmarks:
gcc

C++ benchmarks:
g++

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-O3 -funroll-loops -fno-strict-aliasing`

C++ benchmarks:
`-O3 -funroll-loops -fno-strict-aliasing`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.05.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.05.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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