SPEC® CINT2006 Result

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Hewlett-Packard Company

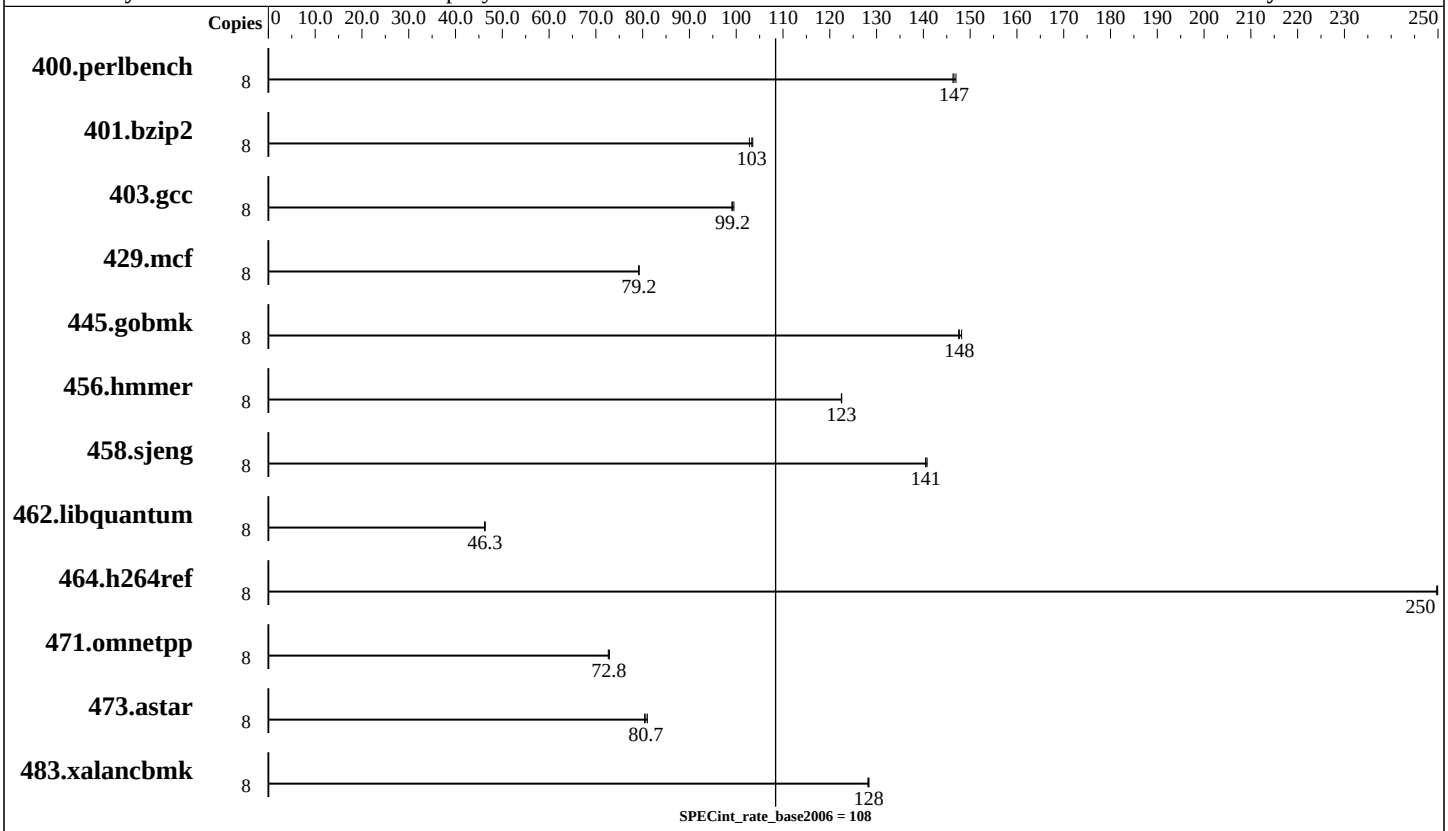
ProLiant BL2x220c G5
(3.00 GHz, Intel Xeon E5450)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 108

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jun-2008
Hardware Availability: May-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5450
CPU Characteristics:	3.0 GHz, 2x6 MB L2 shared, 1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	2,3,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4x4 GB PC2-5300P CL5)
Disk Subsystem:	1x120 GB 5.4 K RPM 2.5" SFF SATA
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	No
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap Library 8.1 binutils-2.17.50

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**533**	**147**	532	147	534	146							
401.bzip2	8	746	104	**747**	**103**	751	103							
403.gcc	8	650	99.1	**649**	**99.2**	647	99.5							
429.mcf	8	922	79.2	**921**	**79.2**	921	79.2							
445.gobmk	8	**568**	**148**	569	148	566	148							
456.hmmer	8	**609**	**123**	609	123	609	122							
458.sjeng	8	687	141	**689**	**141**	689	141							
462.libquantum	8	3585	46.2	**3580**	**46.3**	3579	46.3							
464.h264ref	8	708	250	709	250	**709**	**250**							
471.omnetpp	8	688	72.7	686	72.9	**686**	**72.8**							
473.astar	8	693	81.0	698	80.4	**696**	**80.7**							
483.xalancbmk	8	430	128	**430**	**128**	431	128							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
  Power Regulator set to Static High Performance Mode
  Adjacent Sector Prefetch Disabled
  Hardware Prefetcher Disabled

General Notes

The ProLiant BL2x220c G5 is comprised of two independent server nodes in a single chassis. Only one of the server nodes was used for this benchmark; the other server node was idle during the benchmark. The active server node contained all of the CPUs and memory described in this disclosure.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/cpu2006/SmartHeap_8.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 17:45:42 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 25 June 2008.